ANALYZING GIGAHERTZ BUNCH LENGTH INSTABILITIES WITH A DIGITAL SIGNAL PROCESSOR*

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ABSTRACT

A bunch length instability, nicknamed the "sawtooth", because of its transient behavior, has been observed at high current running in the Stanford Linear Collider (SLC) electron damping ring. The incompatibility of this instability with successful SLC running prompted its study using a high bandwidth real-time spectrum analyzer, the Tektronix 3052 digital signal processor (DSP) system. This device has been used to study energy ramping in storage rings¹ but this is the first time it has been used to study transient instability phenomena. It is a particularly valuable tool for use in understanding non-linear, multiple frequency phenomena. The frequency range of this device has been extended through the use of radio frequency (RF) down converters. This paper describes the measurement setup and presents some of the results.

INTRODUCTION

In the SLC north (electron) and south (positron) damping rings, both the longitudinal and the transverse phase space of the orbiting particles is radiation damped to achieve smaller beam emittances for injection into the linear accelerator (LINAC). During normal running, the beams are injected and extracted at 120 Hz allowing a total time for damping of about 8.3 msec for the electrons and 16.6 msec for the positrons. In 1991, as SLC beam currents were increased to produce more luminosity and increase Z production, "fliers" began to interfere with operation. A "flier" was an errant beam pulse, with a transverse displacement, which resulted in beam trips due to excessive radiation levels. In 1992, as currents were increased further, an instability was observed that involved a blow-up of the bunch length and energy spread in the damping rings. This instability, nicknamed the "sawtooth", was visible on an oscilloscope when the bunch length signal, obtained from a BPM sum signal, repeatedly blew up and then became radiation damped. The sawtooth instability had a measured single bunch threshold of about 3×10¹⁰ particles. For running above this threshold the

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instability was overcome by ramping the RF gap voltage from the nominal 800 kV to about 250 kV. In anticipation of running higher currents in the 1993 running period, it is expected that the instability may prove difficult to suppress and therefore should be understood.

Measurements made with a network analyzer showed that the beam was easily excited at three times the synchrotron tune, v_s . The synchrotron frequency and hence the excitation at $3v_s$ was also observed to depend on beam current. The optimum frequency for observing an m^{th} order synchrotron sideband is approximately

$$f = \frac{m+1}{2\pi\sigma_L} \times c,\tag{1}$$

where:

 σ_L = bunch length ($\approx 8.5 \text{ mm at } 3 \times 10^{10}$) $c = \text{speed of light} = 3.0 \times 10^{10} \text{ cm/sec}$

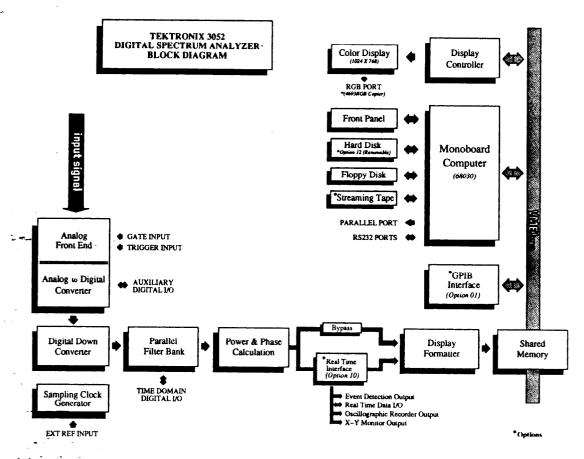
as determined by where the m^{th} order Bessel function which describes the envelope of the beam frequency spectrum peaks. Thus we expect to be maximally sensitive to the $3v_s$ sideband near 25 GHz.

FUNCTIONAL DESCRIPTION OF THE TEKTRONIX 3052

A conventional analog spectrum analyzer mixes the input signal with a swept local oscillator. The swept IF is then applied to a fixed filter and the output of the filter used to drive a CRT display of the frequency spectrum. This technique has an inherent disadvantage when analyzing multiple frequency transient phenomena. Due to this sweeping technique, different frequencies are measured at different times. The signals that we are interested in have low signal to noise ratios requiring slow sweep speeds to resolve thus making it difficult to observe transient events. A DSP spectrum analyzer, on the other hand, is a real time analyzer which uses a parallel digital filter bank to simultaneously analyze the range of frequencies and produce a digital frequency spectrum. Since the DSP processes all frequencies simultaneously, transient phenomena may be studied and correlations between signals observed.

A block diagram of the Tektronix 3052 DSP system is shown in figure 1. The input signal enters through an analog front end module which contains the necessary gain and attenuation controls as well as the analog to digital converter (ADC). The ADC provides 10 bit resolution at each data point and operates at 25.6 MHz. This digitized signal is then passed to the parallel filter bank.

The parallel filter bank has 1024 filters or "bins" which are equally spaced in the frequency domain. The filter outputs are sampled sequentially



Tektronix 3052 OPERATORS MANUAL

Figure 1. Block Diagram of the Tektronix 3052 DSP System.²

at a maximum rate of 5 kHz and the central 801 are used to represent one spectral frame. The frequency resolution of the 3052 is determined by the frequency span divided by the 801 filter bins. Our frequency range of interest typically spanned 1 or 2 MHz corresponding to a bin resolution of 1.25 and 2.50 kHz respectively. For these ranges, the criteria for real time analysis, that the sample time be at least twice the resolution, is satisfied. The default filters are of a linear-phase, finite impulse response design. In addition the user may choose from Bartlett, Blackman, Hamming, Hanning or rectangular filters. User designed filters can also be implemented in the system.

The real and imaginary data is then converted back to amplitude and phase form and formatted for the display. The data is displayed on a 16 inch color monitor with 1024 pixel by 768 line resolution. The available displays include amplitude vs. frequency, phase vs. frequency, a color spectrogram and a waterfall display. The color spectrogram format, which displays frequency vs. time with color coded amplitude, is particularly useful for observing transient phenomena. In the data to be presented the bottom half shows a color spectrogram with the vertical axis indicating time with the most recent event at the bottom. Frequency is plotted on the horizontal axis and the amplitudes are color coded according to the scale as indicated on the

right. The upper plot shows the frequency vs. amplitude corresponding to the spectral frame indicated by the small arrowhead in the left margin of the color spectrogram below. This arrow can be moved through the entire spectrogram making it possible to review the data frame by frame.

Because frame update rates as high as 5 MHz are not always necessary or desirable, the 3052 is provided with several display summary modes which can be adjusted by varying the Display Reduction Rate or R value. Options include a display of every Rth frame, the average of R frames, the maximum of R frames or the minimum and maximum over R frames.

MEASUREMENT SETUP

The measurement setup is shown in figure 2. The signal from the beam was picked up on a single beam position monitor (BPM) stripline electrode located in the electron damping ring. To minimize cable losses, a high frequency, 1/4 inch heliax cable was run directly through the roof of the vault for a total cable length of less than 20 m. The initial processing was done using a HP 70000 series modular spectrum analyzer with a 100 Hz – 26.5 GHz HP 70909A RF Section. In the data to be presented the center frequency of the HP spectrum analyzer was set to a harmonic of the revolution frequency. A center frequency near 20 GHz was chosen to be close to the calculated frequency of maximum sensitivity and less than the roll off frequency of the

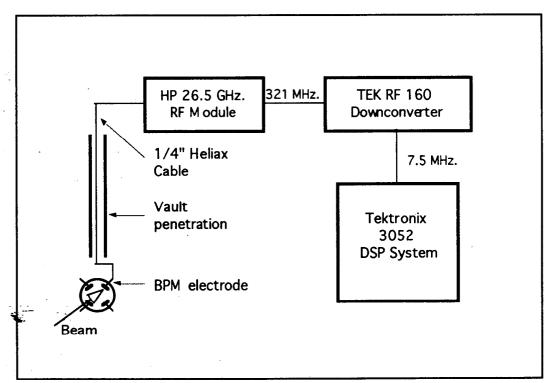


Figure 2. Measurement setup.

heliax cable. The analyzer was set to zero span and the sweep was stopped. In this mode the analyzer was functionally equivalent to a receiver. The spectrum analyzer converted the input signal to a 321.4 MHz IF signal which was then mixed down to 7.5 MHz using a Tektronix RF162 down converter. This 7.5 MHz signal was then transported to the Tektronix 3052 DSP system for processing.

The Tektronix 3052 was set for proper input gain and reference level offset with the aid of the "thermometer" in the lower right hand corner of the display. The thermometer indicated the input level from 0 to 100 percent of the full range of the ADC. The DSP was remotely triggered on successive beam pulses in the ring. The trigger timing could be varied to allow the beam spectrum to be recorded at different times in the machine store cycle. The display reduction rate was adjusted to obtain the desired time scale on the color spectrogram.

RESULTS

Figure 3 shows the measured beam spectrum with the DSP triggered 1/2 msec after injection. The center frequency in the figure is 20.349 GHz down converted to 7.5 MHz and the span is 5 MHz.

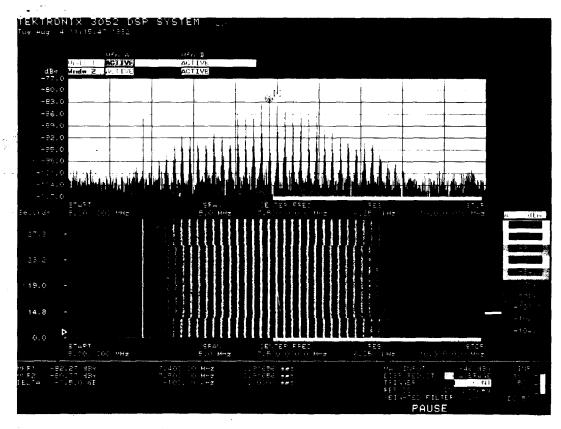


Figure 3. Many higher order sidebands are visible when the beam is recorded at injection time.

From the spectrum one can observe many sidebands separated by about 100 kHz, the synchrotron frequency. The observation of at least 15 sidebands shows that this set up was very sensitive to the frequency range of interest. The rf voltage was varied slightly to confirm that the side bands were derived from the beam and not an instrument artifact. For the signal to be beam related the sidebands are separated by $v_s \propto V_{RF}^{1/2}$

Shown in figure 4 is the measured beam spectrum as a function of current where the beam was stored in the ring for over 200 seconds. The center frequency was 20.349 GHz, the span was 1.0 MHz and the DSP was triggered externally and synchronously with the injection triggers. The display reduction was set to display every 120th trigger.

The frequency vs. amplitude plot shows again the behavior at injection. The color spectrogram shows the behavior after injection. With currents above the instability threshold, the sidebands, characteristic of the instability, were pronounced. As the current decayed, the power in the sidebands decreased. When the current decayed to below the instability threshold, the $3v_s$ sidebands were observed to disappear.

Shown in figure 5 is the measured spectrum as a function of rf cavity gap voltage. The beam current was 3.6×10^{10} particles per bunch and the

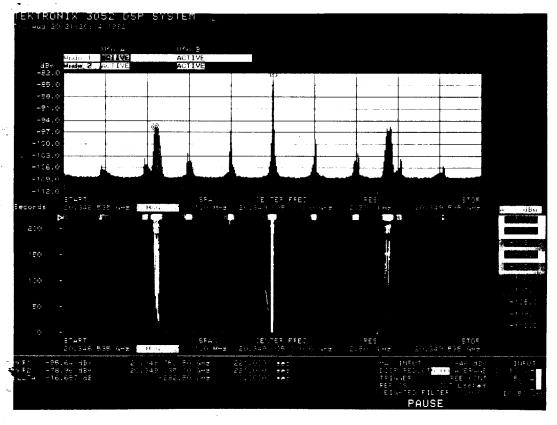


Figure 4. Showing the disappearance of the $3v_s$ sideband as the current drops below the instability threshold.

machine cycle time was 8.3 ms. The rf cavity voltage was varied from 990 kV to 550 kV. The center frequency was 20.349 GHz down converted to 7.5 MHz and the span was 2 MHz. The DSP was triggered externally with the trigger delay adjusted to view the beam close to extraction. The display reduction was set to 25. The time scale of the color spectrogram spans about 40 minutes. The data acquisition was paused each time the rf voltage was adjusted.

Shown in the frequency vs. amplitude spectrum is the beam response with the cavity voltage set to 990 kV. At these high currents synchrotron sidebands were clearly discerned at both $3v_s$ and $6v_s$. This was a direct indication of higher order longitudinal microbunching modes. In the color spectrogram the beam response was measured with the cavity voltage set as indicated on the left. As the voltage was lowered the power in each side band decreased. At 550 kV, the instability was no longer excited.

CONCLUSION

The DSP has proved invaluable in discriminating the high frequency mode structure associated with instabilities in high intensity, bunched beams. In combination with a high frequency receiver we observed bunch structure

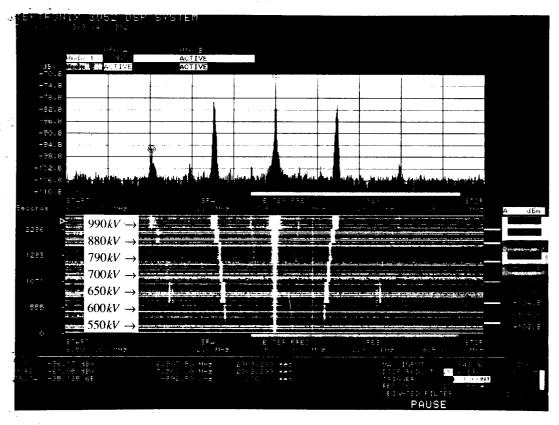


Figure 5. The $3v_s$ and $6v_s$ sideband frequencies for different RF voltages.

in the 20 GHz regime. The fine time resolution of the DSP allowed the high frequency modes to be identified with specific dynamic phenomena during different parts of the beam store cycle.

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REFERENCES

1 Kevin J. Cassidy, Sam Howry, "Development Of A Model For Ramping In A Storage Ring," AIP Conference Proceedings No 252, page 144 (1991).

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