# MPS Link Node Inputs: 2023-12-17-a

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# 1 Link Node 10

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | COLL:LI20:900:SCRAPER_FS | MPS_SCRAPPER_FLOW_OK_LI20 | STATE |
| □ | 1 | 2 | VVPG:LI20:1:POSITION | SLOW_VLV_OPEN_LI20 | STATE |
| □ | 1 | 3 | VVFS:LI20:1:FAST_VLV | FAST_VLV_OPEN_LI20 | STATE |
| □ | 1 | 4 | MMG:LI20:1:MM_GAUGE | MPS_MM_GAUGE_OK_LI20 | STATE |
| □ | 1 | 5 | PPS:LI20:72:SECTOR | PPS_SECTOR_OK_SECURE_LI20 | STATE |
| □ | 1 | 6 | SBST:LI20:1:SBST_OK | MPS_SBST_OK_READY_LI20 | STATE |
| □ | 1 | 7 | VVS:LI20:1:BREAKER | VVS_BRKR_OK_READY_LI20 | STATE |
| □ | 1 | 8 | STPR:LI20:1:BASII_POS | BK_BEAM_STPR_LI20 | STATE |
| □ | 1 | 9 | DUMP:LI20:1:FLOW_SW | DUMP_20_1_FS | STATE |
| □ | 1 | 10 | PLIC:LI20:1:SUM | PIC_SUM_LI20 | STATE |
| □ | 1 | 11 | PICM:LI20:1:HV_PWR | PICM_LI20_1_HV_PWR | STATE |
| □ | 2 | 1 | BEND:DMP1:400:BACT_WDOG | BEND_DMP1_400_BACT_WDOG | STATE |
| □ | 3 | 1 | MPS:IN20:200:LHSHT1_OUT | MPS_IN20_200_LHSHT1_OUT | FAULT |
| □ | 3 | 2 | MPS:IN20:200:LHSHT1_IN | MPS_IN20_200_LHSHT1_IN | FAULT |
| □ | 3 | 3 | MPS:IN20:200:LHSHT2_OUT | MPS_IN20_200_LHSHT2_OUT | FAULT |
| □ | 3 | 4 | MPS:IN20:200:LHSHT2_IN | MPS_IN20_200_LHSHT2_IN | FAULT |
| □ | 3 | 5 | MPS:IN20:200:MSHT1_OUT | MPS_IN20_200_MSHT1_OUT | FAULT |
| □ | 3 | 6 | MPS:IN20:200:MSHT1_IN | MPS_IN20_200_MSHT1_IN | FAULT |
| □ | 3 | 7 | MPS:IN20:200:MSHT2_OUT | MPS_IN20_200_MSHT2_OUT | FAULT |
| □ | 3 | 8 | MPS:IN20:200:MSHT2_IN | MPS_IN20_200_MSHT2_IN | FAULT |
| □ | 3 | 9 | MPS:IN20:200:ANDALLACTV | MPS_IN20_200_ANDALLACTV | STATE |
| □ | 3 | 10 | MPS:IN20:200:ANDCH1ACTV | MPS_IN20_200_ANDCH1ACTV | STATE |
| □ | 3 | 11 | MPS:IN20:200:ANDCH2ACTV | MPS_IN20_200_ANDCH2ACTV | STATE |
| □ | 3 | 12 | MPS:IN20:200:ANDCH3ACTV | MPS_IN20_200_ANDCH3ACTV | STATE |
| □ | 3 | 13 | MPS:IN20:200:ANDCH4ACTV | MPS_IN20_200_ANDCH4ACTV | STATE |
| □ | 3 | 14 | MPS:IN20:200:ANDCH5ACTV | MPS_IN20_200_ANDCH5ACTV | STATE |
| □ | 3 | 15 | MPS:IN20:200:ANDCH6ACTV | MPS_IN20_200_ANDCH6ACTV | STATE |
| □ | 3 | 16 | MPS:IN20:200:ANDCH7ACTV | MPS_IN20_200_ANDCH7ACTV | STATE |
| □ | 4 | 1 | MPS:IN20:200:ANDCH8ACTV | MPS_IN20_200_ANDCH8ACTV | STATE |
| □ | 4 | 2 | USEG:UND1:1:ALL_OUT | USEG_UND1_1_ALL_OUT | STATE |
| □ | 4 | 3 | XTAL:FEE1:441:PULSEENERGY | XTAL_FEE1_441_PULSEENERGY | THRESHOLD |
| □ | 5 | 1 | MPS:IN20:100:ANDALLACTV | MPS_IN20_100_ANDALLACTV | STATE |
| □ | 5 | 2 | MPS:IN20:100:ANDCH1ACTV | MPS_IN20_100_ANDCH1ACTV | STATE |
| □ | 5 | 3 | MPS:IN20:100:ANDCH2ACTV | MPS_IN20_100_ANDCH2ACTV | STATE |
| □ | 5 | 4 | MPS:IN20:100:ANDCH3ACTV | MPS_IN20_100_ANDCH3ACTV | STATE |
| □ | 5 | 5 | MPS:IN20:100:ANDCH4ACTV | MPS_IN20_100_ANDCH4ACTV | STATE |
| □ | 5 | 6 | MPS:IN20:100:ANDCH5ACTV | MPS_IN20_100_ANDCH5ACTV | STATE |
| □ | 5 | 7 | MPS:IN20:100:ANDCH6ACTV | MPS_IN20_100_ANDCH6ACTV | STATE |

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| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 5 | 8 | MPS:IN20:100:ANDCH7ACTV | MPS_IN20_100_ANDCH7ACTV | STATE |
| □ | 5 | 9 | MPS:IN20:100:ANDCH8ACTV | MPS_IN20_100_ANDCH8ACTV | STATE |

# 2 Link Node 11

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | VVPG:IN20:155:POSITION | VV_BL155 | STATE |
| □ | 1 | 2 | VVPG:IN20:435:POSITION | VV_BL435 | STATE |
| □ | 1 | 3 | VVPG:IN20:545:POSITION | VV_BL545 | STATE |
| □ | 1 | 4 | VVPG:IN20:635:POSITION | VV_BL635 | STATE |
| □ | 1 | 5 | VVPG:IN20:915:POSITION | VV_IS915 | STATE |
| □ | 1 | 6 | VVMG:IN20:175:POSITION | VV_BL175 | STATE |
| □ | 1 | 7 | VVMG:LI21:196:POSITION | VV_BL195 | STATE |
| □ | 1 | 8 | MKSU:LI20:61:WGVACSUM | WGVACGUN | STATE |
| □ | 1 | 9 | YAGS:IN20:211:OUT_LMTSW | YAGS_IN20_211_OUT_LMTSW | STATE |
| □ | 1 | 10 | YAGS:IN20:211:IN_LMTSW | YAGS_IN20_211_IN_LMTSW | STATE |
| □ | 1 | 11 | YAGS:IN20:921:OUT_LMTSW | YAGS_IN20_921_OUT_LMTSW | STATE |
| □ | 1 | 12 | YAGS:IN20:921:IN_LMTSW | YAGS_IN20_921_IN_LMTSW | STATE |
| □ | 1 | 13 | YAGS:IN20:351:OUT_LMTSW | YAGS_IN20_351_OUT_LMTSW | STATE |
| □ | 1 | 14 | YAGS:IN20:351:IN_LMTSW | YAGS_IN20_351_IN_LMTSW | STATE |
| □ | 1 | 15 | YAGS:IN20:471:OUT_LMTSW | YAGS_IN20_471_OUT_LMTSW | STATE |
| □ | 1 | 16 | YAGS:IN20:471:IN_LMTSW | YAGS_IN20_471_IN_LMTSW | STATE |
| □ | 2 | 1 | YAGS:IN20:841:OUT_LMTSW | YAGS_IN20_841_OUT_LMTSW | STATE |
| □ | 2 | 2 | YAGS:IN20:841:IN_LMTSW | YAGS_IN20_841_IN_LMTSW | STATE |
| □ | 2 | 3 | YAGS:IN20:241:OUT_LMTSW | YAGS_IN20_241_OUT_LMTSW | STATE |
| □ | 2 | 4 | YAGS:IN20:241:IN_LMTSW | YAGS_IN20_241_IN_LMTSW | STATE |
| □ | 2 | 5 | YAGS:LI21:995:OUT_LMTSW | YAGS_LI21_995_OUT_LMTSW | STATE |
| □ | 2 | 6 | YAGS:LI21:995:IN_LMTSW | YAGS_LI21_995_IN_LMTSW | STATE |
| □ | 2 | 7 | OTRS:IN20:541:OUT_LMTSW | OTRS_IN20_541_OUT_LMTSW | STATE |
| □ | 2 | 8 | OTRS:IN20:541:IN_LMTSW | OTRS_IN20_541_IN_LMTSW | STATE |
| □ | 2 | 9 | OTRS:IN20:465:OUT_LMTSW | OTRH1_OUT_LIMIT | STATE |
| □ | 2 | 10 | OTRS:IN20:465:IN_LMTSW | OTRH1_IN_LIMIT | STATE |
| □ | 2 | 11 | OTRS:IN20:621:OUT_LMTSW | OTRS_IN20_621_OUT_LMTSW | STATE |
| □ | 2 | 12 | OTRS:IN20:621:IN_LMTSW | OTRS_IN20_621_IN_LMTSW | STATE |
| □ | 2 | 13 | OTRS:IN20:471:OUT_LMTSW | OTRH2_OUT_LIMIT | STATE |
| □ | 2 | 14 | OTRS:IN20:471:IN_LMTSW | OTRH2_IN_LIMIT | STATE |
| □ | 2 | 15 | OTRS:IN20:991:OUT_LMTSW | OTRS_IN20_991_OUT_LMTSW | STATE |
| □ | 2 | 16 | OTRS:IN20:991:IN_LMTSW | OTRS_IN20_991_IN_LMTSW | STATE |
| □ | 3 | 1 | OTRS:LI21:237:OUT_LMTSW | OTRS_LI21_237_OUT_LMTSW | STATE |
| □ | 3 | 2 | OTRS:LI21:237:IN_LMTSW | OTRS_LI21_237_IN_LMTSW | STATE |
| □ | 3 | 3 | OTRS:LI21:291:OUT_LMTSW | OTRS_LI21_291_OUT_LMTSW | STATE |
| □ | 3 | 4 | OTRS:LI21:291:IN_LMTSW | OTRS_LI21_291_IN_LMTSW | STATE |
| □ | 3 | 5 | TORO:IN20:215:CHASIM11 | CHASIM11 | STATE |
| □ | 3 | 6 | TORO:IN20:431:CHASIM12 | CHASIM12 | STATE |

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| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 3 | 7 | MIRR:IN20:191:AM1_OUT | MR_BL191 | STATE |
| □ | 3 | 8 | WKFL:IN20:231:OUT | WL_BL231_OUT | STATE |
| □ | 3 | 9 | WKFL:IN20:231:IN | WL_BL231_IN | STATE |
| □ | 3 | 10 | TIU:LI20:1:RECEIVER | TIU_2921 | STATE |
| □ | 3 | 11 | COLL:LI21:235:KLIXON | CE11_KLIXON | STATE |
| □ | 3 | 12 | DUMP:LI21:305:FLOW_SW | FS_BL305 | STATE |
| □ | 3 | 13 | FLTR:IN20:130:FLT1_OUT | LHATTEN_OUT_LIMIT | STATE |
| □ | 3 | 14 | FLTR:IN20:130:FLT1_IN | LHATTEN_IN_LIMIT | STATE |
| □ | 3 | 15 | FLTR:IN20:450:FLT1_OUT | LHPHD_OUT_LIMIT | STATE |
| □ | 3 | 16 | FLTR:IN20:450:FLT1_IN | LHPHD_IN_LIMIT | STATE |
| □ | 4 | 1 | STPR:IN20:BL551:POSITION | ST_BL551 | STATE |
| □ | 4 | 2 | TORO:IN20:BL215:MPS_STATE | TR_BL215 | STATE |
| □ | 4 | 3 | TORO:IN20:203:TC203L1 | TC203L1 | STATE |
| □ | 4 | 4 | TORO:IN20:203:TC203L2 | TC203L2 | STATE |
| □ | 4 | 5 | TORO:IN20:971:TC2S1L1 | TC2S1L1 | STATE |
| □ | 4 | 6 | TORO:IN20:971:TC2S1L2 | TC2S1L2 | STATE |
| □ | 4 | 7 | TORO:LI21:205:TCBC1L1 | TCBC1L1 | STATE |
| □ | 4 | 8 | TORO:LI21:205:TCBC1L2 | TCBC1L2 | STATE |
| □ | 4 | 9 | DUMP:LI21:305:TD11_OUT | TD11_OUT_LIMIT | STATE |
| □ | 4 | 10 | DUMP:LI21:305:TD11_IN | TD11_IN_LIMIT | STATE |
| □ | 4 | 11 | STPR:LI20:1:BASII_OUT | BASII_ST_OUT_LIMIT | STATE |
| □ | 4 | 12 | STPR:LI20:1:BASII_IN | BASII_ST_IN_LIMIT | STATE |
| □ | 4 | 14 | XTAL:UNDH:2850:POSITIONSTAT | XTAL_UNDH_2850_POSITIONSTAT | STATE |
| □ | 4 | 15 | BEND:DMPH:400:INTOL_BRANGE | BEND_DMPH_400_INTOL_BRANGE | STATE |
| □ | 4 | 16 | OTRS:IN20:711:IN_LMTSW | OTRS_IN20_711_IN_LMTSW | STATE |
| □ | 5 | 1 | SEPT:LI19:172:HLAM_ENB | HLAM_ENB | STATE |
| □ | 5 | 2 | KICK:LI20:7:EKIC_ENB | EKIC_ENB | STATE |
| □ | 5 | 3 | JMTR:IN20:1:JOULEMTR | JOULEMTR | STATE |
| □ | 5 | 4 | JMTR:IN20:2:JOULEMTR2 | JOULEMTR2 | STATE |
| □ | 5 | 5 | COLL:LI21:235:OUT_LMTSW | CE11_X1_X2_MOTOR | STATE |
| □ | 5 | 6 | MOVR:LI21:235:BC1_POS_OK | BC1_CHICANE | STATE |
| □ | 5 | 7 | BEND:UNDH:2810:INTOL_BMIN | BEND_UNDH_2810_INTOL_BMIN | STATE |
| □ | 5 | 8 | MBRC:LI20:8:BEAMPERM | BEAM_PERMIT | STATE |
| □ | 5 | 9 | OTRS:IN20:571:OUT_LMTSW | OTRS_IN20_571_OUT_LMTSW | STATE |
| □ | 5 | 10 | OTRS:IN20:571:IN_LMTSW | OTRS_IN20_571_IN_LMTSW | STATE |
| □ | 5 | 11 | OTRS:IN20:711:OUT_LMTSW | OTRS_IN20_711_OUT_LMTSW | STATE |
| □ | 5 | 12 | BLEN:LI21:265:BL11_OUT | MIR_BL11_OUT | STATE |
| □ | 5 | 13 | BLEN:LI21:265:BL11_IN | MIR_BL11_IN | STATE |
| □ | 5 | 14 | MBRC:IN20:1:SS_MODE | SS_MODE | STATE |
| □ | 5 | 15 | MBRC:IN20:1:SS_REQST | SS_REQST | STATE |
| □ | 5 | 16 | COLL:LI21:235:FLOW_SW | COLL_LI21_235_OPEN | STATE |

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| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 6 | 1 | SHUT:IN20:1:FAULT | SHFLT | STATE |
| □ | 6 | 2 | SHUT:IN20:1:BYPASS | SHFLTBYP | STATE |
| □ | 6 | 3 | BEND:IN20:751:STATE_ON | BEND_IN20_751_OK | STATE |
| □ | 6 | 4 | BEND:IN20:751:STATE_OFF | BEND_IN20_751_OFF | STATE |
| □ | 6 | 5 | BEND:IN20:231:STATE_ON | BEND_IN20_231_OK | STATE |
| □ | 6 | 6 | BEND:IN20:231:STATE_OFF | BEND_IN20_231_OFF | STATE |
| □ | 6 | 7 | BEND:IN20:931:STATE_ON | BEND_IN20_931_OK | STATE |
| □ | 6 | 8 | BEND:IN20:931:STATE_OFF | BEND_IN20_931_OFF | STATE |
| □ | 6 | 9 | XTAL:UNDH:2850:IN_ENCDR | XTAL_UNDH_2850_IN_ENCDR | STATE |
| □ | 6 | 10 | MIRR:FEE1:1560:ENERGY | MIRR_FEE1_1560_ENERGY | STATE |
| □ | 6 | 11 | IOC:IN20:MP01:HEARTBEATSUM | MN_HEART | STATE |
| □ | 6 | 12 | XTAL:FEE1:441:ENERGY | XTAL_FEE1_441_ENERGY | STATE |
| □ | 6 | 15 | VVFV:IN20:785:OPEN_LMTSW | VVFV_IN20_785_OPEN_LMTSW | POSITION |
| □ | 6 | 16 | KLYS:LI20:61:PEAKPOWER | FLT_20_6_TRP | STATE |
| □ | 7 | 8 | PICM:IN20:665:NO_TRIG | PICM_IN20_665_NO_TRIG | MISSED |
| □ | 9 | 1 | PICM:IN20:665:LOSSL | PICM_IN20_665_LOSSL | THRESHOLD |
| □ | 9 | 2 | PICM:IN20:665:LOSSH | PICM_IN20_665_LOSSH | THRESHOLD |
| □ | 10 | 1 | PICM:IN20:775:LOSSL | PICM_IN20_775_LOSSL | THRESHOLD |
| □ | 10 | 2 | PICM:IN20:775:LOSSH | PICM_IN20_775_LOSSH | THRESHOLD |
| □ | 11 | 1 | PICM:IN20:933:LOSSL | PICM_IN20_933_LOSSL | THRESHOLD |
| □ | 11 | 2 | PICM:IN20:933:LOSSH | PICM_IN20_933_LOSSH | THRESHOLD |
| □ | 12 | 1 | PICM:IN20:185:LOSSL | PICM_IN20_185_LOSSL | THRESHOLD |
| □ | 12 | 2 | PICM:IN20:185:LOSSH | PICM_IN20_185_LOSSH | THRESHOLD |
| □ | 13 | 1 | PICM:IN20:232:LOSSL | PICM_IN20_232_LOSSL | THRESHOLD |
| □ | 13 | 2 | PICM:IN20:232:LOSSH | PICM_IN20_232_LOSSH | THRESHOLD |
| □ | 14 | 1 | PICM:IN20:245:LOSSL | PICM_IN20_245_LOSSL | THRESHOLD |
| □ | 14 | 2 | PICM:IN20:245:LOSSH | PICM_IN20_245_LOSSH | THRESHOLD |

# 3 Link Node 12

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 2 | VVPG:LI21:1:POSITION | SLOW_VLV_OPEN_LI21 | STATE |
| □ | 1 | 4 | MMG:LI21:1:MM_GAUGE | MPS_MM_GAUGE_OK_LI21 | STATE |
| □ | 1 | 5 | PPS:LI21:72:SECTOR | PPS_SECTOR_OK_SECURE_LI21 | STATE |
| □ | 1 | 6 | SBST:LI21:1:SBST_OK | MPS_SBST_OK_READY_LI21 | STATE |

# 4 Link Node 13

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | COLL:LI22:900:SCRAPER_FS | MPS_SCRAPPER_FLOW_OK_LI22 | STATE |
| □ | 1 | 2 | VVPG:LI22:1:POSITION | SLOW_VLV_OPEN_LI22 | STATE |
| □ | 1 | 3 | VVFS:LI22:1:FAST_VLV | FAST_VLV_OPEN_LI22 | STATE |
| □ | 1 | 4 | MMG:LI22:1:MM_GAUGE | MPS_MM_GAUGE_OK_LI22 | STATE |
| □ | 1 | 5 | PPS:LI22:72:SECTOR | PPS_SECTOR_OK_SECURE_LI22 | STATE |
| □ | 1 | 6 | SBST:LI22:1:SBST_OK | MPS_SBST_OK_READY_LI22 | STATE |
| □ | 1 | 7 | VVS:LI22:1:BREAKER | VVS_BRKR_OK_READY_LI22 | STATE |

# 5 Link Node 14

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | COLL:LI23:900:SCRAPER_FS | MPS_SCRAPPER_FLOW_OK_LI23 | STATE |
| □ | 1 | 2 | VVPG:LI23:1:POSITION | SLOW_VLV_OPEN_LI23 | STATE |
| □ | 1 | 3 | VVFS:LI23:1:FAST_VLV | FAST_VLV_OPEN_LI23 | STATE |
| □ | 1 | 4 | MMG:LI23:1:MM_GAUGE | MPS_MM_GAUGE_OK_LI23 | STATE |
| □ | 1 | 5 | PPS:LI23:72:SECTOR | PPS_SECTOR_OK_SECURE_LI23 | STATE |
| □ | 1 | 6 | SBST:LI23:1:SBST_OK | MPS_SBST_OK_READY_LI23 | STATE |

# 6 Link Node 15

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | COLL:LI24:900:SCRAPER_FS | MPS_SCRAPPER_FLOW_OK_LI24 | STATE |
| □ | 1 | 2 | VVPG:LI24:1:POSITION | SLOW_VLV_OPEN_LI24 | STATE |
| □ | 1 | 3 | VVFS:LI24:1:FAST_VLV | FAST_VLV_OPEN_LI24 | STATE |
| □ | 1 | 4 | MMG:LI24:1:MM_GAUGE | MPS_MM_GAUGE_OK_LI24 | STATE |
| □ | 1 | 5 | PPS:LI24:72:SECTOR | PPS_SECTOR_OK_SECURE_LI24 | STATE |
| □ | 1 | 6 | SBST:LI24:1:SBST_OK | MPS_SBST_OK_READY_LI24 | STATE |
| □ | 1 | 7 | VVS:LI24:1:BREAKER | VVS_BRKR_OK_READY_LI24 | STATE |
| □ | 1 | 11 | PICM:LI24:1:HV_PWR | PICM_LI24_1_HV_PWR | STATE |
| □ | 1 | 14 | TORO:LI24:707:TCBC2L1 | TCBC2L1 | STATE |
| □ | 1 | 15 | TORO:LI24:707:TCBC2L2 | TCBC2L2 | STATE |
| □ | 1 | 16 | TORO:LI24:1:CHASIMB2 | CHASIMB2 | STATE |

# 7 Link Node 16

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | OTRS:LI24:807:OUT_LMTSW | OTRS_LI24_807_OUT_LMTSW | STATE |
| □ | 1 | 2 | OTRS:LI24:807:IN_LMTSW | OTRS_LI24_807_IN_LMTSW | STATE |
| □ | 1 | 5 | OTRS:LI25:920:OUT_LMTSW | OTR_TCAV_OUT | STATE |
| □ | 1 | 6 | OTRS:LI25:920:IN_LMTSW | OTR_TCAV_IN | STATE |
| □ | 1 | 7 | CHIC:LI24:790:BC2_LGPS | BC2_LGPS | STATE |
| □ | 1 | 8 | PICM:LI24:1:GAS_PRESS | PIC_GAS_PRESS | STATE |
| □ | 1 | 9 | BLEN:LI25:886:BL22_OUT | BL22_OUT | STATE |
| □ | 1 | 10 | BLEN:LI25:886:BL22_IN | BL22_IN | STATE |
| □ | 1 | 11 | COLL:LI24:805:POSITION | COL_805 | STATE |
| □ | 1 | 12 | COLL:LI24:805:FLOW_SW | COL_805_FLOSW | STATE |
| □ | 1 | 13 | VVPG:LI24:710:POSITION | VV21 | STATE |
| □ | 1 | 14 | VVPG:LI24:896:POSITION | VV22 | STATE |
| □ | 1 | 15 | CHIC:LI24:805:BC2_POS_OK | BC2_CHICANE | STATE |
| □ | 1 | 16 | CHIC:LI24:790:BC2_BEND | BC2_BEND | STATE |
| □ | 7 | 8 | PICM:LI24:791:NO_TRIG | PICM_LI24_791_NO_TRIG | MISSED |
| □ | 9 | 1 | PICM:LI24:791:LOSSL | PICM_LI24_791_LOSSL | THRESHOLD |
| □ | 9 | 2 | PICM:LI24:791:LOSSH | PICM_LI24_791_LOSSH | THRESHOLD |
| □ | 10 | 1 | PICM:LI24:811:LOSSL | PICM_LI24_811_LOSSL | THRESHOLD |
| □ | 10 | 2 | PICM:LI24:811:LOSSH | PICM_LI24_811_LOSSH | THRESHOLD |

# 8 Link Node 17

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | COLL:LI25:900:SCRAPER_FS | MPS_SCRAPPER_FLOW_OK_LI25 | STATE |
| □ | 1 | 3 | VVFS:LI25:1:FAST_VLV | FAST_VLV_OPEN_LI25 | STATE |
| □ | 1 | 4 | MMG:LI25:1:MM_GAUGE | MPS_MM_GAUGE_OK_LI25 | STATE |
| □ | 1 | 5 | PPS:LI25:72:SECTOR | PPS_SECTOR_OK_SECURE_LI25 | STATE |
| □ | 1 | 6 | SBST:LI25:1:SBST_OK | MPS_SBST_OK_READY_LI25 | STATE |

# 9 Link Node 18

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | COLL:LI26:900:SCRAPER_FS | MPS_SCRAPPER_FLOW_OK_LI26 | STATE |
| □ | 1 | 2 | VVPG:LI26:1:POSITION | SLOW_VLV_OPEN_LI26 | STATE |
| □ | 1 | 3 | VVFS:LI26:1:FAST_VLV | FAST_VLV_OPEN_LI26 | STATE |
| □ | 1 | 4 | MMG:LI26:1:MM_GAUGE | MPS_MM_GAUGE_OK_LI26 | STATE |
| □ | 1 | 5 | PPS:LI26:72:SECTOR | PPS_SECTOR_OK_SECURE_LI26 | STATE |
| □ | 1 | 6 | SBST:LI26:1:SBST_OK | MPS_SBST_OK_READY_LI26 | STATE |
| □ | 1 | 7 | VVS:LI26:1:BREAKER | VVS_BRKR_OK_READY_LI26 | STATE |

# 10 Link Node 19

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | COLL:LI27:900:SCRAPER_FS | MPS_SCRAPPER_FLOW_OK_LI27 | STATE |
| □ | 1 | 2 | VVPG:LI27:1:POSITION | SLOW_VLV_OPEN_LI27 | STATE |
| □ | 1 | 3 | VVFS:LI27:1:FAST_VLV | FAST_VLV_OPEN_LI27 | STATE |
| □ | 1 | 4 | MMG:LI27:1:MM_GAUGE | MPS_MM_GAUGE_OK_LI27 | STATE |
| □ | 1 | 5 | PPS:LI27:72:SECTOR | PPS_SECTOR_OK_SECURE_LI27 | STATE |
| □ | 1 | 6 | SBST:LI27:1:SBST_OK | MPS_SBST_OK_READY_LI27 | STATE |

# 11 Link Node 20

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | COLL:LI28:900:SCRAPER_FS | MPS_SCRAPPER_FLOW_OK_LI28 | STATE |
| □ | 1 | 2 | VVPG:LI28:1:POSITION | SLOW_VLV_OPEN_LI28 | STATE |
| □ | 1 | 3 | VVFS:LI28:1:FAST_VLV | FAST_VLV_OPEN_LI28 | STATE |
| □ | 1 | 4 | MMG:LI28:1:MM_GAUGE | MPS_MM_GAUGE_OK_LI28 | STATE |
| □ | 1 | 5 | PPS:LI28:72:SECTOR | PPS_SECTOR_OK_SECURE_LI28 | STATE |
| □ | 1 | 6 | SBST:LI28:1:SBST_OK | MPS_SBST_OK_READY_LI28 | STATE |
| □ | 1 | 7 | VVS:LI28:1:BREAKER | VVS_BRKR_OK_READY_LI28 | STATE |
| □ | 1 | 9 | DUMP:LI28:5879:KLIXON | DUMP_5879_KLIXON_OK | STATE |
| □ | 1 | 10 | DUMP:LI28:5879:FLOW_SW | DUMP_5879_FLOW_SW | STATE |
| □ | 1 | 11 | STPR:LI28:950:BASII_IN | BASII_STOPPER_IN | STATE |
| □ | 1 | 12 | STPR:LI28:950:BASII_OUT | BASII_STOPPER_OUT | STATE |

# 12 Link Node 21

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | COLL:LI29:900:SCRAPER_FS | MPS_SCRAPPER_FLOW_OK_LI29 | STATE |
| □ | 1 | 2 | VVPG:LI29:1:POSITION | SLOW_VLV_OPEN_LI29 | STATE |
| □ | 1 | 3 | VVFS:LI29:1:FAST_VLV | FAST_VLV_OPEN_LI29 | STATE |
| □ | 1 | 4 | MMG:LI29:1:MM_GAUGE | MPS_MM_GAUGE_OK | STATE |
| □ | 1 | 5 | PPS:LI29:72:SECTOR | PPS_SECTOR_OK_SECURE_LI29 | STATE |
| □ | 1 | 6 | SBST:LI29:1:SBST_OK | MPS_SBST_OK_READY_LI29 | STATE |
| □ | 1 | 7 | COLL:LI29:96:28_9_FS | FLOW_SW_28_9 | STATE |
| □ | 1 | 8 | COLL:LI29:146:29_1_FS | FLOW_SW_29_1 | STATE |
| □ | 1 | 9 | COLL:LI29:446:29_4_FS | FLOW_SW_29_4 | STATE |
| □ | 1 | 10 | COLL:LI29:546:29_5_FS | FLOW_SW_29_5 | STATE |
| □ | 1 | 11 | PLIC:LI29:1:SYS_OK | PLIC_SYSTEM_OK | STATE |

# 13 Link Node 22

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | COLL:LI30:900:SCRAPER_FS | MPS_SCRAPPER_FLOW_OK_LI30 | STATE |
| □ | 1 | 2 | VVPG:LI30:1:POSITION | SLOW_VLV_OPEN_LI30 | STATE |
| □ | 1 | 3 | VVFS:LI30:1:FAST_VLV | FAST_VLV_OPEN_LI30 | STATE |
| □ | 1 | 4 | MMG:LI30:1:MM_GAUGE | MPS_MM_GAUGE_30_5_OK | STATE |
| □ | 1 | 5 | PPS:LI30:72:SECTOR | PPS_SECTOR_OK_SECURE_LI30 | STATE |
| □ | 1 | 6 | SBST:LI30:1:SBST_OK | MPS_SBST_OK_READY_LI30 | STATE |
| □ | 1 | 7 | VVS:LI30:1:BREAKER | VVS_BRKR_OK_READY_LI30 | STATE |
| □ | 1 | 8 | VVPG:LI30:43:SLOW_VLV30_9 | SLOW_VLV_30_9_OK_OPEN | STATE |
| □ | 1 | 9 | MMG:LI30:1:MMG30_9 | MPS_MM_GAUGE_30_9_OK | STATE |
| □ | 1 | 13 | COLL:LI30:96:29_9_FS | FLOW_SW_29_9 | STATE |
| □ | 1 | 14 | COLL:LI30:146:30_1_FS | FLOW_SW_30_1 | STATE |
| □ | 1 | 15 | COLL:LI30:446:30_4_FS | FLOW_SW_30_4 | STATE |
| □ | 1 | 16 | COLL:LI30:546:30_5_FS | FLOW_SW_30_5 | STATE |
| □ | 2 | 1 | PICM:LI30:1:HV_PWR | PICM_LI30_1_HV_PWR | STATE |
| □ | 7 | 8 | PICM:LI28:916:NO_TRIG | PICM_LI28_916_NO_TRIG | MISSED |
| □ | 9 | 1 | PICM:LI28:916:LOSSL | PICM_LI28_916_LOSSL | THRESHOLD |
| □ | 9 | 2 | PICM:LI28:916:LOSSH | PICM_LI28_916_LOSSH | THRESHOLD |
| □ | 10 | 1 | PICM:LI29:146:LOSSL | PICM_LI29_146_LOSSL | THRESHOLD |
| □ | 10 | 2 | PICM:LI29:146:LOSSH | PICM_LI29_146_LOSSH | THRESHOLD |
| □ | 11 | 1 | PICM:LI29:446:LOSSL | PICM_LI29_446_LOSSL | THRESHOLD |
| □ | 11 | 2 | PICM:LI29:446:LOSSH | PICM_LI29_446_LOSSH | THRESHOLD |
| □ | 12 | 1 | PICM:LI29:546:LOSSL | PICM_LI29_546_LOSSL | THRESHOLD |
| □ | 12 | 2 | PICM:LI29:546:LOSSH | PICM_LI29_546_LOSSH | THRESHOLD |
| □ | 13 | 1 | PICM:LI29:801:LOSSL | PICM_LI29_801_LOSSL | THRESHOLD |
| □ | 13 | 2 | PICM:LI29:801:LOSSH | PICM_LI29_801_LOSSH | THRESHOLD |
| □ | 17 | 1 | PICM:LI29:956:LOSSL | PICM_LI29_956_LOSSL | THRESHOLD |
| □ | 17 | 2 | PICM:LI29:956:LOSSH | PICM_LI29_956_LOSSH | THRESHOLD |
| □ | 18 | 1 | PICM:LI30:146:LOSSL | PICM_LI30_146_LOSSL | THRESHOLD |
| □ | 18 | 2 | PICM:LI30:146:LOSSH | PICM_LI30_146_LOSSH | THRESHOLD |
| □ | 19 | 1 | PICM:LI30:446:LOSSL | PICM_LI30_446_LOSSL | THRESHOLD |
| □ | 19 | 2 | PICM:LI30:446:LOSSH | PICM_LI30_446_LOSSH | THRESHOLD |
| □ | 20 | 1 | PICM:LI30:546:LOSSL | PICM_LI30_546_LOSSL | THRESHOLD |
| □ | 20 | 2 | PICM:LI30:546:LOSSH | PICM_LI30_546_LOSSH | THRESHOLD |
| □ | 21 | 1 | PICM:LI30:801:LOSSL | PICM_LI30_801_LOSSL | THRESHOLD |
| □ | 21 | 2 | PICM:LI30:801:LOSSH | PICM_LI30_801_LOSSH | THRESHOLD |

# 14 Link Node 23

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 1 | KICK:BSY0:66:PM1_ON | KICK_BSY0_66_PM1_ON | STATUS |
| □ | 2 | 2 | KICK:BSY0:66:PM1_OFF | KICK_BSY0_66_PM1_OFF | STATUS |
| □ | 2 | 3 | KICK:BSY0:66:PM1_TRIG_ENB | KICK_BSY0_66_PM1_TRIG_ENB | STATUS |
| □ | 2 | 4 | KICK:BSY0:66:PM1_TRIG_LOCK | KICK_BSY0_66_PM1_TRIG_LOCK | STATUS |
| □ | 2 | 5 | KICK:BSY0:66:PM2_ON | KICK_BSY0_66_PM2_ON | STATUS |
| □ | 2 | 6 | KICK:BSY0:66:PM2_OFF | KICK_BSY0_66_PM2_OFF | STATUS |
| □ | 2 | 7 | KICK:BSY0:66:PM2_TRIG_ENB | KICK_BSY0_66_PM2_TRIG_ENB | STATUS |
| □ | 2 | 8 | KICK:BSY0:66:PM2_TRIG_LOCK | KICK_BSY0_66_PM2_TRIG_LOCK | STATUS |

# 15 Link Node 25

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | HXRC:B969:1:ROOM_LEL | HXRC_B969_1_ROOM_LEL | THRESHOLD |
| □ | 1 | 2 | HXRC:B969:1:RCBR_LEL | HXRC_B969_1_RCBR_LEL | THRESHOLD |
| □ | 1 | 3 | HXRC:B969:1:TIMER | HXRC_B969_1_TIMER | STATE |
| □ | 1 | 4 | STPR:BSYS:891:IN_LIMIT | STPR_BSYS_891_IN_LIMIT | POSITION |
| □ | 1 | 5 | STPR:BSYS:891:OUT_LIMIT | STPR_BSYS_891_OUT_LIMIT | POSITION |
| □ | 1 | 6 | VGFS:BSY0:2:FSG1_OK | FSG1_VAC_OK | STATE |
| □ | 1 | 7 | VGFS:BSY0:2:FSG12_OK | FSG12_VAC_OK | STATE |
| □ | 1 | 8 | VGFS:BSY0:2:FSG3_OK | FSG3_VAC_OK | STATE |
| □ | 1 | 9 | VGFS:BSY0:2:FSG10_OK | FSG10_VAC_OK | STATE |
| □ | 1 | 10 | STPR:BSYH:847:IN_LIMIT | STPR_BSYH_847_IN_LIMIT | POSITION |
| □ | 1 | 11 | STPR:BSYH:847:OUT_LIMIT | STPR_BSYH_847_OUT_LIMIT | POSITION |
| □ | 1 | 12 | STPR:CLTS:585:IN_LIMIT | STPR_CLTS_585_IN_LIMIT | POSITION |
| □ | 1 | 13 | STPP:CLTS:585:OUT_LIMIT | STPP_CLTS_585_OUT_LIMIT | POSITION |
| □ | 2 | 2 | MISC:MCC0:1:EMG_RAD_STOP | EMG_RAD_STOP | STATE |
| □ | 2 | 5 | PPS:MCC0:1:BATTP24OVER | PPS_MCC0_1_BATTP24OVER | STATE |
| □ | 2 | 6 | PPS:MCC0:1:BATTP24UNDER | PPS_MCC0_1_BATTP24UNDER | STATE |
| □ | 2 | 7 | PPS:MCC0:1:BATTN24OVER | PPS_MCC0_1_BATTN24OVER | STATE |
| □ | 2 | 8 | PPS:MCC0:1:BATTN24UNDER | PPS_MCC0_1_BATTN24UNDER | STATE |
| □ | 2 | 9 | CHAS:MCC0:2:BTM_GAS | CHAS_MCC0_2_BTM_GAS | STATE |
| □ | 2 | 10 | CHAS:MCC0:2:BAS2_BTM | CHAS_MCC0_2_BAS2_BTM | STATE |
| □ | 2 | 11 | CHAS:MCC0:2:TEMP_SUM | CHAS_MCC0_2_TEMP_SUM | STATE |
| □ | 2 | 12 | CHAS:MCC0:2:EPVTANK | CHAS_MCC0_2_EPVTANK | STATE |
| □ | 2 | 13 | CHAS:MCC0:2:CCR_BAT | CHAS_MCC0_2_CCR_BAT | STATE |
| □ | 3 | 2 | CHAS:MCC0:1:LNW_BSO | CHAS_MCC0_1_LNW_BSO | STATE |
| □ | 3 | 3 | CHAS:MCC0:1:PPS30MCC | CHAS_MCC0_1_PPS30MCC | STATE |
| □ | 3 | 4 | CHAS:MCC0:1:PPS_2_20 | CHAS_MCC0_1_PPS_2_20 | STATE |
| □ | 3 | 5 | CHAS:MCC0:1:PPS_UND | CHAS_MCC0_1_PPS_UND | STATE |
| □ | 3 | 6 | CHAS:MCC0:1:PPS_WE | CHAS_MCC0_1_PPS_WE | STATE |
| □ | 3 | 7 | CHAS:MCC0:1:PPS_S2_S29 | CHAS_MCC0_1_PPS_S2_S29 | STATE |
| □ | 3 | 8 | CHAS:MCC0:1:WE_BSOIC | CHAS_MCC0_1_WE_BSOIC | STATE |
| □ | 3 | 9 | CHAS:MCC0:1:EE_BSOIC | CHAS_MCC0_1_EE_BSOIC | STATE |
| □ | 3 | 11 | CHAS:MCC0:1:LCLSS20BSOIC | CHAS_MCC0_1_LCLSBSOIC | STATE |
| □ | 3 | 12 | CHAS:MCC0:1:UNDBSOIC | CHAS_MCC0_1_UNDBSOIC | STATE |
| □ | 3 | 13 | CHAS:MCC0:2:BSOIC_ANLG | CHAS_MCC0_2_BSOIC_ANLG | STATE |
| □ | 3 | 14 | CHAS:MCC0:2:VAX_TEMP | CHAS_MCC0_2_VAX_TEMP | STATE |
| □ | 3 | 15 | CHAS:MCC0:2:SITE_AIR | CHAS_MCC0_2_SITE_AIR | STATE |
| □ | 3 | 16 | CHAS:MCC0:2:STPR409_TEMP | CHAS_MCC0_2_STPR409_TEMP | STATE |
| □ | 4 | 2 | CHAS:MCC0:1:VDPS_TEMP | CHAS_MCC0_1_VDPS_TEMP | STATE |

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| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 4 | 3 | CHAS:MCC0:1:DR_LCW | CHAS_MCC0_1_DR_LCW | STATE |
| □ | 4 | 5 | CHAS:MCC0:1:MCC_PLIC | CHAS_MCC0_1_MCC_PLIC | STATE |
| □ | 4 | 6 | CHAS:MCC0:1:MCC_BAT | CHAS_MCC0_1_MCC_BAT | STATE |
| □ | 4 | 9 | CHAS:MCC0:1:1200_LEVEL | CHAS_MCC0_1_1200_LEVEL | STATE |
| □ | 4 | 10 | CHAS:MCC0:1:1200_PUMP | CHAS_MCC0_1_1200_PUMP | STATE |
| □ | 4 | 11 | CHAS:MCC0:1:1201_PUMP | CHAS_MCC0_1_1201_PUMP | STATE |
| □ | 4 | 12 | CHAS:MCC0:1:1202_PUMP | CHAS_MCC0_1_1202_PUMP | STATE |
| □ | 4 | 15 | CHAS:MCC0:1:BDERCBNR | CHAS_MCC0_1_BDERCBNR | STATE |
| □ | 5 | 1 | PROF:BSYA:1:OUT_SUM | PROF_BSYA_1_OUT_SUM | STATE |
| □ | 5 | 2 | BEND:BSYA:140:B1_2_PS_ON | BEND_BSYA_140_B1_2_PS_ON | STATE |
| □ | 5 | 3 | COLL:BSYA:1015:FLOW_SW | COLL_BSYA_1015_FLOW_SW | STATE |
| □ | 5 | 4 | VVFS:BSYA:1030:POSITION | VVFS_BSYA_1030_POSITION | STATE |
| □ | 5 | 5 | VVPG:BSYA:1018:POSITION | VVPG_BSYA_1018_POSITION | STATE |
| □ | 5 | 6 | BEND:BSYA:1100:B11_PS_ON | BEND_BSYA_1100_B11_PS_ON | STATE |
| □ | 5 | 7 | BPM:BSYA:1020:FLOW_SW | BPM_BSYA_1020_FLOW_SW | STATE |
| □ | 5 | 8 | QUAD:BSYA:1010:Q10_PS_ON | QUAD_BSYA_1010_Q10_PS_ON | STATE |
| □ | 5 | 9 | QUAD:BSYA:1090:Q11_PS_ON | QUAD_BSYA_1090_Q11_PS_ON | STATE |
| □ | 5 | 10 | BPM:BSYA:1240:FLOW_SW | BPM_BSYA_1240_FLOW_SW | STATE |
| □ | 5 | 11 | COLL:BSYA:1230:FLOW_SW | COLL_BSYA_1230_FLOW_SW | STATE |
| □ | 5 | 12 | VVPG:BSYA:2635:POSITION | VVPG_BSYA_2635_POSITION | STATE |
| □ | 5 | 13 | VVFS:BSYA:2855:POSITION | VVFS_BSYA_2855_POSITION | STATE |
| □ | 5 | 14 | VVPG:BSYA:4005:POSITION | VVPG_BSYA_4005_POSITION | STATE |
| □ | 5 | 15 | STPR:BSY0:88:IN_LIMIT | STPR_BSY0_88_IN_LIMIT | POSITION |
| □ | 5 | 16 | STPR:BSY0:88:OUT_LIMIT | STPR_BSY0_88_OUT_LIMIT | POSITION |
| □ | 6 | 1 | PICM:BSY0:1:HV_PWR | PICM_BSY0_1_HV_PWR | STATE |
| □ | 6 | 2 | PICM:BSY0:2:HV_PWR | PICM_BSY0_2_HV_PWR | STATE |
| □ | 6 | 3 | PICM:BSY0:3:HV_PWR | PICM_BSY0_3_HV_PWR | STATE |
| □ | 6 | 4 | PICM:BSY0:4:HV_PWR | PICM_BSY0_4_HV_PWR | STATE |
| □ | 6 | 5 | PICM:BSY0:5:HV_PWR | PICM_BSY0_5_HV_PWR | STATE |
| □ | 6 | 6 | PICM:BSY0:6:HV_PWR | PICM_BSY0_6_HV_PWR | STATE |
| □ | 6 | 7 | BEND:BSYA:140:PS_OFF_A | BEND_BSYA_140_PS_OFF_A | STATE |
| □ | 6 | 8 | BEND:BSYA:140:PS_OFF_B | BEND_BSYA_140_PS_OFF_B | STATE |
| □ | 6 | 10 | TRGT:BSYA:964:OUT_LIMSW | TRGT_BSYA_964_OUT_LIMSW | POSITION |
| □ | 6 | 11 | TRGT:BSYA:964:IN_LIMSW | TRGT_BSYA_964_IN_LIMSW | POSITION |
| □ | 7 | 8 | PICM:BSY0:16:NO_TRIG | PICM_BSY0_16_NO_TRIG | MISSED |
| □ | 9 | 1 | PICM:BSYH:479:LOSSL | PICM_BSYH_479_LOSSL | THRESHOLD |
| □ | 9 | 2 | PICM:BSYH:479:LOSSH | PICM_BSYH_479_LOSSH | THRESHOLD |
| □ | 10 | 1 | PICM:BSYH:487:LOSSL | PICM_BSYH_487_LOSSL | THRESHOLD |
| □ | 10 | 2 | PICM:BSYH:487:LOSSH | PICM_BSYH_487_LOSSH | THRESHOLD |
| □ | 11 | 1 | PICM:BSYH:495:LOSSL | PICM_BSYH_495_LOSSL | THRESHOLD |
| □ | 11 | 2 | PICM:BSYH:495:LOSSH | PICM_BSYH_495_LOSSH | THRESHOLD |

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| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 12 | 1 | PICM:BSYH:836:LOSSL | PICM_BSYH_836_LOSSL | THRESHOLD |
| □ | 12 | 2 | PICM:BSYH:836:LOSSH | PICM_BSYH_836_LOSSH | THRESHOLD |
| □ | 13 | 1 | PICM:BSYH:503:LOSSL | PICM_BSYH_503_LOSSL | THRESHOLD |
| □ | 13 | 2 | PICM:BSYH:503:LOSSH | PICM_BSYH_503_LOSSH | THRESHOLD |
| □ | 22 | 1 | PICM:BSYA:1016:LOSSL | PICM_BSYA_1016_LOSSL | THRESHOLD |
| □ | 22 | 2 | PICM:BSYA:1016:LOSSH | PICM_BSYA_1016_LOSSH | THRESHOLD |
| □ | 23 | 1 | PICM:BSYA:1021:LOSSL | PICM_BSYA_1021_LOSSL | THRESHOLD |
| □ | 23 | 2 | PICM:BSYA:1021:LOSSH | PICM_BSYA_1021_LOSSH | THRESHOLD |
| □ | 24 | 1 | PICM:BSYA:1120:LOSSL | PICM_BSYA_1120_LOSSL | THRESHOLD |
| □ | 24 | 2 | PICM:BSYA:1120:LOSSH | PICM_BSYA_1120_LOSSH | THRESHOLD |
| □ | 25 | 1 | PICM:BSYA:1320:LOSSL | PICM_BSYA_1320_LOSSL | THRESHOLD |
| □ | 25 | 2 | PICM:BSYA:1320:LOSSH | PICM_BSYA_1320_LOSSH | THRESHOLD |
| □ | 26 | 1 | PICM:BSYA:1520:LOSSL | PICM_BSYA_1520_LOSSL | THRESHOLD |
| □ | 26 | 2 | PICM:BSYA:1520:LOSSH | PICM_BSYA_1520_LOSSH | THRESHOLD |
| □ | 27 | 1 | PICM:BSYA:1980:LOSSL | PICM_BSYA_1980_LOSSL | THRESHOLD |
| □ | 27 | 2 | PICM:BSYA:1980:LOSSH | PICM_BSYA_1980_LOSSH | THRESHOLD |
| □ | 28 | 1 | PICM:BSYA:1701A:LOSSL | PICM_BSYA_1701A_LOSSL | THRESHOLD |
| □ | 28 | 2 | PICM:BSYA:1701A:LOSSH | PICM_BSYA_1701A_LOSSH | THRESHOLD |
| □ | 29 | 1 | PICM:BSYA:1701B:LOSSL | PICM_BSYA_1701B_LOSSL | THRESHOLD |
| □ | 29 | 2 | PICM:BSYA:1701B:LOSSH | PICM_BSYA_1701B_LOSSH | THRESHOLD |
| □ | 30 | 1 | PICM:BSYH:762:LOSSL | PICM_BSYH_762_LOSSL | THRESHOLD |
| □ | 30 | 2 | PICM:BSYH:762:LOSSH | PICM_BSYH_762_LOSSH | THRESHOLD |

# 16 Link Node 27

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 4 | VVPG:CLTH:178:OPEN_LIMSW | VVPG_CLTH_178_OPEN_LIMSW | POSITION |
| □ | 1 | 5 | VVPG:CLTH:375:OPEN_LIMSW | VVPG_CLTH_375_OPEN_LIMSW | POSITION |
| □ | 1 | 8 | VVPG:CLTS:685:POSITION | VVPG_CLTS_685_POSITION | STATE |
| □ | 1 | 13 | VVPG:BSYA:340:OPEN_LIMSW | VVPG_BSYA_340_OPEN_LIMSW | POSITION |
| □ | 1 | 15 | VVFS:CLTH:101:OPEN_LIMSW | VVFS_CLTH_101_OPEN_LIMSW | POSITION |

# 17 Link Node 28

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 10 | PICM:LTUH:B911:HV_PWR | PICM_LTUH_B911_HV_PWR | STATE |
| □ | 1 | 11 | COLL:LTUH:253:STATE_OUT | COLL_LTUH_253_STATE_OUT | STATE |
| □ | 1 | 12 | COLL:LTUH:452:STATE_OUT | COLL_LTUH_452_STATE_OUT | STATE |
| □ | 2 | 1 | DCHP:LTUH:555:OUT_LIMSW_NEGX | DCHP_LTUH_555_OUT_LIMSW_NEGX | POSITION |
| □ | 2 | 2 | DCHP:LTUH:555:OUT_LIMSW_POSX | DCHP_LTUH_555_OUT_LIMSW_POSX | POSITION |
| □ | 2 | 3 | DCHP:LTUH:545:OUT_LIMSW_NEGY | DCHP_LTUH_545_OUT_LIMSW_NEGY | POSITION |
| □ | 2 | 4 | DCHP:LTUH:545:OUT_LIMSW_POSY | DCHP_LTUH_545_OUT_LIMSW_POSY | POSITION |
| □ | 2 | 5 | DCHP:LTUH:555:GAP_LIMSW_NEGZ | DCHP_LTUH_555_GAP_LIMSW_NEGZ | POSITION |
| □ | 2 | 6 | DCHP:LTUH:555:GAP_LIMSW_POSZ | DCHP_LTUH_555_GAP_LIMSW_POSZ | POSITION |
| □ | 2 | 7 | DCHP:LTUH:545:GAP_LIMSW_NEGZ | DCHP_LTUH_545_GAP_LIMSW_NEGZ | POSITION |
| □ | 2 | 8 | DCHP:LTUH:545:GAP_LIMSW_POSZ | DCHP_LTUH_545_GAP_LIMSW_POSZ | POSITION |
| □ | 7 | 8 | PICM:LTUH:220:NO_TRIG | PICM_LTUH_220_NO_TRIG | MISSED |
| □ | 9 | 1 | PICM:LTUH:220:LOSSL | PICM_LTUH_220_LOSSL | THRESHOLD |
| □ | 9 | 2 | PICM:LTUH:220:LOSSH | PICM_LTUH_220_LOSSH | THRESHOLD |
| □ | 10 | 1 | PICM:LTUH:253:LOSSL | PICM_LTUH_253_LOSSL | THRESHOLD |
| □ | 10 | 2 | PICM:LTUH:253:LOSSH | PICM_LTUH_253_LOSSH | THRESHOLD |
| □ | 11 | 1 | PICM:LTUH:280:LOSSL | PICM_LTUH_280_LOSSL | THRESHOLD |
| □ | 11 | 2 | PICM:LTUH:280:LOSSH | PICM_LTUH_280_LOSSH | THRESHOLD |
| □ | 12 | 1 | PICM:LTUH:420:LOSSL | PICM_LTUH_420_LOSSL | THRESHOLD |
| □ | 12 | 2 | PICM:LTUH:420:LOSSH | PICM_LTUH_420_LOSSH | THRESHOLD |
| □ | 13 | 1 | PICM:LTUH:452:LOSSL | PICM_LTUH_452_LOSSL | THRESHOLD |
| □ | 13 | 2 | PICM:LTUH:452:LOSSH | PICM_LTUH_452_LOSSH | THRESHOLD |

# 18 Link Node 29

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 2 | PICM:LTUH:B912:HV_PWR | PICM_LTUH_B912_HV_PWR | STATE |
| □ | 7 | 8 | PICM:LTUH:480:NO_TRIG | PICM_LTUH_480_NO_TRIG | MISSED |
| □ | 9 | 1 | PICM:LTUH:722:LOSSL | PICM_LTUH_722_LOSSL | THRESHOLD |
| □ | 9 | 2 | PICM:LTUH:722:LOSSH | PICM_LTUH_722_LOSSH | THRESHOLD |
| □ | 11 | 1 | PICM:LTUH:480:LOSSL | PICM_LTUH_480_LOSSL | THRESHOLD |
| □ | 11 | 2 | PICM:LTUH:480:LOSSH | PICM_LTUH_480_LOSSH | THRESHOLD |

# 19 Link Node 30

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 11 | 1 | BLM:LTUH:BLF545:LOSS_L | BLM_LTUH_BLF545_LOSS_L | THRESHOLD |
| □ | 11 | 2 | BLM:LTUH:BLF545:LOSS_H | BLM_LTUH_BLF545_LOSS_H | THRESHOLD |
| □ | 11 | 11 | BLM:LTUH:BLF545:TEST_L | BLM_LTUH_BLF545_TEST_L | THRESHOLD |
| □ | 11 | 12 | BLM:LTUH:BLF545:TEST_H | BLM_LTUH_BLF545_TEST_H | THRESHOLD |
| □ | 11 | 15 | BLM:LTUH:BLF545:PED_L | BLM_LTUH_BLF545_PED_L | THRESHOLD |
| □ | 11 | 16 | BLM:LTUH:BLF545:PED_H | BLM_LTUH_BLF545_PED_H | THRESHOLD |

# 20 Link Node 39

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | STPR:FEE1:ST1:OUT | STPR_FEE1_ST1_OUT | POSITION |
| □ | 1 | 2 | STPR:FEE1:ST1:IN | STPR_FEE1_ST1_IN | POSITION |
| □ | 1 | 3 | STPR:FEE1:ST2:OUT | STPR_FEE1_ST2_OUT | POSITION |
| □ | 1 | 4 | STPR:FEE1:ST2:IN | STPR_FEE1_ST2_IN | POSITION |
| □ | 3 | 10 | XPP:NH3:POS_00:IN | XPP_NH3_POS_00_IN | STATE |
| □ | 3 | 11 | HX3:UVD:VGC_01:CLS_DI | HX3_UVD_VGC_01_CLS_DI | STATE |
| □ | 3 | 12 | HX3:UVD:VGC_01:OPN_DI | HX3_UVD_VGC_01_OPN_DI | STATE |
| □ | 3 | 13 | HX3:DVD:VGC_01:CLS_DI | HX3_DVD_VGC_01_CLS_DI | STATE |
| □ | 3 | 14 | HX3:DVD:VGC_01:OPN_DI | HX3_DVD_VGC_01_OPN_DI | STATE |
| □ | 4 | 4 | XPP:SB2:VGC_04:OPN_DI | XPP_SB2_VGC_04_OPN_DI | POSITION |
| □ | 4 | 10 | XPP:SB2:VGC_02:OPN_DI | XPP_SB2_VGC_02_OPN_DI | POSITION |
| □ | 4 | 11 | XPP:SB2:VGC_03:OPN_DI | XPP_SB2_VGC_03_OPN_DI | POSITION |

# 21 Link Node 40

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | HXX:UM6:CVP_01:A | HXX_UM6_CVP_01_A | STATE |
| □ | 1 | 2 | HXX:UM6:CVP_01:B | HXX_UM6_CVP_01_B | STATE |
| □ | 1 | 3 | HXX:UM6:VGC_01:CLS_DI | HXX_UM6_VGC_01_CLS_DI | STATE |
| □ | 1 | 4 | HXX:UM6:VGC_01:OPN_DI | HXX_UM6_VGC_01_OPN_DI | STATE |
| □ | 1 | 5 | HXX:MXT:VGC_03:CLS_DI | HXX_MXT_VGC_03_CLS_DI | POSITION |
| □ | 1 | 6 | HXX:MXT:VGC_03:OPN_DI | HXX_MXT_VGC_03_OPN_DI | POSITION |
| □ | 1 | 7 | XRT:DIA:MMS_01:B | XRT_DIA_MMS_01_B | POSITION |
| □ | 1 | 8 | CXI:DG1:MMS_03:A | CXI_DG1_MMS_03_A | POSITION |
| □ | 1 | 9 | CXI:DG1:MMS_08:B | CXI_DG1_MMS_08_B | POSITION |
| □ | 1 | 12 | CXI:DG2:MMS_06:B | CXI_DG2_MMS_06_B | POSITION |
| □ | 1 | 13 | CXI:DG2:MMS_08:B | CXI_DG2_MMS_08_B | POSITION |
| □ | 1 | 14 | CXI:DG2:MMS_09:B | CXI_DG2_MMS_09_B | POSITION |
| □ | 1 | 15 | CXI:DG2:MMS_10:B | CXI_DG2_MMS_10_B | POSITION |
| □ | 1 | 16 | CXI:SC3:VGC_01:OPN_DI | CXI_SC3_VGC_01_OPN_DI | POSITION |
| □ | 2 | 1 | CXI:MPS:CFG:1 | CXI_MPS_CFG_1 | CONFID |
| □ | 2 | 2 | CXI:MPS:CFG:2 | CXI_MPS_CFG_2 | CONFID |
| □ | 2 | 3 | CXI:MPS:CFG:3 | CXI_MPS_CFG_3 | CONFID |
| □ | 2 | 4 | CXI:MPS:CFG:4 | CXI_MPS_CFG_4 | CONFID |
| □ | 2 | 5 | CXI:MPS:CFG:5 | CXI_MPS_CFG_5 | CONFID |
| □ | 2 | 6 | CXI:MPS:CFG:6 | CXI_MPS_CFG_6 | CONFID |
| □ | 2 | 7 | CXI:MPS:CFG:7 | CXI_MPS_CFG_7 | CONFID |
| □ | 2 | 8 | CXI:MPS:PLC_01:OK | CXI_MPS_PLC_01_OK | STATUS |
| □ | 2 | 12 | CXI:DG4:MMS_05:B | CXI_DG4_MMS_05_B | POSITION |
| □ | 2 | 13 | CXI:DG4:MMS_03:B | CXI_DG4_MMS_03_B | POSITION |
| □ | 2 | 14 | CXI:DIA:ATTEN:OK | CXI_DIA_ATTEN_OK | POSITION |
| □ | 2 | 15 | HFA:SND:VGC_01:OPN_DI | HFA_SND_VGC_01_OPN_DI | POSITION |
| □ | 2 | 16 | HF4:LAM:VGC_01:OPN_DI | HF4_LAM_VGC_01_OPN_DI | POSITION |
| □ | 3 | 1 | HXX:LOM:VGC_01:CLS_DI | HXX_LOM_VGC_01_CLS_DI | STATE |
| □ | 3 | 2 | HXX:LOM:VGC_01:OPN_DI | HXX_LOM_VGC_01_OPN_DI | STATE |
| □ | 3 | 3 | HXX:DIA:VGC_01:CLS_DI | HXX_DIA_VGC_01_CLS_DI | STATE |
| □ | 3 | 4 | HXX:DIA:VGC_01:OPN_DI | HXX_DIA_VGC_01_OPN_DI | STATE |
| □ | 3 | 5 | HFX:DIA:VGC_02:OPN_DI | HFX_DIA_VGC_02_OPN_DI | POSITION |
| □ | 3 | 6 | CXI:DG1:VGC_01:OPN_DI | CXI_DG1_VGC_01_OPN_DI | POSITION |
| □ | 3 | 7 | CXI:DG1:VGC_02:OPN_DI | CXI_DG1_VGC_02_OPN_DI | POSITION |
| □ | 3 | 8 | CXI:KB1:VGC_01:OPN_DI | CXI_KB1_VGC_01_OPN_DI | POSITION |
| □ | 3 | 9 | CXI:KB1:VGC_02:OPN_DI | CXI_KB1_VGC_02_OPN_DI | POSITION |
| □ | 3 | 10 | CXI:KB2:VGC_01:OPN_DI | CXI_KB2_VGC_01_OPN_DI | POSITION |
| □ | 3 | 12 | CXI:DG2:VGC_01:OPN_DI | CXI_DG2_VGC_01_OPN_DI | POSITION |

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| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 3 | 13 | CXI:DG2:VGC_02:OPN_DI | CXI_DG2_VGC_02_OPN_DI | POSITION |
| □ | 3 | 14 | CXI:SC1:VGC_01:OPN_DI | CXI_SC1_VGC_01_OPN_DI | POSITION |
| □ | 3 | 15 | CXI:SC1:VGC_02:OPN_DI | CXI_SC1_VGC_02_OPN_DI | POSITION |
| □ | 3 | 16 | CXI:DSC:VGC_01:OPN_DI | CXI_DSC_VGC_01_OPN_DI | POSITION |
| □ | 4 | 1 | CXI:SC2:VGC_02:OPN_DI | CXI_SC2_VGC_02_OPN_DI | POSITION |
| □ | 4 | 2 | CXI:SC1:VGC_01:CLS_DI | CXI_SC1_VGC_01_CLS_DI | POSITION |
| □ | 4 | 3 | CXI:DSC:VGC_01:CLS_DI | CXI_DSC_VGC_01_CLS_DI | POSITION |
| □ | 4 | 4 | CXI:DET:01:MPSFAULT | CXI_DET_01_MPSFAULT | STATUS |
| □ | 4 | 5 | CXI:DET:02:MPSFAULT | CXI_DET_02_MPSFAULT | STATUS |
| □ | 4 | 6 | CXI:USR:01:MPSFAULT | CXI_USR_01_MPSFAULT | STATUS |
| □ | 4 | 7 | CXI:USR:02:MPSFAULT | CXI_USR_02_MPSFAULT | STATUS |
| □ | 4 | 8 | CXI:DG2:VGC_01:CLS_DI | CXI_DG2_VGC_01_CLS_DI | POSITION |
| □ | 4 | 9 | CXI:DG2:VCG_02:CLS_DI | CXI_DG2_VCG_02_CLS_DI | POSITION |
| □ | 4 | 10 | HXX:MXT:VGC_01:OPN_DI | HXX_MXT_VGC_01_OPN_DI | POSITION |
| □ | 4 | 11 | HXX:MXT:VGC_02:OPN_DI | HXX_MXT_VGC_02_OPN_DI | POSITION |
| □ | 4 | 12 | HXX:MXT:VGC_05:OPN_DI | HXX_MXT_VGC_05_OPN_DI | POSITION |
| □ | 4 | 13 | CXI:KB2:VGC_02:OPN_DI | CXI_KB2_VGC_02_OPN_DI | POSITION |
| □ | 4 | 14 | MEC:HXM:MMS_04:B | MEC_HXM_MMS_04_B | POSITION |
| □ | 4 | 15 | HXX:HXM:MMS_01:B | HXX_HXM_MMS_01_B | POSITION |
| □ | 4 | 16 | XCS:MPS:PLC_01:OK | XCS_MPS_PLC_01_OK | STATUS |
| □ | 5 | 2 | STPR:XRT1:1:SH2IN | STPR_XRT1_1_SH2IN | STATE |
| □ | 5 | 3 | STPR:XRT1:1:S5IN | STPR_XRT1_1_S5IN | POSITION |
| □ | 5 | 4 | STPR:XRT1:1:S5OUT | STPR_XRT1_1_S5OUT | POSITION |
| □ | 5 | 5 | STPR:FEH1:5:S5BIN | STPR_FEH1_5_S5BIN | POSITION |
| □ | 5 | 6 | STPR:FEH1:5:S5BOUT | STPR_FEH1_5_S5BOUT | POSITION |
| □ | 5 | 7 | STPR:XRT1:1:SH2OUT | STPR_XRT1_1_SH2OUT | STATE |
| □ | 5 | 9 | HFX:UM6:STP_01:IN_DI | HFX_UM6_STP_01_IN_DI | POSITION |
| □ | 5 | 10 | HFX:UM6:STP_01:OUT_DI | HFX_UM6_STP_01_OUT_DI | POSITION |
| □ | 5 | 11 | HFX:DG2:STP_01:IN_DI | HFX_DG2_STP_01_IN_DI | POSITION |
| □ | 5 | 12 | HFX:DG2:STP_01:OUT_DI | HFX_DG2_STP_01_OUT_DI | POSITION |
| □ | 5 | 13 | HFX:DG2:VGC_01:OPN_DI | HFX_DG2_VGC_01_OPN_DI | POSITION |
| □ | 5 | 14 | HFX:MON:VGC_01:OPN_DI | HFX_MON_VGC_01_OPN_DI | POSITION |
| □ | 5 | 15 | MEC:MPS:PLC_01:OK | MEC_MPS_PLC_01_OK | STATUS |
| □ | 5 | 16 | HFX:MON:VGC_02:OPN_DI | HFX_MON_VGC_02_OPN_DI | POSITION |
| □ | 6 | 1 | CXI:KB2:VGC_02:CLS_DI | CXI_KB2_VGC_02_CLS_DI | POSITION |
| □ | 6 | 2 | CXI:DSU:MMS_07:B | CXI_DSU_MMS_07_B | POSITION |
| □ | 6 | 3 | HXX:HXM:MMS_01:INBEAM | HXX_HXM_MMS_01_INBEAM | POSITION |
| □ | 6 | 4 | XCS:SB1:VGC_01:OPN_DI | XCS_SB1_VGC_01_OPN_DI | POSITION |
| □ | 6 | 5 | XCS:SB1:VGC_01:CLS_DI | XCS_SB1_VGC_01_CLS_DI | POSITION |
| □ | 6 | 6 | XCS:SB1:VGC_02:OPN_DI | XCS_SB1_VGC_02_OPN_DI | POSITION |
| □ | 6 | 7 | XCS:SB1:VGC_02:CLS_DI | XCS_SB1_VGC_02_CLS_DI | POSITION |

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| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 6 | 8 | XCS:SB2:VGC_01:OPN_DI | XCS_SB2_VGC_01_OPN_DI | POSITION |
| □ | 6 | 9 | XCS:SB2:VGC_01:CLS_DI | XCS_SB2_VGC_01_CLS_DI | POSITION |
| □ | 6 | 10 | XCS:SB:POS:MONO | XCS_SB_POS_MONO | POSITION |
| □ | 6 | 11 | XCS:SB:POS:HARD | XCS_SB_POS_HARD | POSITION |
| □ | 6 | 12 | XCS:SB1:STP_01:OPN_DI | XCS_SB1_STP_01_OPN_DI | POSITION |
| □ | 6 | 13 | XCS:SB1:STP_01:CLS_DI | XCS_SB1_STP_01_CLS_DI | POSITION |
| □ | 6 | 14 | XCS:USR:01:MPSFAULT | XCS_USR_01_MPSFAULT | STATUS |
| □ | 6 | 15 | STPR:XRT1:1:S6IN | STPR_XRT1_1_S6IN | POSITION |
| □ | 6 | 16 | STPR:XRT1:1:S6OUT | STPR_XRT1_1_S6OUT | POSITION |

# 22 Link Node 41

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | MEC:DV5:VGC_01:OPN_DI | MEC_DV5_VGC_01_OPN_DI | POSITION |
| □ | 1 | 2 | MEC:XT1:VGC_01:OPN_DI | MEC_XT1_VGC_01_OPN_DI | POSITION |
| □ | 1 | 3 | MEC:XT1:MMS_01:B | MEC_XT1_MMS_01_B | POSITION |
| □ | 1 | 4 | MEC:XT2:VGC_02:OPN_DI | MEC_XT2_VGC_02_OPN_DI | POSITION |
| □ | 1 | 5 | MEC:XT2:VGC_03:OPN_DI | MEC_XT2_VGC_03_OPN_DI | POSITION |
| □ | 1 | 6 | MEC:XT2:VGC_04:OPN_DI | MEC_XT2_VGC_04_OPN_DI | POSITION |
| □ | 1 | 7 | MEC:XT2:VGC_05:OPN_DI | MEC_XT2_VGC_05_OPN_DI | POSITION |
| □ | 1 | 8 | MEC:XT2:VGC_05:CLS_DI | MEC_XT2_VGC_05_CLS_DI | POSITION |
| □ | 1 | 9 | MEC:XT2:VGC_06:OPN_DI | MEC_XT2_VGC_06_OPN_DI | POSITION |
| □ | 1 | 10 | MEC:HXM:VGC_01:OPN_DI | MEC_HXM_VGC_01_OPN_DI | POSITION |
| □ | 1 | 13 | MFX:MPS:PLC_VAC:OK | MFX_MPS_PLC_VAC_OK | STATUS |
| □ | 1 | 14 | MEC:DV4:VGC_01:OPN_DI | MEC_DV4_VGC_01_OPN_DI | POSITION |
| □ | 1 | 15 | MEC:BXL:VGC_01:OPN_DI | MEC_BXL_VGC_01_OPN_DI | POSITION |
| □ | 2 | 1 | MFX:XRT:VGC:OUT_SUM | MFX_XRT_VGC_OUT_SUM | STATE |
| □ | 2 | 2 | STPR:XRT:1:S45IN | STPR_XRT_1_S45IN | STATE |
| □ | 2 | 3 | STPR:XRT:1:S45OUT | STPR_XRT_1_S45OUT | STATE |
| □ | 2 | 4 | MFX:MPS:1:SUM | MFX_MPS_1_SUM | STATE |
| □ | 2 | 6 | XCS:PBT:VGC:OUT_SUM | XCS_PBT_VGC_OUT_SUM | STATE |

# 23 Link Node 42

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 7 | COLL:BSYH:479:PCAPM_FLOW_SW | COLL_BSYH_479_PCAPM_FLOW_SW | STATE |
| □ | 1 | 9 | EXPT:ESA0:1:EPS_STAT | EXPT_ESA0_1_EPS_STAT | STATE |
| □ | 1 | 11 | BEND:BSY0:40:40B1_FLOW_SW | BEND_BSY0_40_40B1_FLOW_SW | STATE |
| □ | 2 | 1 | COLL:BSYA:1490:FLOW_SW | COLL_BSYA_1490_FLOW_SW | STATE |
| □ | 2 | 2 | COLL:BSYA:1700:FLOW_SW | COLL_BSYA_1700_FLOW_SW | STATE |
| □ | 2 | 3 | SLIT:BSYA:1980:SL19_FLOW_SW | SLIT_BSYA_1980_SL19_FLOW_SW | STATE |
| □ | 2 | 5 | VGFS:BSYA:1410:FSG22_OK | VGFS_BSYA_1410_FSG22_OK | STATE |
| □ | 2 | 8 | QUAD:BSYA:1950:Q19_PS_ON | QUAD_BSYA_1950_Q19_PS_ON | STATE |
| □ | 2 | 9 | COLL:BSYA:2470:V_FLOW_SW | COLL_BSYA_2470_V_FLOW_SW | STATE |
| □ | 2 | 10 | COLL:BSYA:2470:H_FLOW_SW | COLL_BSYA_2470_H_FLOW_SW | STATE |
| □ | 2 | 11 | COLL:BSYA:2410:FLOW_SW | COLL_BSYA_2410_FLOW_SW | STATE |
| □ | 2 | 12 | COLL:BSYA:2060:FLOW_SW | COLL_BSYA_2060_FLOW_SW | STATE |
| □ | 2 | 13 | COLL:BSYA:2600:FLOW_SW | COLL_BSYA_2600_FLOW_SW | STATE |
| □ | 2 | 14 | COLL:BSYA:2990:FLOW_SW | COLL_BSYA_2990_FLOW_SW | STATE |
| □ | 2 | 15 | COLL:BSYA:3710:FLOW_SW | COLL_BSYA_3710_FLOW_SW | STATE |
| □ | 2 | 16 | BPMS:BSYA:2420:FLOW_SW | BPMS_BSYA_2420_FLOW_SW | STATE |
| □ | 3 | 1 | BPMS:BSYA:2860:FLOW_SW | BPMS_BSYA_2860_FLOW_SW | STATE |
| □ | 3 | 2 | BPMS:BSYA:3115:FLOW_SW | BPMS_BSYA_3115_FLOW_SW | STATE |
| □ | 3 | 3 | COLL:ESA0:4120:FLOW_SW | COLL_ESA0_4120_FLOW_SW | STATE |
| □ | 3 | 4 | VVPG:ESA0:4195:POSITION | VVPG_ESA0_4195_POSITION | STATE |
| □ | 3 | 5 | VGFS:BSYA:3320:FSG29_OK | VGFS_BSYA_3320_FSG29_OK | STATE |
| □ | 3 | 6 | PICM:ESA0:1:GAS_OK | PICM_ESA0_1_GAS_OK | STATE |
| □ | 3 | 7 | MPS:ESA0:1:SEM_HV | MPS_ESA0_1_SEM_HV | STATE |
| □ | 3 | 9 | QUAD:BSYA:2750:Q27_PS_ON | QUAD_BSYA_2750_Q27_PS_ON | STATE |
| □ | 3 | 10 | QUAD:BSYA:2850:Q28_PS_ON | QUAD_BSYA_2850_Q28_PS_ON | STATE |
| □ | 3 | 11 | QUAD:BSYA:2050:Q20_PS_ON | QUAD_BSYA_2050_Q20_PS_ON | STATE |
| □ | 3 | 12 | XCOR:BSYA:2890:A28_PS_ON | XCOR_BSYA_2890_A28_PS_ON | STATE |
| □ | 3 | 13 | YCOR:BSYA:2911:A29_PS_ON | YCOR_BSYA_2911_A29_PS_ON | STATE |
| □ | 3 | 14 | XCOR:BSYA:3212:A32_PS_ON | XCOR_BSYA_3212_A32_PS_ON | STATE |
| □ | 3 | 15 | YCOR:BSYA:3313:A33_PS_ON | YCOR_BSYA_3313_A33_PS_ON | STATE |
| □ | 4 | 1 | KICK:BSY0:66:PM1_5_OFF | KICK_BSY0_66_PM1_5_OFF | STATE |
| □ | 4 | 2 | BEND:BSYA:140:B1_2_PS_OFF | BEND_BSYA_140_B1_2_PS_OFF | STATE |
| □ | 4 | 3 | STPR:BSYA:2275:ST22_IN_LIM | STPR_BSYA_2275_ST22_IN_LIM | STATE |
| □ | 4 | 4 | STPR:BSYA:2930:ST29_IN_LIM | STPR_BSYA_2930_ST29_IN_LIM | STATE |
| □ | 4 | 5 | STPR:BSYA:2275:ST22_OUT_LIM | STPR_BSYA_2275_ST22_OUT_LIM | STATE |
| □ | 4 | 6 | STPR:BSYA:2930:ST29_OUT_LIM | STPR_BSYA_2930_ST29_OUT_LIM | STATE |
| □ | 4 | 7 | SLIT:BSYA:2010:SL10_FT_IN | SLIT_BSYA_2010_SL10_FT_IN | STATE |
| □ | 4 | 8 | SLIT:BSYA:2010:SL10_FB_IN | SLIT_BSYA_2010_SL10_FB_IN | STATE |

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| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 4 | 9 | SLIT:BSYA:2010:SL10_RT_IN | SLIT_BSYA_2010_SL10_RT_IN | STATE |
| □ | 4 | 10 | SLIT:BSYA:2010:SL10_RB_IN | SLIT_BSYA_2010_SL10_RB_IN | STATE |
| □ | 5 | 1 | STPR:BSY0:89:IN_LIMSW_A | STPR_BSY0_89_IN_LIMSW_A | POSITION |
| □ | 5 | 2 | STPR:BSY0:89:IN_LIMSW_B | STPR_BSY0_89_IN_LIMSW_B | POSITION |
| □ | 5 | 3 | STPR:BSY0:89:OUT_LIMSW_A | STPR_BSY0_89_OUT_LIMSW_A | POSITION |
| □ | 5 | 4 | STPR:BSY0:89:OUT_LIMSW_B | STPR_BSY0_89_OUT_LIMSW_B | POSITION |
| □ | 5 | 5 | STPR:BSY0:90:IN_LIMSW_A | STPR_BSY0_90_IN_LIMSW_A | POSITION |
| □ | 5 | 6 | STPR:BSY0:90:IN_LIMSW_B | STPR_BSY0_90_IN_LIMSW_B | POSITION |
| □ | 5 | 7 | STPR:BSY0:90:OUT_LIMSW_A | STPR_BSY0_90_OUT_LIMSW_A | POSITION |
| □ | 5 | 8 | STPR:BSY0:90:OUT_LIMSW_B | STPR_BSY0_90_OUT_LIMSW_B | POSITION |
| □ | 5 | 9 | STPR:BSY0:88:IN_LIMSW_A | STPR_BSY0_88_IN_LIMSW_A | POSITION |
| □ | 5 | 10 | STPR:BSY0:88:IN_LIMSW_B | STPR_BSY0_88_IN_LIMSW_B | POSITION |
| □ | 5 | 11 | STPR:BSY0:88:OUT_LIMSW_A | STPR_BSY0_88_OUT_LIMSW_A | POSITION |
| □ | 5 | 12 | BTM:BSY0:89:IN_LIMSW_A | BTM_BSY0_89_IN_LIMSW_A | POSITION |
| □ | 5 | 13 | BTM:BSY0:89:IN_LIMSW_B | BTM_BSY0_89_IN_LIMSW_B | POSITION |
| □ | 5 | 14 | BTM:BSY0:89:OUT_LIMSW_A | BTM_BSY0_89_OUT_LIMSW_A | POSITION |
| □ | 5 | 15 | BTM:BSY0:89:OUT_CNTRL_A | BTM_BSY0_89_OUT_CNTRL_A | PPS_CNTRL |
| □ | 5 | 16 | BTM:BSY0:89:OUT_CNTRL_B | BTM_BSY0_89_OUT_CNTRL_B | PPS_CNTRL |
| □ | 7 | 8 | PICM:BSYA:2061:NO_TRIG | PICM_BSYA_2061_NO_TRIG | MISSED |
| □ | 9 | 1 | PICM:BSYA:2061:LOSSL | PICM_BSYA_2061_LOSSL | THRESHOLD |
| □ | 9 | 2 | PICM:BSYA:2061:LOSSH | PICM_BSYA_2061_LOSSH | THRESHOLD |
| □ | 10 | 1 | PICM:BSYA:2120:LOSSL | PICM_BSYA_2120_LOSSL | THRESHOLD |
| □ | 10 | 2 | PICM:BSYA:2120:LOSSH | PICM_BSYA_2120_LOSSH | THRESHOLD |
| □ | 11 | 1 | PICM:BSYA:2220:LOSSL | PICM_BSYA_2220_LOSSL | THRESHOLD |
| □ | 11 | 2 | PICM:BSYA:2220:LOSSH | PICM_BSYA_2220_LOSSH | THRESHOLD |
| □ | 12 | 1 | PICM:BSYA:2321:LOSSL | PICM_BSYA_2321_LOSSL | THRESHOLD |
| □ | 12 | 2 | PICM:BSYA:2321:LOSSH | PICM_BSYA_2321_LOSSH | THRESHOLD |
| □ | 13 | 1 | PICM:BSYA:2411:LOSSL | PICM_BSYA_2411_LOSSL | THRESHOLD |
| □ | 13 | 2 | PICM:BSYA:2411:LOSSH | PICM_BSYA_2411_LOSSH | THRESHOLD |
| □ | 14 | 1 | PICM:BSYA:2472:LOSSL | PICM_BSYA_2472_LOSSL | THRESHOLD |
| □ | 14 | 2 | PICM:BSYA:2472:LOSSH | PICM_BSYA_2472_LOSSH | THRESHOLD |
| □ | 15 | 1 | PICM:BSYA:2473:LOSSL | PICM_BSYA_2473_LOSSL | THRESHOLD |
| □ | 15 | 2 | PICM:BSYA:2473:LOSSH | PICM_BSYA_2473_LOSSH | THRESHOLD |
| □ | 16 | 1 | PICM:BSYA:2474:LOSSL | PICM_BSYA_2474_LOSSL | THRESHOLD |
| □ | 16 | 2 | PICM:BSYA:2474:LOSSH | PICM_BSYA_2474_LOSSH | THRESHOLD |
| □ | 17 | 1 | PICM:BSYA:2475:LOSSL | PICM_BSYA_2475_LOSSL | THRESHOLD |
| □ | 17 | 2 | PICM:BSYA:2475:LOSSH | PICM_BSYA_2475_LOSSH | THRESHOLD |
| □ | 18 | 1 | PICM:BSYA:2520:LOSSL | PICM_BSYA_2520_LOSSL | THRESHOLD |
| □ | 18 | 2 | PICM:BSYA:2520:LOSSH | PICM_BSYA_2520_LOSSH | THRESHOLD |
| □ | 19 | 1 | PICM:BSYA:2601:LOSSL | PICM_BSYA_2601_LOSSL | THRESHOLD |
| □ | 19 | 2 | PICM:BSYA:2601:LOSSH | PICM_BSYA_2601_LOSSH | THRESHOLD |

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| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 20 | 1 | PICM:BSYA:2861:LOSSL | PICM_BSYA_2861_LOSSL | THRESHOLD |
| □ | 20 | 2 | PICM:BSYA:2861:LOSSH | PICM_BSYA_2861_LOSSH | THRESHOLD |
| □ | 21 | 1 | PICM:BSYA:2990:LOSSL | PICM_BSYA_2990_LOSSL | THRESHOLD |
| □ | 21 | 2 | PICM:BSYA:2990:LOSSH | PICM_BSYA_2990_LOSSH | THRESHOLD |
| □ | 22 | 1 | PICM:BSYA:3320:LOSSL | PICM_BSYA_3320_LOSSL | THRESHOLD |
| □ | 22 | 2 | PICM:BSYA:3320:LOSSH | PICM_BSYA_3320_LOSSH | THRESHOLD |
| □ | 23 | 1 | PICM:BSYA:3321:LOSSL | PICM_BSYA_3321_LOSSL | THRESHOLD |
| □ | 23 | 2 | PICM:BSYA:3321:LOSSH | PICM_BSYA_3321_LOSSH | THRESHOLD |
| □ | 24 | 1 | PICM:BSYA:3720:LOSSL | PICM_BSYA_3720_LOSSL | THRESHOLD |
| □ | 24 | 2 | PICM:BSYA:3720:LOSSH | PICM_BSYA_3720_LOSSH | THRESHOLD |
| □ | 25 | 1 | PICM:BSYA:3721:LOSSL | PICM_BSYA_3721_LOSSL | THRESHOLD |
| □ | 25 | 2 | PICM:BSYA:3721:LOSSH | PICM_BSYA_3721_LOSSH | THRESHOLD |
| □ | 26 | 1 | PICM:BSYA:3890:LOSSL | PICM_BSYA_3890_LOSSL | THRESHOLD |
| □ | 26 | 2 | PICM:BSYA:3890:LOSSH | PICM_BSYA_3890_LOSSH | THRESHOLD |
| □ | 32 | 1 | PICM:CLTS:280:LOSSL | PICM_CLTS_280_LOSSL | THRESHOLD |
| □ | 32 | 2 | PICM:CLTS:280:LOSSH | PICM_CLTS_280_LOSSH | THRESHOLD |
| □ | 32 | 3 | PICM:CLTS:280:LOSS1L | PICM_CLTS_280_LOSS1L | THRESHOLD |
| □ | 32 | 4 | PICM:CLTS:280:LOSS1H | PICM_CLTS_280_LOSS1H | THRESHOLD |
| □ | 32 | 5 | PICM:CLTS:280:LOSS10L | PICM_CLTS_280_LOSS10L | THRESHOLD |
| □ | 32 | 6 | PICM:CLTS:280:LOSS10H | PICM_CLTS_280_LOSS10H | THRESHOLD |
| □ | 32 | 7 | PICM:CLTS:280:LOSS30L | PICM_CLTS_280_LOSS30L | THRESHOLD |
| □ | 32 | 8 | PICM:CLTS:280:LOSS30H | PICM_CLTS_280_LOSS30H | THRESHOLD |
| □ | 32 | 9 | PICM:CLTS:280:LOSS60L | PICM_CLTS_280_LOSS60L | THRESHOLD |
| □ | 32 | 10 | PICM:CLTS:280:LOSS60H | PICM_CLTS_280_LOSS60H | THRESHOLD |

# 24 Link Node 43

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 1 | 1 | KICK:BSY0:66:A_KIK | KICK_BSY0_66_A_KIK | STATUS |
| □ | 1 | 2 | KICK:BSY0:66:A_KIKRB | KICK_BSY0_66_A_KIKRB | STATUS |
| □ | 7 | 1 | KICK:BSY0:66:TA120MISS | KICK_BSY0_66_TA120MISS | MISSED |
| □ | 7 | 2 | KICK:BSY0:66:TS30MISS | KICK_BSY0_66_TS30MISS | MISSED |
| □ | 7 | 3 | KICK:BSY0:66:TA30MISS | KICK_BSY0_66_TA30MISS | MISSED |
| □ | 7 | 4 | KICK:BSY0:66:TS120MISS | KICK_BSY0_66_TS120MISS | MISSED |
| □ | 7 | 5 | KICK:BSY0:66:SPARE | KICK_BSY0_66_SPARE | MISSED |
| □ | 7 | 6 | KICK:BSY0:66:THRSHUNDR | KICK_BSY0_66_THRSHUNDR | MISSED |
| □ | 7 | 7 | KICK:BSY0:66:THRSHOVER | KICK_BSY0_66_THRSHOVER | MISSED |

# 25 Link Node 127

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 1 | BPMS:UNDH:3190:YCUHBR | BPMS_UNDH_3190_YCUHBR | ORBIT |
| □ | 2 | 2 | BPMS:UNDH:3290:XCUHBR | BPMS_UNDH_3290_XCUHBR | ORBIT |
| □ | 2 | 3 | BPMS:UNDH:3290:YCUHBR | BPMS_UNDH_3290_YCUHBR | ORBIT |
| □ | 2 | 4 | BPMS:UNDH:3390:XCUHBR | BPMS_UNDH_3390_XCUHBR | ORBIT |
| □ | 2 | 5 | BPMS:UNDH:3390:YCUHBR | BPMS_UNDH_3390_YCUHBR | ORBIT |
| □ | 2 | 6 | BPMS:UNDH:3490:XCUHBR | BPMS_UNDH_3490_XCUHBR | ORBIT |
| □ | 2 | 7 | BPMS:UNDH:3490:YCUHBR | BPMS_UNDH_3490_YCUHBR | ORBIT |
| □ | 2 | 8 | BPMS:UNDH:3590:XCUHBR | BPMS_UNDH_3590_XCUHBR | ORBIT |
| □ | 2 | 9 | SIOC:BSYH:MP01:HEARTBEAT | SIOC_BSYH_MP01_HEARTBEAT | STATE |
| □ | 2 | 10 | SIOC:BSYH:MP02:HEARTBEAT | SIOC_BSYH_MP02_HEARTBEAT | STATE |
| □ | 2 | 14 | BPMS:UNDH:3090:XCUHBR | BPMS_UNDH_3090_XCUHBR | ORBIT |
| □ | 2 | 15 | BPMS:UNDH:3090:YCUHBR | BPMS_UNDH_3090_YCUHBR | ORBIT |
| □ | 2 | 16 | BPMS:UNDH:3190:XCUHBR | BPMS_UNDH_3190_XCUHBR | ORBIT |
| □ | 4 | 1 | VVPG:BSYH:837:POSITION | VVPG_BSYH_837_POSITION | STATE |
| □ | 4 | 2 | VVMG:LTUH:174:POSITION | VVMG_LTUH_174_POSITION | STATE |
| □ | 4 | 3 | VVMG:LTUH:355:POSITION | VVMG_LTUH_355_POSITION | STATE |
| □ | 4 | 4 | VVFS:LTUH:102:POSITION | VVFS_LTUH_102_POSITION | STATE |
| □ | 4 | 5 | VVPG:LTUH:102:POSITION | VVPG_LTUH_102_POSITION | STATE |
| □ | 4 | 9 | COLL:BSYH:892:TEMP | COLL_BSYH_892_TEMP | STATE |
| □ | 4 | 14 | VVPG:LTUH:265:POSITION | VVPG_LTUH_265_POSITION | STATE |
| □ | 4 | 15 | VVPG:BSYH:470:POSITION | VVPG_BSYH_470_POSITION | STATE |
| □ | 4 | 16 | VVPG:BSYH:541:POSITION | VVPG_BSYH_541_POSITION | STATE |

# 26 Link Node 129

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 1 | BPMS:UNDH:3990:XCUHBR | BPMS_UNDH_3990_XCUHBR | ORBIT |
| □ | 2 | 2 | BPMS:UNDH:3990:YCUHBR | BPMS_UNDH_3990_YCUHBR | ORBIT |
| □ | 2 | 3 | BPMS:UNDH:4090:XCUHBR | BPMS_UNDH_4090_XCUHBR | ORBIT |
| □ | 2 | 4 | BPMS:UNDH:4090:YCUHBR | BPMS_UNDH_4090_YCUHBR | ORBIT |
| □ | 2 | 5 | BPMS:UNDH:4190:XCUHBR | BPMS_UNDH_4190_XCUHBR | ORBIT |
| □ | 2 | 6 | BPMS:UNDH:4190:YCUHBR | BPMS_UNDH_4190_YCUHBR | ORBIT |
| □ | 2 | 7 | BPMS:UNDH:4290:XCUHBR | BPMS_UNDH_4290_XCUHBR | ORBIT |
| □ | 2 | 8 | BPMS:UNDH:4290:YCUHBR | BPMS_UNDH_4290_YCUHBR | ORBIT |
| □ | 2 | 9 | SIOC:BSYH:MP03:HEARTBEAT | SIOC_BSYH_MP03_HEARTBEAT | STATE |
| □ | 2 | 10 | KICK:LTUH:320:TREF | KICK_LTUH_320_TREF | TIMING |
| □ | 2 | 11 | KICK:LTUH:320:TDES | KICK_LTUH_320_TDES | TIMING |
| □ | 2 | 12 | BPMS:UNDH:3690:YCUHBR | BPMS_UNDH_3690_YCUHBR | ORBIT |
| □ | 2 | 13 | BPMS:UNDH:3790:XCUHBR | BPMS_UNDH_3790_XCUHBR | ORBIT |
| □ | 2 | 14 | BPMS:UNDH:3790:YCUHBR | BPMS_UNDH_3790_YCUHBR | ORBIT |
| □ | 2 | 15 | BPMS:UNDH:3890:XCUHBR | BPMS_UNDH_3890_XCUHBR | ORBIT |
| □ | 2 | 16 | BPMS:UNDH:3890:YCUHBR | BPMS_UNDH_3890_YCUHBR | ORBIT |
| □ | 4 | 1 | COLL:LTUH:253:TEMP | COLL_LTUH_253_TEMP | STATE |
| □ | 4 | 2 | COLL:LTUH:276:TEMP | COLL_LTUH_276_TEMP | STATE |
| □ | 4 | 3 | KICK:LTUH:320:ON | KICK_LTUH_320_ON | STATE |
| □ | 4 | 4 | KICK:LTUH:320:OFF | KICK_LTUH_320_OFF | STATE |
| □ | 16 | 9 | KICK:LTUH:320:CURRENT | KICK_LTUH_320_CURRENT | THRESHOLD |

# 27 Link Node 130

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 1 | BPMS:UNDH:4690:XCUHBR | BPMS_UNDH_4690_XCUHBR | ORBIT |
| □ | 2 | 2 | BPMS:UNDH:4690:YCUHBR | BPMS_UNDH_4690_YCUHBR | ORBIT |
| □ | 2 | 3 | BPMS:UNDH:1890:XCUHBR | BPMS_UNDH_1890_XCUHBR | ORBIT |
| □ | 2 | 4 | BPMS:UNDH:1890:YCUHBR | BPMS_UNDH_1890_YCUHBR | ORBIT |
| □ | 2 | 5 | BPMS:UNDH:3590:YCUHBR | BPMS_UNDH_3590_YCUHBR | ORBIT |
| □ | 2 | 6 | BPMS:UNDH:3690:XCUHBR | BPMS_UNDH_3690_XCUHBR | ORBIT |
| □ | 2 | 11 | BPMS:UNDH:4390:XCUHBR | BPMS_UNDH_4390_XCUHBR | ORBIT |
| □ | 2 | 12 | BPMS:UNDH:4390:YCUHBR | BPMS_UNDH_4390_YCUHBR | ORBIT |
| □ | 2 | 13 | BPMS:UNDH:4490:XCUHBR | BPMS_UNDH_4490_XCUHBR | ORBIT |
| □ | 2 | 14 | BPMS:UNDH:4490:YCUHBR | BPMS_UNDH_4490_YCUHBR | ORBIT |
| □ | 2 | 15 | BPMS:UNDH:4590:XCUHBR | BPMS_UNDH_4590_XCUHBR | ORBIT |
| □ | 2 | 16 | BPMS:UNDH:4590:YCUHBR | BPMS_UNDH_4590_YCUHBR | ORBIT |

# 28 Link Node 131

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:LTUH:MP02:HEARTBEAT | SIOC_LTUH_MP02_HEARTBEAT | STATE |

# 29 Link Node 132

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:LTUH:MP03:HEARTBEAT | SIOC_LTUH_MP03_HEARTBEAT | STATE |
| □ | 4 | 3 | BEND:DMPH:400:ON | BEND_DMPH_400_ON | STATE |
| □ | 4 | 4 | BEND:DMPH:400:OFF | BEND_DMPH_400_OFF | STATE |
| □ | 4 | 9 | OTRS:LTUH:449:IN_LIMIT | OTRS_LTUH_449_IN_LIMIT | POSITION |
| □ | 4 | 10 | OTRS:LTUH:449:OUT_LIMIT | OTRS_LTUH_449_OUT_LIMIT | POSITION |
| □ | 4 | 11 | MIRR:LTUH:548:IN_LIMIT | MIRR_LTUH_548_IN_LIMIT | POSITION |
| □ | 4 | 12 | MIRR:LTUH:548:OUT_LIMIT | MIRR_LTUH_548_OUT_LIMIT | POSITION |
| □ | 4 | 13 | COLL:LTUH:393:TEMP | COLL_LTUH_393_TEMP | STATE |
| □ | 4 | 14 | COLL:LTUH:452:TEMP | COLL_LTUH_452_TEMP | STATE |
| □ | 4 | 15 | COLL:LTUH:475:TEMP | COLL_LTUH_475_TEMP | STATE |
| □ | 16 | 11 | BEND:DMPH:400:CURRENT | BEND_DMPH_400_CURRENT | THRESHOLD |

# 30 Link Node 133

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 1 | BPMS:UNDH:2690:YCUHBR | BPMS_UNDH_2690_YCUHBR | ORBIT |
| □ | 2 | 2 | BPMS:UNDH:2790:XCUHBR | BPMS_UNDH_2790_XCUHBR | ORBIT |
| □ | 2 | 3 | BPMS:UNDH:2790:YCUHBR | BPMS_UNDH_2790_YCUHBR | ORBIT |
| □ | 2 | 4 | BPMS:UNDH:2890:XCUHBR | BPMS_UNDH_2890_XCUHBR | ORBIT |
| □ | 2 | 5 | BPMS:UNDH:2890:YCUHBR | BPMS_UNDH_2890_YCUHBR | ORBIT |
| □ | 2 | 6 | BPMS:UNDH:2990:XCUHBR | BPMS_UNDH_2990_XCUHBR | ORBIT |
| □ | 2 | 7 | BPMS:UNDH:2990:YCUHBR | BPMS_UNDH_2990_YCUHBR | ORBIT |
| □ | 2 | 9 | SIOC:LTUH:MP04:HEARTBEAT | SIOC_LTUH_MP04_HEARTBEAT | STATE |
| □ | 2 | 10 | BPMS:UNDH:2190:XCUHBR | BPMS_UNDH_2190_XCUHBR | ORBIT |
| □ | 2 | 11 | BPMS:UNDH:2190:YCUHBR | BPMS_UNDH_2190_YCUHBR | ORBIT |
| □ | 2 | 12 | BPMS:UNDH:2490:XCUHBR | BPMS_UNDH_2490_XCUHBR | ORBIT |
| □ | 2 | 13 | BPMS:UNDH:2490:YCUHBR | BPMS_UNDH_2490_YCUHBR | ORBIT |
| □ | 2 | 14 | BPMS:UNDH:2590:XCUHBR | BPMS_UNDH_2590_XCUHBR | ORBIT |
| □ | 2 | 15 | BPMS:UNDH:2590:YCUHBR | BPMS_UNDH_2590_YCUHBR | ORBIT |
| □ | 2 | 16 | BPMS:UNDH:2690:XCUHBR | BPMS_UNDH_2690_XCUHBR | ORBIT |

# 31 Link Node 134

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:LTUH:MP05:HEARTBEAT | SIOC_LTUH_MP05_HEARTBEAT | STATE |
| □ | 2 | 10 | SIOC:LTUH:MP06:HEARTBEAT | SIOC_LTUH_MP06_HEARTBEAT | STATE |
| □ | 3 | 1 | VVMG:LTUH:774:POSITION | VVMG_LTUH_774_POSITION | STATE |
| □ | 3 | 9 | VVPG:LTUH:435:POSITION | VVPG_LTUH_435_POSITION | STATE |
| □ | 3 | 10 | VVPG:LTUH:580:POSITION | VVPG_LTUH_580_POSITION | STATE |
| □ | 3 | 11 | VVPG:LTUH:725:POSITION | VVPG_LTUH_725_POSITION | STATE |
| □ | 3 | 12 | VVPG:LTUH:757:POSITION | VVPG_LTUH_757_POSITION | STATE |
| □ | 3 | 14 | VVMG:LTUH:505:POSITION | VVMG_LTUH_505_POSITION | STATE |
| □ | 3 | 15 | VVMG:LTUH:670:POSITION | VVMG_LTUH_670_POSITION | STATE |
| □ | 3 | 16 | VVMG:LTUH:742:POSITION | VVMG_LTUH_742_POSITION | STATE |
| □ | 4 | 9 | YAGS:LTUH:743:P1_LMTSW | YAGS_LTUH_743_P1_LMTSW | POSITION |
| □ | 4 | 10 | YAGS:LTUH:743:OUT_LMTSW | YAGS_LTUH_743_OUT_LMTSW | POSITION |
| □ | 4 | 11 | OTRS:LTUH:745:IN_LIMIT | OTRS_LTUH_745_IN_LIMIT | POSITION |
| □ | 4 | 12 | OTRS:LTUH:745:OUT_LIMIT | OTRS_LTUH_745_OUT_LIMIT | POSITION |
| □ | 4 | 13 | YAGS:LTUH:743:P2_LMTSW | YAGS_LTUH_743_P2_LMTSW | POSITION |

# 32 Link Node 136

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 1 | BPMS:UNDH:2090:XCUHBR | BPMS_UNDH_2090_XCUHBR | ORBIT |
| □ | 2 | 2 | BPMS:UNDH:2090:YCUHBR | BPMS_UNDH_2090_YCUHBR | ORBIT |
| □ | 2 | 3 | BPMS:UNDH:1305:XCUHBR | BPMS_UNDH_1305_XCUHBR | ORBIT |
| □ | 2 | 4 | BPMS:UNDH:1305:YCUHBR | BPMS_UNDH_1305_YCUHBR | ORBIT |
| □ | 2 | 5 | BPMS:UNDH:1390:XCUHBR | BPMS_UNDH_1390_XCUHBR | ORBIT |
| □ | 2 | 6 | BPMS:UNDH:1390:YCUHBR | BPMS_UNDH_1390_YCUHBR | ORBIT |
| □ | 2 | 7 | BPMS:UNDH:1490:XCUHBR | BPMS_UNDH_1490_XCUHBR | ORBIT |
| □ | 2 | 8 | BPMS:UNDH:1490:YCUHBR | BPMS_UNDH_1490_YCUHBR | ORBIT |
| □ | 2 | 9 | SIOC:UNDH:MP01:HEARTBEAT | SIOC_UNDH_MP01_HEARTBEAT | STATE |
| □ | 2 | 10 | SIOC:UNDH:MP02:HEARTBEAT | SIOC_UNDH_MP02_HEARTBEAT | STATE |
| □ | 2 | 11 | BPMS:UNDH:2290:XCUHBR | BPMS_UNDH_2290_XCUHBR | ORBIT |
| □ | 2 | 12 | BPMS:UNDH:2290:YCUHBR | BPMS_UNDH_2290_YCUHBR | ORBIT |
| □ | 2 | 13 | BPMS:UNDH:2390:XCUHBR | BPMS_UNDH_2390_XCUHBR | ORBIT |
| □ | 2 | 14 | BPMS:UNDH:2390:YCUHBR | BPMS_UNDH_2390_YCUHBR | ORBIT |
| □ | 2 | 15 | BPMS:UNDH:1990:XCUHBR | BPMS_UNDH_1990_XCUHBR | ORBIT |
| □ | 2 | 16 | BPMS:UNDH:1990:YCUHBR | BPMS_UNDH_1990_YCUHBR | ORBIT |
| □ | 6 | 6 | BPMS:UNDH:2690:ORBIT | BPMS_UNDH_2690_ORBIT | STATUS |
| □ | 6 | 16 | BPMS:UNDH:2490:ORBIT | BPMS_UNDH_2490_ORBIT | STATUS |
| □ | 16 | 1 | CBLM:UNDH:2575:I1 | CBLM_UNDH_2575_I1 | THRESHOLD |
| □ | 16 | 2 | CBLM:UNDH:2675:I1 | CBLM_UNDH_2675_I1 | THRESHOLD |
| □ | 16 | 9 | CBLM:UNDH:2375:I0 | CBLM_UNDH_2375_I0 | THRESHOLD |
| □ | 16 | 10 | CBLM:UNDH:2475:I0 | CBLM_UNDH_2475_I0 | THRESHOLD |
| □ | 16 | 11 | CBLM:UNDH:2575:I0 | CBLM_UNDH_2575_I0 | THRESHOLD |
| □ | 16 | 12 | CBLM:UNDH:2675:I0 | CBLM_UNDH_2675_I0 | THRESHOLD |
| □ | 16 | 15 | CBLM:UNDH:2375:I1 | CBLM_UNDH_2375_I1 | THRESHOLD |
| □ | 16 | 16 | CBLM:UNDH:2475:I1 | CBLM_UNDH_2475_I1 | THRESHOLD |

# 33 Link Node 138

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:UNDH:MP03:HEARTBEAT | SIOC_UNDH_MP03_HEARTBEAT | STATE |
| □ | 2 | 10 | BPMS:UNDH:1690:YCUHBR | BPMS_UNDH_1690_YCUHBR | ORBIT |
| □ | 2 | 11 | BPMS:UNDH:1790:XCUHBR | BPMS_UNDH_1790_XCUHBR | ORBIT |
| □ | 2 | 12 | BPMS:UNDH:1790:YCUHBR | BPMS_UNDH_1790_YCUHBR | ORBIT |
| □ | 2 | 13 | BPMS:UNDH:1590:XCUHBR | BPMS_UNDH_1590_XCUHBR | ORBIT |
| □ | 2 | 14 | BPMS:UNDH:1690:XCUHBR | BPMS_UNDH_1690_XCUHBR | ORBIT |
| □ | 2 | 15 | BPMS:UNDH:1590:YCUHBR | BPMS_UNDH_1590_YCUHBR | ORBIT |
| □ | 5 | 5 | BPMS:UNDH:2190:ORBIT | BPMS_UNDH_2190_ORBIT | STATUS |
| □ | 16 | 1 | CBLM:UNDH:1975:I1 | CBLM_UNDH_1975_I1 | THRESHOLD |
| □ | 16 | 2 | CBLM:UNDH:2075:I1 | CBLM_UNDH_2075_I1 | THRESHOLD |
| □ | 16 | 3 | CBLM:UNDH:2175:I1 | CBLM_UNDH_2175_I1 | THRESHOLD |
| □ | 16 | 4 | CBLM:UNDH:2275:I1 | CBLM_UNDH_2275_I1 | THRESHOLD |
| □ | 16 | 9 | CBLM:UNDH:1775:I0 | CBLM_UNDH_1775_I0 | THRESHOLD |
| □ | 16 | 10 | CBLM:UNDH:1875:I0 | CBLM_UNDH_1875_I0 | THRESHOLD |
| □ | 16 | 11 | CBLM:UNDH:1975:I0 | CBLM_UNDH_1975_I0 | THRESHOLD |
| □ | 16 | 12 | CBLM:UNDH:2075:I0 | CBLM_UNDH_2075_I0 | THRESHOLD |
| □ | 16 | 13 | CBLM:UNDH:2175:I0 | CBLM_UNDH_2175_I0 | THRESHOLD |
| □ | 16 | 14 | CBLM:UNDH:2275:I0 | CBLM_UNDH_2275_I0 | THRESHOLD |
| □ | 16 | 15 | CBLM:UNDH:1775:I1 | CBLM_UNDH_1775_I1 | THRESHOLD |
| □ | 16 | 16 | CBLM:UNDH:1875:I1 | CBLM_UNDH_1875_I1 | THRESHOLD |

# 34 Link Node 139

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:UNDH:MP04:HEARTBEAT | SIOC_UNDH_MP04_HEARTBEAT | STATE |
| □ | 2 | 10 | MPS:UNDH:1:CBLM_RMON_STATE | MPS_UNDH_1_CBLM_RMON_STATE | STATE |
| □ | 2 | 11 | MPS:UNDH:1:CBLM_WDOG_STATE | MPS_UNDH_1_CBLM_WDOG_STATE | STATE |
| □ | 3 | 9 | VVMG:UNDH:1338:POSITION | VVMG_UNDH_1338_POSITION | STATE |
| □ | 3 | 10 | VVMG:UNDH:1343:POSITION | VVMG_UNDH_1343_POSITION | STATE |
| □ | 3 | 11 | VVMG:UNDH:2138:POSITION | VVMG_UNDH_2138_POSITION | STATE |
| □ | 3 | 12 | VVMG:UNDH:2143:POSITION | VVMG_UNDH_2143_POSITION | STATE |
| □ | 4 | 2 | DUMP:LTUH:970:IN_LIMIT | DUMP_LTUH_970_IN_LIMIT | POSITION |
| □ | 4 | 3 | DUMP:LTUH:970:OUT_LIMIT | DUMP_LTUH_970_OUT_LIMIT | POSITION |
| □ | 4 | 4 | BEND:UNDH:2810:ON | BEND_UNDH_2810_ON | STATE |
| □ | 4 | 5 | BEND:UNDH:2810:OFF | BEND_UNDH_2810_OFF | STATE |
| □ | 4 | 6 | XTAL:UNDH:2850:OUT_LMTSW | XTAL_UNDH_2850_OUT_LMTSW | STATE |
| □ | 4 | 9 | VVPG:UNDH:1288:POSITION | VVPG_UNDH_1288_POSITION | STATE |
| □ | 4 | 10 | VVPG:UNDH:2098:POSITION | VVPG_UNDH_2098_POSITION | STATE |
| □ | 4 | 11 | VVPG:UNDH:2298:POSITION | VVPG_UNDH_2298_POSITION | STATE |
| □ | 4 | 12 | VVMG:UNDH:1498:POSITION | VVMG_UNDH_1498_POSITION | STATE |
| □ | 4 | 13 | VVMG:UNDH:1798:POSITION | VVMG_UNDH_1798_POSITION | STATE |
| □ | 4 | 14 | VVMG:UNDH:2498:POSITION | VVMG_UNDH_2498_POSITION | STATE |
| □ | 16 | 1 | CBLM:UNDH:1375:I1 | CBLM_UNDH_1375_I1 | THRESHOLD |
| □ | 16 | 2 | CBLM:UNDH:1475:I1 | CBLM_UNDH_1475_I1 | THRESHOLD |
| □ | 16 | 3 | CBLM:UNDH:1575:I1 | CBLM_UNDH_1575_I1 | THRESHOLD |
| □ | 16 | 4 | CBLM:UNDH:1675:I1 | CBLM_UNDH_1675_I1 | THRESHOLD |
| □ | 16 | 11 | CBLM:UNDH:1375:I0 | CBLM_UNDH_1375_I0 | THRESHOLD |
| □ | 16 | 12 | CBLM:UNDH:1475:I0 | CBLM_UNDH_1475_I0 | THRESHOLD |
| □ | 16 | 13 | CBLM:UNDH:1575:I0 | CBLM_UNDH_1575_I0 | THRESHOLD |
| □ | 16 | 14 | CBLM:UNDH:1675:I0 | CBLM_UNDH_1675_I0 | THRESHOLD |

# 35 Link Node 140

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:UNDH:MP05:HEARTBEAT | SIOC_UNDH_MP05_HEARTBEAT | STATE |
| □ | 6 | 6 | BPMS:UNDH:3590:ORBIT | BPMS_UNDH_3590_ORBIT | STATUS |
| □ | 6 | 10 | BPMS:UNDH:3190:ORBIT | BPMS_UNDH_3190_ORBIT | STATUS |
| □ | 6 | 16 | BPMS:UNDH:3390:ORBIT | BPMS_UNDH_3390_ORBIT | STATUS |
| □ | 16 | 1 | CBLM:UNDH:2975:I1 | CBLM_UNDH_2975_I1 | THRESHOLD |
| □ | 16 | 2 | CBLM:UNDH:3075:I1 | CBLM_UNDH_3075_I1 | THRESHOLD |
| □ | 16 | 3 | CBLM:UNDH:3175:I1 | CBLM_UNDH_3175_I1 | THRESHOLD |
| □ | 16 | 4 | CBLM:UNDH:3275:I1 | CBLM_UNDH_3275_I1 | THRESHOLD |
| □ | 16 | 9 | CBLM:UNDH:2775:I0 | CBLM_UNDH_2775_I0 | THRESHOLD |
| □ | 16 | 10 | CBLM:UNDH:2875:I0 | CBLM_UNDH_2875_I0 | THRESHOLD |
| □ | 16 | 11 | CBLM:UNDH:2975:I0 | CBLM_UNDH_2975_I0 | THRESHOLD |
| □ | 16 | 12 | CBLM:UNDH:3075:I0 | CBLM_UNDH_3075_I0 | THRESHOLD |
| □ | 16 | 13 | CBLM:UNDH:3175:I0 | CBLM_UNDH_3175_I0 | THRESHOLD |
| □ | 16 | 14 | CBLM:UNDH:3275:I0 | CBLM_UNDH_3275_I0 | THRESHOLD |
| □ | 16 | 15 | CBLM:UNDH:2775:I1 | CBLM_UNDH_2775_I1 | THRESHOLD |
| □ | 16 | 16 | CBLM:UNDH:2875:I1 | CBLM_UNDH_2875_I1 | THRESHOLD |

# 36 Link Node 141

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:UNDH:MP06:HEARTBEAT | SIOC_UNDH_MP06_HEARTBEAT | STATE |
| □ | 2 | 10 | SIOC:UNDH:MP07:HEARTBEAT | SIOC_UNDH_MP07_HEARTBEAT | STATE |
| □ | 3 | 1 | OTRS:DMPH:695:IN_LIMIT | OTRS_DMPH_695_IN_LIMIT | POSITION |
| □ | 3 | 2 | OTRS:DMPH:695:OUT_LIMIT | OTRS_DMPH_695_OUT_LIMIT | POSITION |
| □ | 3 | 4 | BEND:DMPH:395:ON | BEND_DMPH_395_ON | STATE |
| □ | 3 | 5 | BEND:DMPH:395:OFF | BEND_DMPH_395_OFF | STATE |
| □ | 3 | 9 | VVMG:UNDH:2844:POSITION | VVMG_UNDH_2844_POSITION | STATE |
| □ | 3 | 10 | VVPG:DMPH:355:POSITION | VVPG_DMPH_355_POSITION | STATE |
| □ | 3 | 11 | DUMP:LTUH:970:FLOW_SW | DUMP_LTUH_970_FLOW_SW | STATE |
| □ | 4 | 1 | VVPG:UNDH:3798:POSITION | VVPG_UNDH_3798_POSITION | STATE |
| □ | 4 | 2 | VVPG:UNDH:4598:POSITION | VVPG_UNDH_4598_POSITION | STATE |
| □ | 4 | 3 | VVPG:UNDH:5196:POSITION | VVPG_UNDH_5196_POSITION | STATE |
| □ | 4 | 4 | VVMG:UNDH:3298:POSITION | VVMG_UNDH_3298_POSITION | STATE |
| □ | 4 | 5 | VVMG:UNDH:3598:POSITION | VVMG_UNDH_3598_POSITION | STATE |
| □ | 4 | 6 | VVMG:UNDH:4098:POSITION | VVMG_UNDH_4098_POSITION | STATE |
| □ | 4 | 7 | VVMG:UNDH:4398:POSITION | VVMG_UNDH_4398_POSITION | STATE |
| □ | 4 | 8 | VVMG:UNDH:2838:POSITION | VVMG_UNDH_2838_POSITION | STATE |
| □ | 4 | 9 | VVFS:DMPH:157:POSITION | VVFS_DMPH_157_POSITION | STATE |
| □ | 4 | 11 | VVPG:DMPH:396:POSITION | VVPG_DMPH_396_POSITION | STATE |
| □ | 4 | 13 | VVMG:DMPH:643:POSITION | VVMG_DMPH_643_POSITION | STATE |
| □ | 4 | 14 | VVMG:DMPH:681:POSITION | VVMG_DMPH_681_POSITION | STATE |
| □ | 4 | 15 | VVPG:UNDH:2798:POSITION | VVPG_UNDH_2798_POSITION | STATE |
| □ | 4 | 16 | VVPG:UNDH:2998:POSITION | VVPG_UNDH_2998_POSITION | STATE |
| □ | 6 | 6 | BPMS:UNDH:3090:ORBIT | BPMS_UNDH_3090_ORBIT | STATUS |
| □ | 6 | 16 | BPMS:UNDH:2890:ORBIT | BPMS_UNDH_2890_ORBIT | STATUS |
| □ | 16 | 9 | CBLM:UNDH:4575:I0 | CBLM_UNDH_4575_I0 | THRESHOLD |
| □ | 16 | 10 | CBLM:UNDH:4675:I0 | CBLM_UNDH_4675_I0 | THRESHOLD |
| □ | 16 | 11 | BEND:DMPH:395:CURRENT | BEND_DMPH_395_CURRENT | THRESHOLD |
| □ | 16 | 15 | CBLM:UNDH:4575:I1 | CBLM_UNDH_4575_I1 | THRESHOLD |
| □ | 16 | 16 | CBLM:UNDH:4675:I1 | CBLM_UNDH_4675_I1 | THRESHOLD |

# 37 Link Node 143

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:UNDH:MP08:HEARTBEAT | SIOC_UNDH_MP08_HEARTBEAT | STATE |
| □ | 6 | 10 | BPMS:UNDH:4490:ORBIT | BPMS_UNDH_4490_ORBIT | STATUS |
| □ | 6 | 16 | BPMS:UNDH:4690:ORBIT | BPMS_UNDH_4690_ORBIT | STATUS |
| □ | 16 | 1 | CBLM:UNDH:3575:I1 | CBLM_UNDH_3575_I1 | THRESHOLD |
| □ | 16 | 2 | CBLM:UNDH:3675:I1 | CBLM_UNDH_3675_I1 | THRESHOLD |
| □ | 16 | 3 | CBLM:UNDH:3775:I1 | CBLM_UNDH_3775_I1 | THRESHOLD |
| □ | 16 | 4 | CBLM:UNDH:3875:I1 | CBLM_UNDH_3875_I1 | THRESHOLD |
| □ | 16 | 9 | CBLM:UNDH:3375:I0 | CBLM_UNDH_3375_I0 | THRESHOLD |
| □ | 16 | 10 | CBLM:UNDH:3475:I0 | CBLM_UNDH_3475_I0 | THRESHOLD |
| □ | 16 | 11 | CBLM:UNDH:3575:I0 | CBLM_UNDH_3575_I0 | THRESHOLD |
| □ | 16 | 12 | CBLM:UNDH:3675:I0 | CBLM_UNDH_3675_I0 | THRESHOLD |
| □ | 16 | 13 | CBLM:UNDH:3775:I0 | CBLM_UNDH_3775_I0 | THRESHOLD |
| □ | 16 | 14 | CBLM:UNDH:3875:I0 | CBLM_UNDH_3875_I0 | THRESHOLD |
| □ | 16 | 15 | CBLM:UNDH:3375:I1 | CBLM_UNDH_3375_I1 | THRESHOLD |
| □ | 16 | 16 | CBLM:UNDH:3475:I1 | CBLM_UNDH_3475_I1 | THRESHOLD |

# 38 Link Node 144

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:UNDH:MP09:HEARTBEAT | SIOC_UNDH_MP09_HEARTBEAT | STATE |
| □ | 5 | 12 | BPMS:UNDH:4290:ORBIT | BPMS_UNDH_4290_ORBIT | STATUS |
| □ | 6 | 6 | BPMS:UNDH:4090:ORBIT | BPMS_UNDH_4090_ORBIT | STATUS |
| □ | 6 | 10 | BPMS:UNDH:3690:ORBIT | BPMS_UNDH_3690_ORBIT | STATUS |
| □ | 6 | 16 | BPMS:UNDH:3890:ORBIT | BPMS_UNDH_3890_ORBIT | STATUS |
| □ | 16 | 1 | CBLM:UNDH:4175:I1 | CBLM_UNDH_4175_I1 | THRESHOLD |
| □ | 16 | 2 | CBLM:UNDH:4275:I1 | CBLM_UNDH_4275_I1 | THRESHOLD |
| □ | 16 | 3 | CBLM:UNDH:4375:I1 | CBLM_UNDH_4375_I1 | THRESHOLD |
| □ | 16 | 4 | CBLM:UNDH:4475:I1 | CBLM_UNDH_4475_I1 | THRESHOLD |
| □ | 16 | 9 | CBLM:UNDH:3975:I0 | CBLM_UNDH_3975_I0 | THRESHOLD |
| □ | 16 | 10 | CBLM:UNDH:4075:I0 | CBLM_UNDH_4075_I0 | THRESHOLD |
| □ | 16 | 11 | CBLM:UNDH:4175:I0 | CBLM_UNDH_4175_I0 | THRESHOLD |
| □ | 16 | 12 | CBLM:UNDH:4275:I0 | CBLM_UNDH_4275_I0 | THRESHOLD |
| □ | 16 | 13 | CBLM:UNDH:4375:I0 | CBLM_UNDH_4375_I0 | THRESHOLD |
| □ | 16 | 14 | CBLM:UNDH:4475:I0 | CBLM_UNDH_4475_I0 | THRESHOLD |
| □ | 16 | 15 | CBLM:UNDH:3975:I1 | CBLM_UNDH_3975_I1 | THRESHOLD |
| □ | 16 | 16 | CBLM:UNDH:4075:I1 | CBLM_UNDH_4075_I1 | THRESHOLD |

# 39 Link Node 145

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 3 | 1 | MPLN:FEEH:1:TXIFASTFLT | MPLN_FEEH_1_TXIFASTFLT | STATE |
| □ | 3 | 2 | MPLN:FEEH:2:TXIFASTFLT | MPLN_FEEH_2_TXIFASTFLT | STATE |
| □ | 3 | 3 | VVFS:FEEH:1:POSITION | VVFS_FEEH_1_POSITION | STATE |
| □ | 3 | 4 | MPLN:FEEH:1:XRTHOMSFSTFLT | MPLN_FEEH_1_XRTHOMSFSTFLT | STATE |
| □ | 3 | 9 | MPLN:FEEH:1:VACFASTFLT | MPLN_FEEH_1_VACFASTFLT | STATE |
| □ | 3 | 10 | MPLN:FEEH:2:VACFASTFLT | MPLN_FEEH_2_VACFASTFLT | STATE |
| □ | 3 | 11 | MPLN:FEEH:1:MCFASTFLT | MPLN_FEEH_1_MCFASTFLT | STATE |
| □ | 3 | 12 | MPLN:FEEH:2:MCFASTFLT | MPLN_FEEH_2_MCFASTFLT | STATE |
| □ | 3 | 13 | MPLN:FEEH:1:OPTFASTFLT | MPLN_FEEH_1_OPTFASTFLT | STATE |
| □ | 3 | 14 | MPLN:FEEH:2:OPTFASTFLT | MPLN_FEEH_2_OPTFASTFLT | STATE |
| □ | 3 | 15 | MPLN:FEEH:1:GASFASTFLT | MPLN_FEEH_1_GASFASTFLT | STATE |
| □ | 3 | 16 | MPLN:FEEH:2:GASFASTFLT | MPLN_FEEH_2_GASFASTFLT | STATE |
| □ | 4 | 1 | MPLN:FEEH:1:RATEREQ | MPLN_FEEH_1_RATEREQ | STATE |
| □ | 4 | 2 | MPLN:FEEH:10:RATEREQ | MPLN_FEEH_10_RATEREQ | STATE |
| □ | 4 | 3 | MPLN:FEEH:120:RATEREQ | MPLN_FEEH_120_RATEREQ | STATE |
| □ | 4 | 4 | MPLN:FEEH:1:SPARE | MPLN_FEEH_1_SPARE | STATE |
| □ | 4 | 5 | MPLN:FEEH:2:SPARE | MPLN_FEEH_2_SPARE | STATE |
| □ | 4 | 6 | MPLN:FEEH:3:SPARE | MPLN_FEEH_3_SPARE | STATE |
| □ | 4 | 7 | MPLN:FEEH:1:FASTFLT | MPLN_FEEH_1_FASTFLT | STATE |
| □ | 4 | 8 | MPLN:FEEH:2:FASTFLT | MPLN_FEEH_2_FASTFLT | STATE |
| □ | 4 | 9 | MPLN:FEEH:1:PWRCLASS | MPLN_FEEH_1_PWRCLASS | STATE |
| □ | 4 | 10 | MPLN:FEEH:2:PWRCLASS | MPLN_FEEH_2_PWRCLASS | STATE |
| □ | 4 | 11 | MPLN:FEEH:3:PWRCLASS | MPLN_FEEH_3_PWRCLASS | STATE |
| □ | 4 | 12 | MPLN:FEEH:4:PWRCLASS | MPLN_FEEH_4_PWRCLASS | STATE |
| □ | 4 | 13 | MPLN:FEEH:5:PWRCLASS | MPLN_FEEH_5_PWRCLASS | STATE |
| □ | 4 | 14 | MPLN:FEEH:6:PWRCLASS | MPLN_FEEH_6_PWRCLASS | STATE |
| □ | 4 | 15 | MPLN:FEEH:7:PWRCLASS | MPLN_FEEH_7_PWRCLASS | STATE |
| □ | 4 | 16 | MPLN:FEEH:15:PWRCLASS | MPLN_FEEH_15_PWRCLASS | STATE |

# 40 Link Node 147

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:UNDH:MP11:HEARTBEAT | SIOC_UNDH_MP11_HEARTBEAT | STATE |
| □ | 2 | 11 | BPMS:UNDH:4790:XCUHBR | BPMS_UNDH_4790_XCUHBR | ORBIT |
| □ | 2 | 12 | BPMS:UNDH:4790:YCUHBR | BPMS_UNDH_4790_YCUHBR | ORBIT |
| □ | 16 | 9 | CBLM:UNDH:4775:I0 | CBLM_UNDH_4775_I0 | THRESHOLD |
| □ | 16 | 15 | CBLM:UNDH:4775:I1 | CBLM_UNDH_4775_I1 | THRESHOLD |

# 41 Link Node 195

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 1 | BPMS:UNDS:2490:YCUSBR | BPMS_UNDS_2490_YCUSBR | ORBIT |
| □ | 2 | 2 | MPS:UNDH:1:FACMODE | MPS_UNDH_1_FACMODE | STATE |
| □ | 2 | 3 | MPS:UNDS:1:FACMODE | MPS_UNDS_1_FACMODE | STATE |
| □ | 2 | 4 | BPMS:UNDS:2690:XCUSBR | BPMS_UNDS_2690_XCUSBR | ORBIT |
| □ | 2 | 5 | BPMS:UNDS:2690:YCUSBR | BPMS_UNDS_2690_YCUSBR | ORBIT |
| □ | 2 | 6 | BPMS:UNDS:2790:XCUSBR | BPMS_UNDS_2790_XCUSBR | ORBIT |
| □ | 2 | 7 | BPMS:UNDS:2790:YCUSBR | BPMS_UNDS_2790_YCUSBR | ORBIT |
| □ | 2 | 8 | BPMS:UNDS:2890:XCUSBR | BPMS_UNDS_2890_XCUSBR | ORBIT |
| □ | 2 | 9 | SIOC:CLTS:MP01:HEARTBEAT | SIOC_CLTS_MP01_HEARTBEAT | STATE |
| □ | 2 | 10 | KICK:CLTS:185:TREF | KICK_CLTS_185_TREF | TIMING |
| □ | 2 | 11 | KICK:CLTS:185:TDES | KICK_CLTS_185_TDES | TIMING |
| □ | 2 | 12 | LASR:IN20:475:PWR | LASR_IN20_475_PWR | THRESHOLD |
| □ | 2 | 13 | BPMS:UNDS:1990:YCUSBR | BPMS_UNDS_1990_YCUSBR | ORBIT |
| □ | 2 | 14 | BPMS:UNDS:2190:XCUSBR | BPMS_UNDS_2190_XCUSBR | ORBIT |
| □ | 2 | 15 | BPMS:UNDS:2190:YCUSBR | BPMS_UNDS_2190_YCUSBR | ORBIT |
| □ | 2 | 16 | BPMS:UNDS:2490:XCUSBR | BPMS_UNDS_2490_XCUSBR | ORBIT |
| □ | 4 | 1 | BEND:CLTS:765:ON | BEND_CLTS_765_ON | STATE |
| □ | 4 | 2 | BEND:CLTS:765:OFF | BEND_CLTS_765_OFF | STATE |
| □ | 4 | 9 | VVPG:CLTS:372:POSITION | VVPG_CLTS_372_POSITION | STATE |
| □ | 4 | 11 | BEND:CLTS:280:ON | BEND_CLTS_280_ON | STATE |
| □ | 4 | 12 | BEND:CLTS:280:OFF | BEND_CLTS_280_OFF | STATE |
| □ | 4 | 13 | KICK:CLTS:185:ON | KICK_CLTS_185_ON | STATE |
| □ | 4 | 14 | KICK:CLTS:185:OFF | KICK_CLTS_185_OFF | STATE |
| □ | 4 | 15 | BEND:CLTS:180:ON | BEND_CLTS_180_ON | STATE |
| □ | 4 | 16 | BEND:CLTS:180:OFF | BEND_CLTS_180_OFF | STATE |
| □ | 16 | 9 | KICK:CLTS:185:CURRENT | KICK_CLTS_185_CURRENT | THRESHOLD |
| □ | 16 | 10 | BEND:CLTS:280:CURRENT | BEND_CLTS_280_CURRENT | THRESHOLD |
| □ | 16 | 11 | BEND:CLTS:180:CURRENT | BEND_CLTS_180_CURRENT | THRESHOLD |
| □ | 16 | 12 | BEND:CLTS:765:CURRENT | BEND_CLTS_765_CURRENT | THRESHOLD |

# 42 Link Node 199

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 1 | BPMS:UNDS:3190:XCUSBR | BPMS_UNDS_3190_XCUSBR | ORBIT |
| □ | 2 | 2 | BPMS:UNDS:3190:YCUSBR | BPMS_UNDS_3190_YCUSBR | ORBIT |
| □ | 2 | 3 | BPMS:UNDS:3290:XCUSBR | BPMS_UNDS_3290_XCUSBR | ORBIT |
| □ | 2 | 4 | BPMS:UNDS:3290:YCUSBR | BPMS_UNDS_3290_YCUSBR | ORBIT |
| □ | 2 | 5 | BPMS:UNDS:3390:XCUSBR | BPMS_UNDS_3390_XCUSBR | ORBIT |
| □ | 2 | 6 | BPMS:UNDS:3390:YCUSBR | BPMS_UNDS_3390_YCUSBR | ORBIT |
| □ | 2 | 7 | BPMS:UNDS:3490:XCUSBR | BPMS_UNDS_3490_XCUSBR | ORBIT |
| □ | 2 | 8 | BPMS:UNDS:3490:YCUSBR | BPMS_UNDS_3490_YCUSBR | ORBIT |
| □ | 2 | 9 | SIOC:BSYS:MP01:HEARTBEAT | SIOC_BSYS_MP01_HEARTBEAT | STATE |
| □ | 2 | 10 | SIOC:BSYS:MP02:HEARTBEAT | SIOC_BSYS_MP02_HEARTBEAT | STATE |
| □ | 2 | 11 | SIOC:BSYS:MP03:HEARTBEAT | SIOC_BSYS_MP03_HEARTBEAT | STATE |
| □ | 2 | 12 | BPMS:UNDS:2890:YCUSBR | BPMS_UNDS_2890_YCUSBR | ORBIT |
| □ | 2 | 13 | BPMS:UNDS:2990:XCUSBR | BPMS_UNDS_2990_XCUSBR | ORBIT |
| □ | 2 | 14 | BPMS:UNDS:2990:YCUSBR | BPMS_UNDS_2990_YCUSBR | ORBIT |
| □ | 2 | 15 | BPMS:UNDS:3090:XCUSBR | BPMS_UNDS_3090_XCUSBR | ORBIT |
| □ | 2 | 16 | BPMS:UNDS:3090:YCUSBR | BPMS_UNDS_3090_YCUSBR | ORBIT |
| □ | 4 | 9 | VVFS:LTUS:109:POSITION | VVFS_LTUS_109_POSITION | STATE |
| □ | 4 | 10 | VVPG:LTUS:108:POSITION | VVPG_LTUS_108_POSITION | STATE |
| □ | 4 | 11 | VVPG:LTUS:272:POSITION | VVPG_LTUS_272_POSITION | STATE |
| □ | 4 | 12 | VVMG:LTUS:185:POSITION | VVMG_LTUS_185_POSITION | STATE |
| □ | 4 | 13 | VVMG:LTUS:348:POSITION | VVMG_LTUS_348_POSITION | STATE |
| □ | 4 | 14 | VVPG:BSYS:904:POSITION | VVPG_BSYS_904_POSITION | STATE |
| □ | 4 | 15 | COLL:BSYS:859:TEMP | COLL_BSYS_859_TEMP | STATE |

# 43 Link Node 202

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 1 | BPMS:UNDS:3890:YCUSBR | BPMS_UNDS_3890_YCUSBR | ORBIT |
| □ | 2 | 2 | BPMS:UNDS:3990:XCUSBR | BPMS_UNDS_3990_XCUSBR | ORBIT |
| □ | 2 | 3 | BPMS:UNDS:3990:YCUSBR | BPMS_UNDS_3990_YCUSBR | ORBIT |
| □ | 2 | 4 | BPMS:UNDS:4090:XCUSBR | BPMS_UNDS_4090_XCUSBR | ORBIT |
| □ | 2 | 5 | BPMS:UNDS:4090:YCUSBR | BPMS_UNDS_4090_YCUSBR | ORBIT |
| □ | 2 | 6 | BPMS:UNDS:4190:XCUSBR | BPMS_UNDS_4190_XCUSBR | ORBIT |
| □ | 2 | 7 | BPMS:UNDS:4190:YCUSBR | BPMS_UNDS_4190_YCUSBR | ORBIT |
| □ | 2 | 8 | BPMS:UNDS:4290:XCUSBR | BPMS_UNDS_4290_XCUSBR | ORBIT |
| □ | 2 | 9 | SIOC:BSYS:MP04:HEARTBEAT | SIOC_BSYS_MP04_HEARTBEAT | STATE |
| □ | 2 | 10 | KICK:LTUS:350:TREF | KICK_LTUS_350_TREF | TIMING |
| □ | 2 | 11 | KICK:LTUS:350:TDES | KICK_LTUS_350_TDES | TIMING |
| □ | 2 | 12 | BPMS:UNDS:3690:XCUSBR | BPMS_UNDS_3690_XCUSBR | ORBIT |
| □ | 2 | 13 | BPMS:UNDS:3690:YCUSBR | BPMS_UNDS_3690_YCUSBR | ORBIT |
| □ | 2 | 14 | BPMS:UNDS:3790:XCUSBR | BPMS_UNDS_3790_XCUSBR | ORBIT |
| □ | 2 | 15 | BPMS:UNDS:3790:YCUSBR | BPMS_UNDS_3790_YCUSBR | ORBIT |
| □ | 2 | 16 | BPMS:UNDS:3890:XCUSBR | BPMS_UNDS_3890_XCUSBR | ORBIT |
| □ | 4 | 1 | COLL:LTUS:345:TEMP | COLL_LTUS_345_TEMP | STATE |
| □ | 4 | 2 | COLL:LTUS:116:TEMP | COLL_LTUS_116_TEMP | STATE |
| □ | 4 | 3 | KICK:LTUS:350:ON | KICK_LTUS_350_ON | STATE |
| □ | 4 | 4 | KICK:LTUS:350:OFF | KICK_LTUS_350_OFF | STATE |
| □ | 4 | 16 | COLL:LTUS:235:TEMP | COLL_LTUS_235_TEMP | STATE |
| □ | 16 | 9 | KICK:LTUS:350:CURRENT | KICK_LTUS_350_CURRENT | THRESHOLD |

# 44 Link Node 203

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 1 | BPMS:UNDS:4690:XCUSBR | BPMS_UNDS_4690_XCUSBR | ORBIT |
| □ | 2 | 2 | BPMS:UNDS:4690:YCUSBR | BPMS_UNDS_4690_YCUSBR | ORBIT |
| □ | 2 | 3 | BPMS:UNDS:4790:XCUSBR | BPMS_UNDS_4790_XCUSBR | ORBIT |
| □ | 2 | 4 | BPMS:UNDS:4790:YCUSBR | BPMS_UNDS_4790_YCUSBR | ORBIT |
| □ | 2 | 5 | BPMS:UNDS:2590:XCUSBR | BPMS_UNDS_2590_XCUSBR | ORBIT |
| □ | 2 | 6 | BPMS:UNDS:2590:YCUSBR | BPMS_UNDS_2590_YCUSBR | ORBIT |
| □ | 2 | 9 | SIOC:LTUS:MP01:HEARTBEAT | SIOC_LTUS_MP01_HEARTBEAT | STATE |
| □ | 2 | 10 | BPMS:UNDS:4290:YCUSBR | BPMS_UNDS_4290_YCUSBR | ORBIT |
| □ | 2 | 11 | BPMS:UNDS:4390:XCUSBR | BPMS_UNDS_4390_XCUSBR | ORBIT |
| □ | 2 | 12 | BPMS:UNDS:4390:YCUSBR | BPMS_UNDS_4390_YCUSBR | ORBIT |
| □ | 2 | 13 | BPMS:UNDS:4490:XCUSBR | BPMS_UNDS_4490_XCUSBR | ORBIT |
| □ | 2 | 14 | BPMS:UNDS:4490:YCUSBR | BPMS_UNDS_4490_YCUSBR | ORBIT |
| □ | 2 | 15 | BPMS:UNDS:4590:XCUSBR | BPMS_UNDS_4590_XCUSBR | ORBIT |
| □ | 2 | 16 | BPMS:UNDS:4590:YCUSBR | BPMS_UNDS_4590_YCUSBR | ORBIT |

# 45 Link Node 204

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:LTUS:MP02:HEARTBEAT | SIOC_LTUS_MP02_HEARTBEAT | STATE |
| □ | 2 | 10 | BPMS:UNDS:3590:XCUSBR | BPMS_UNDS_3590_XCUSBR | ORBIT |
| □ | 2 | 11 | BPMS:UNDS:3590:YCUSBR | BPMS_UNDS_3590_YCUSBR | ORBIT |
| □ | 2 | 12 | BPMS:UNDS:1690:XCUSBR | BPMS_UNDS_1690_XCUSBR | ORBIT |
| □ | 2 | 13 | BPMS:UNDS:1690:YCUSBR | BPMS_UNDS_1690_YCUSBR | ORBIT |
| □ | 2 | 14 | BPMS:UNDS:1990:XCUSBR | BPMS_UNDS_1990_XCUSBR | ORBIT |
| □ | 16 | 9 | BEND:DMPS:400:CURRENT | BEND_DMPS_400_CURRENT | THRESHOLD |
| □ | 16 | 10 | BEND:LTUS:525:CURRENT | BEND_LTUS_525_CURRENT | THRESHOLD |

# 46 Link Node 205

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:LTUS:MP03:HEARTBEAT | SIOC_LTUS_MP03_HEARTBEAT | STATE |
| □ | 3 | 9 | OTRS:LTUS:417:P1_LMTSW | OTRS_LTUS_417_P1_LMTSW | POSITION |
| □ | 3 | 10 | OTRS:LTUS:417:OUT_LMTSW | OTRS_LTUS_417_OUT_LMTSW | POSITION |
| □ | 3 | 11 | OTRS:LTUS:417:P2_LMTSW | OTRS_LTUS_417_P2_LMTSW | POSITION |
| □ | 4 | 1 | BEND:DMPS:400:ON | BEND_DMPS_400_ON | STATE |
| □ | 4 | 2 | BEND:DMPS:400:OFF | BEND_DMPS_400_OFF | STATE |
| □ | 4 | 3 | BEND:LTUS:525:ON | BEND_LTUS_525_ON | STATE |
| □ | 4 | 4 | BEND:LTUS:525:OFF | BEND_LTUS_525_OFF | STATE |
| □ | 4 | 6 | COLL:LTUS:372:TEMP | COLL_LTUS_372_TEMP | STATE |
| □ | 4 | 9 | VVPG:LTUS:442:POSITION | VVPG_LTUS_442_POSITION | STATE |
| □ | 4 | 10 | VVPG:LTUS:586:POSITION | VVPG_LTUS_586_POSITION | STATE |
| □ | 4 | 11 | VVMG:LTUS:451:POSITION | VVMG_LTUS_451_POSITION | STATE |
| □ | 4 | 12 | VVMG:LTUS:505:POSITION | VVMG_LTUS_505_POSITION | STATE |
| □ | 4 | 13 | VVMG:LTUS:552:POSITION | VVMG_LTUS_552_POSITION | STATE |
| □ | 4 | 14 | VVMG:LTUS:656:POSITION | VVMG_LTUS_656_POSITION | STATE |

# 47 Link Node 206

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:LTUS:MP04:HEARTBEAT | SIOC_LTUS_MP04_HEARTBEAT | STATE |
| □ | 2 | 10 | SIOC:LTUS:MP05:HEARTBEAT | SIOC_LTUS_MP05_HEARTBEAT | STATE |
| □ | 2 | 11 | WIGG:LTUS:724:US_IN | WIGG_LTUS_724_US_IN | STATE |
| □ | 4 | 12 | VVMG:LTUS:744:POSITION | VVMG_LTUS_744_POSITION | STATE |
| □ | 4 | 13 | VVPG:LTUS:726:POSITION | VVPG_LTUS_726_POSITION | STATE |
| □ | 4 | 14 | VVPG:LTUS:757:POSITION | VVPG_LTUS_757_POSITION | STATE |

# 48 Link Node 208

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:UNDS:MP01:HEARTBEAT | SIOC_UNDS_MP01_HEARTBEAT | STATE |
| □ | 2 | 10 | MPS:UNDS:1:SXRSS_GUARDIAN_ON | MPS_UNDS_1_SXRSS_GUARDIAN_ON | STATE |
| □ | 2 | 11 | MPS:UNDS:1:SXRSS_GUARDIAN_OK | MPS_UNDS_1_SXRSS_GUARDIAN_OK | STATE |
| □ | 2 | 12 | MPS:UNDS:1:CBLM_RMON_STATE | MPS_UNDS_1_CBLM_RMON_STATE | STATE |
| □ | 2 | 13 | MPS:UNDS:1:CBLM_WDOG_STATE | MPS_UNDS_1_CBLM_WDOG_STATE | STATE |
| □ | 3 | 9 | MIRR:UNDS:3520:IN_LIMIT | MIRR_UNDS_3520_IN_LIMIT | POSITION |
| □ | 3 | 10 | MIRR:UNDS:3520:OUT_LIMIT | MIRR_UNDS_3520_OUT_LIMIT | POSITION |
| □ | 4 | 1 | BEND:UNDS:3510:ON | BEND_UNDS_3510_ON | STATE |
| □ | 4 | 2 | BEND:UNDS:3510:OFF | BEND_UNDS_3510_OFF | STATE |
| □ | 4 | 3 | MIRR:UNDS:3560:IN_LIMIT | MIRR_UNDS_3560_IN_LIMIT | POSITION |
| □ | 4 | 4 | MIRR:UNDS:3560:OUT_LIMIT | MIRR_UNDS_3560_OUT_LIMIT | POSITION |
| □ | 4 | 5 | MIRR:UNDS:3565:IN_LIMIT | MIRR_UNDS_3565_IN_LIMIT | POSITION |
| □ | 4 | 6 | MIRR:UNDS:3565:OUT_LIMIT | MIRR_UNDS_3565_OUT_LIMIT | POSITION |
| □ | 4 | 7 | SLIT:UNDS:3555:IN_LIMIT | SLIT_UNDS_3555_IN_LIMIT | POSITION |
| □ | 4 | 8 | SLIT:UNDS:3555:OUT_LIMIT | SLIT_UNDS_3555_OUT_LIMIT | POSITION |
| □ | 4 | 9 | VVPG:UNDS:1651:POSITION | VVPG_UNDS_1651_POSITION | STATE |
| □ | 4 | 10 | VVPG:UNDS:2177:POSITION | VVPG_UNDS_2177_POSITION | STATE |
| □ | 4 | 11 | VVPG:UNDS:2877:POSITION | VVPG_UNDS_2877_POSITION | STATE |
| □ | 4 | 13 | DUMP:LTUS:972:IN_LIMIT | DUMP_LTUS_972_IN_LIMIT | POSITION |
| □ | 4 | 14 | DUMP:LTUS:972:OUT_LIMIT | DUMP_LTUS_972_OUT_LIMIT | POSITION |
| □ | 4 | 15 | VVMG:LTUS:802:POSITION | VVMG_LTUS_802_POSITION | STATE |
| □ | 4 | 16 | VVMG:LTUS:827:POSITION | VVMG_LTUS_827_POSITION | STATE |
| □ | 6 | 6 | BPMS:UNDS:2590:ORBIT | BPMS_UNDS_2590_ORBIT | STATUS |
| □ | 6 | 10 | BPMS:UNDS:1690:ORBIT | BPMS_UNDS_1690_ORBIT | STATUS |
| □ | 6 | 16 | BPMS:UNDS:2190:ORBIT | BPMS_UNDS_2190_ORBIT | STATUS |

# 49 Link Node 209

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 1 | WIGG:LTUS:770:US_IN | WIGG_LTUS_770_US_IN | STATE |
| □ | 2 | 2 | WIGG:LTUS:770:US_OUT | WIGG_LTUS_770_US_OUT | STATE |
| □ | 2 | 3 | WIGG:LTUS:770:DS_IN | WIGG_LTUS_770_DS_IN | STATE |
| □ | 2 | 4 | WIGG:LTUS:770:DS_OUT | WIGG_LTUS_770_DS_OUT | STATE |
| □ | 2 | 5 | WIGG:LTUS:774:US_IN | WIGG_LTUS_774_US_IN | STATE |
| □ | 2 | 6 | WIGG:LTUS:774:US_OUT | WIGG_LTUS_774_US_OUT | STATE |
| □ | 2 | 7 | WIGG:LTUS:774:DS_IN | WIGG_LTUS_774_DS_IN | STATE |
| □ | 2 | 8 | WIGG:LTUS:774:DS_OUT | WIGG_LTUS_774_DS_OUT | STATE |
| □ | 2 | 9 | SIOC:LTUS:MP06:HEARTBEAT | SIOC_LTUS_MP06_HEARTBEAT | STATE |
| □ | 2 | 10 | WIGG:LTUS:724:US_OUT | WIGG_LTUS_724_US_OUT | STATE |
| □ | 2 | 11 | WIGG:LTUS:724:DS_IN | WIGG_LTUS_724_DS_IN | STATE |
| □ | 2 | 12 | WIGG:LTUS:724:DS_OUT | WIGG_LTUS_724_DS_OUT | STATE |
| □ | 2 | 13 | WIGG:LTUS:726:US_IN | WIGG_LTUS_726_US_IN | STATE |
| □ | 2 | 14 | WIGG:LTUS:726:US_OUT | WIGG_LTUS_726_US_OUT | STATE |
| □ | 2 | 15 | WIGG:LTUS:726:DS_IN | WIGG_LTUS_726_DS_IN | STATE |
| □ | 2 | 16 | WIGG:LTUS:726:DS_OUT | WIGG_LTUS_726_DS_OUT | STATE |

# 50 Link Node 210

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:UNDS:MP02:HEARTBEAT | SIOC_UNDS_MP02_HEARTBEAT | STATE |
| □ | 2 | 10 | SIOC:UNDS:MP03:HEARTBEAT | SIOC_UNDS_MP03_HEARTBEAT | STATE |
| □ | 2 | 11 | SIOC:UNDS:MP04:HEARTBEAT | SIOC_UNDS_MP04_HEARTBEAT | STATE |
| □ | 6 | 6 | BPMS:UNDS:3990:ORBIT | BPMS_UNDS_3990_ORBIT | STATUS |
| □ | 6 | 10 | BPMS:UNDS:3590:ORBIT | BPMS_UNDS_3590_ORBIT | STATUS |
| □ | 6 | 16 | BPMS:UNDS:3790:ORBIT | BPMS_UNDS_3790_ORBIT | STATUS |
| □ | 16 | 1 | CBLM:UNDS:2810:I1 | CBLM_UNDS_2810_I1 | THRESHOLD |
| □ | 16 | 2 | CBLM:UNDS:2910:I1 | CBLM_UNDS_2910_I1 | THRESHOLD |
| □ | 16 | 3 | CBLM:UNDS:3010:I1 | CBLM_UNDS_3010_I1 | THRESHOLD |
| □ | 16 | 4 | CBLM:UNDS:3110:I1 | CBLM_UNDS_3110_I1 | THRESHOLD |
| □ | 16 | 9 | CBLM:UNDS:2610:I0 | CBLM_UNDS_2610_I0 | THRESHOLD |
| □ | 16 | 10 | CBLM:UNDS:2710:I0 | CBLM_UNDS_2710_I0 | THRESHOLD |
| □ | 16 | 11 | CBLM:UNDS:2810:I0 | CBLM_UNDS_2810_I0 | THRESHOLD |
| □ | 16 | 12 | CBLM:UNDS:2910:I0 | CBLM_UNDS_2910_I0 | THRESHOLD |
| □ | 16 | 13 | CBLM:UNDS:3010:I0 | CBLM_UNDS_3010_I0 | THRESHOLD |
| □ | 16 | 14 | CBLM:UNDS:3110:I0 | CBLM_UNDS_3110_I0 | THRESHOLD |
| □ | 16 | 15 | CBLM:UNDS:2610:I1 | CBLM_UNDS_2610_I1 | THRESHOLD |
| □ | 16 | 16 | CBLM:UNDS:2710:I1 | CBLM_UNDS_2710_I1 | THRESHOLD |

# 51 Link Node 213

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 4 | DUMP:LTUS:972:MPSPOWER | DUMP_LTUS_972_MPSPOWER | THRESHOLD |
| □ | 2 | 9 | SIOC:UNDS:MP05:HEARTBEAT | SIOC_UNDS_MP05_HEARTBEAT | STATE |
| □ | 2 | 10 | MPS:UNDS:3500:SXRSS_WDOG | MPS_UNDS_3500_SXRSS_WDOG | STATE |
| □ | 2 | 11 | MPS:UNDS:3500:SX_HRMNC_ENERGY | MPS_UNDS_3500_SX_HRMNC_ENERGY | STATE |
| □ | 2 | 12 | MPS:UNDS:3500:SX_SS_ENERGY | MPS_UNDS_3500_SX_SS_ENERGY | STATE |
| □ | 2 | 13 | MPS:UNDS:3500:SX_HRMNC_X | MPS_UNDS_3500_SX_HRMNC_X | STATE |
| □ | 2 | 14 | MPS:UNDS:3500:SX_SS_X | MPS_UNDS_3500_SX_SS_X | STATE |
| □ | 2 | 15 | MPS:UNDS:3500:SX_SASE_X | MPS_UNDS_3500_SX_SASE_X | STATE |
| □ | 3 | 1 | VVMG:UNDS:4998:POSITION | VVMG_UNDS_4998_POSITION | STATE |
| □ | 3 | 2 | MOVR:UNDS:5013:IN_LMTSW | MOVR_UNDS_5013_IN_LMTSW | POSITION |
| □ | 3 | 3 | MOVR:UNDS:5013:OUT_LMTSW | MOVR_UNDS_5013_OUT_LMTSW | POSITION |
| □ | 3 | 9 | BEND:DMPS:395:ON | BEND_DMPS_395_ON | STATE |
| □ | 3 | 10 | BEND:DMPS:395:OFF | BEND_DMPS_395_OFF | STATE |
| □ | 3 | 11 | VVMG:UNDS:3511:POSITION | VVMG_UNDS_3511_POSITION | STATE |
| □ | 3 | 12 | VVMG:UNDS:3568:POSITION | VVMG_UNDS_3568_POSITION | STATE |
| □ | 3 | 13 | YAGS:UNDS:3575:IN_LIMIT | YAGS_UNDS_3575_IN_LIMIT | POSITION |
| □ | 3 | 14 | YAGS:UNDS:3575:OUT_LIMIT | YAGS_UNDS_3575_OUT_LIMIT | POSITION |
| □ | 3 | 15 | YAGS:UNDS:3795:IN_LIMIT | YAGS_UNDS_3795_IN_LIMIT | POSITION |
| □ | 3 | 16 | YAGS:UNDS:3795:OUT_LIMIT | YAGS_UNDS_3795_OUT_LIMIT | POSITION |
| □ | 4 | 1 | VVPG:UNDS:3877:POSITION | VVPG_UNDS_3877_POSITION | STATE |
| □ | 4 | 2 | VVPG:UNDS:4477:POSITION | VVPG_UNDS_4477_POSITION | STATE |
| □ | 4 | 3 | VVPG:UNDS:5198:POSITION | VVPG_UNDS_5198_POSITION | STATE |
| □ | 4 | 6 | OTRS:DMPS:695:IN_LIMIT | OTRS_DMPS_695_IN_LIMIT | POSITION |
| □ | 4 | 7 | OTRS:DMPS:695:OUT_LIMIT | OTRS_DMPS_695_OUT_LIMIT | POSITION |
| □ | 4 | 8 | VVPG:DMPS:355:POSITION | VVPG_DMPS_355_POSITION | STATE |
| □ | 4 | 9 | VVFS:DMPS:157:POSITION | VVFS_DMPS_157_POSITION | STATE |
| □ | 4 | 11 | VVPG:DMPS:396:POSITION | VVPG_DMPS_396_POSITION | STATE |
| □ | 4 | 13 | VVMG:DMPS:643:POSITION | VVMG_DMPS_643_POSITION | STATE |
| □ | 4 | 14 | VVMG:DMPS:681:POSITION | VVMG_DMPS_681_POSITION | STATE |
| □ | 4 | 15 | VVPG:UNDS:3477:POSITION | VVPG_UNDS_3477_POSITION | STATE |
| □ | 4 | 16 | VVPG:UNDS:3577:POSITION | VVPG_UNDS_3577_POSITION | STATE |
| □ | 5 | 12 | BPMS:UNDS:3390:ORBIT | BPMS_UNDS_3390_ORBIT | STATUS |
| □ | 6 | 6 | BPMS:UNDS:3190:ORBIT | BPMS_UNDS_3190_ORBIT | STATUS |
| □ | 6 | 10 | BPMS:UNDS:2790:ORBIT | BPMS_UNDS_2790_ORBIT | STATUS |
| □ | 6 | 16 | BPMS:UNDS:2990:ORBIT | BPMS_UNDS_2990_ORBIT | STATUS |
| □ | 16 | 1 | CBLM:UNDS:3510:I1 | CBLM_UNDS_3510_I1 | THRESHOLD |
| □ | 16 | 2 | CBLM:UNDS:3610:I1 | CBLM_UNDS_3610_I1 | THRESHOLD |
| □ | 16 | 3 | CBLM:UNDS:3710:I1 | CBLM_UNDS_3710_I1 | THRESHOLD |

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| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 16 | 4 | CBLM:UNDS:3810:I1 | CBLM_UNDS_3810_I1 | THRESHOLD |
| □ | 16 | 10 | CBLM:UNDS:3410:I0 | CBLM_UNDS_3410_I0 | THRESHOLD |
| □ | 16 | 11 | CBLM:UNDS:3510:I0 | CBLM_UNDS_3510_I0 | THRESHOLD |
| □ | 16 | 12 | CBLM:UNDS:3610:I0 | CBLM_UNDS_3610_I0 | THRESHOLD |
| □ | 16 | 13 | CBLM:UNDS:3710:I0 | CBLM_UNDS_3710_I0 | THRESHOLD |
| □ | 16 | 14 | CBLM:UNDS:3810:I0 | CBLM_UNDS_3810_I0 | THRESHOLD |
| □ | 16 | 16 | CBLM:UNDS:3410:I1 | CBLM_UNDS_3410_I1 | THRESHOLD |

# 52 Link Node 214

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:UNDS:MP06:HEARTBEAT | SIOC_UNDS_MP06_HEARTBEAT | STATE |
| □ | 16 | 1 | CBLM:UNDS:4110:I1 | CBLM_UNDS_4110_I1 | THRESHOLD |
| □ | 16 | 2 | CBLM:UNDS:4210:I1 | CBLM_UNDS_4210_I1 | THRESHOLD |
| □ | 16 | 3 | CBLM:UNDS:4310:I1 | CBLM_UNDS_4310_I1 | THRESHOLD |
| □ | 16 | 4 | CBLM:UNDS:4410:I1 | CBLM_UNDS_4410_I1 | THRESHOLD |
| □ | 16 | 9 | CBLM:UNDS:3910:I0 | CBLM_UNDS_3910_I0 | THRESHOLD |
| □ | 16 | 10 | CBLM:UNDS:4010:I0 | CBLM_UNDS_4010_I0 | THRESHOLD |
| □ | 16 | 11 | CBLM:UNDS:4110:I0 | CBLM_UNDS_4110_I0 | THRESHOLD |
| □ | 16 | 12 | CBLM:UNDS:4210:I0 | CBLM_UNDS_4210_I0 | THRESHOLD |
| □ | 16 | 13 | CBLM:UNDS:4310:I0 | CBLM_UNDS_4310_I0 | THRESHOLD |
| □ | 16 | 14 | CBLM:UNDS:4410:I0 | CBLM_UNDS_4410_I0 | THRESHOLD |
| □ | 16 | 15 | CBLM:UNDS:3910:I1 | CBLM_UNDS_3910_I1 | THRESHOLD |
| □ | 16 | 16 | CBLM:UNDS:4010:I1 | CBLM_UNDS_4010_I1 | THRESHOLD |

# 53 Link Node 215

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | SIOC:UNDS:MP07:HEARTBEAT | SIOC_UNDS_MP07_HEARTBEAT | STATE |
| □ | 5 | 12 | BPMS:UNDS:4690:ORBIT | BPMS_UNDS_4690_ORBIT | STATUS |
| □ | 6 | 6 | BPMS:UNDS:4490:ORBIT | BPMS_UNDS_4490_ORBIT | STATUS |
| □ | 6 | 10 | BPMS:UNDS:4090:ORBIT | BPMS_UNDS_4090_ORBIT | STATUS |
| □ | 6 | 16 | BPMS:UNDS:4290:ORBIT | BPMS_UNDS_4290_ORBIT | STATUS |
| □ | 16 | 1 | CBLM:UNDS:4710:I1 | CBLM_UNDS_4710_I1 | THRESHOLD |
| □ | 16 | 2 | CBLM:UNDS:3310:I1 | CBLM_UNDS_3310_I1 | THRESHOLD |
| □ | 16 | 3 | CBLM:UNDS:4810:I1 | CBLM_UNDS_4810_I1 | THRESHOLD |
| □ | 16 | 4 | CBLM:UNDS:3210:I1 | CBLM_UNDS_3210_I1 | THRESHOLD |
| □ | 16 | 9 | CBLM:UNDS:4510:I0 | CBLM_UNDS_4510_I0 | THRESHOLD |
| □ | 16 | 10 | CBLM:UNDS:4610:I0 | CBLM_UNDS_4610_I0 | THRESHOLD |
| □ | 16 | 11 | CBLM:UNDS:4710:I0 | CBLM_UNDS_4710_I0 | THRESHOLD |
| □ | 16 | 12 | CBLM:UNDS:3310:I0 | CBLM_UNDS_3310_I0 | THRESHOLD |
| □ | 16 | 13 | CBLM:UNDS:4810:I0 | CBLM_UNDS_4810_I0 | THRESHOLD |
| □ | 16 | 14 | CBLM:UNDS:3210:I0 | CBLM_UNDS_3210_I0 | THRESHOLD |
| □ | 16 | 15 | CBLM:UNDS:4510:I1 | CBLM_UNDS_4510_I1 | THRESHOLD |
| □ | 16 | 16 | CBLM:UNDS:4610:I1 | CBLM_UNDS_4610_I1 | THRESHOLD |

# 54 Link Node 216

| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| □ | 2 | 9 | MPLN:FEES:1:CRIXMCFASTFLT | MPLN_FEES_1_CRIXMCFASTFLT | STATE |
| □ | 2 | 10 | MPLN:FEES:1:CRIXVACFASTFLT | MPLN_FEES_1_CRIXVACFASTFLT | STATE |
| □ | 2 | 11 | MPLN:FEES:1:QRIXMCFASTFLT | MPLN_FEES_1_QRIXMCFASTFLT | STATE |
| □ | 2 | 12 | MPLN:FEES:1:QRIXVACFASTFLT | MPLN_FEES_1_QRIXVACFASTFLT | STATE |
| □ | 2 | 13 | MPLN:FEES:1:TXIVACFASTFLT | MPLN_FEES_1_TXIVACFASTFLT | STATE |
| □ | 2 | 14 | MPLN:FEES:2:TXIVACFASTFLT | MPLN_FEES_2_TXIVACFASTFLT | STATE |
| □ | 2 | 15 | MPLN:FEES:1:TXIMCFASTFLT | MPLN_FEES_1_TXIMCFASTFLT | STATE |
| □ | 2 | 16 | MPLN:FEES:1:TXIOPTFASTFLT | MPLN_FEES_1_TXIOPTFASTFLT | STATE |
| □ | 3 | 1 | MPLN:FEES:2:RIXMCFASTFLT | MPLN_FEES_2_RIXMCFASTFLT | STATE |
| □ | 3 | 2 | MPLN:FEES:1:TMOVACFASTFLT | MPLN_FEES_1_TMOVACFASTFLT | STATE |
| □ | 3 | 3 | MPLN:FEES:2:TMOVACFASTFLT | MPLN_FEES_2_TMOVACFASTFLT | STATE |
| □ | 3 | 4 | MPLN:FEES:1:TMOMCFASTFLT | MPLN_FEES_1_TMOMCFASTFLT | STATE |
| □ | 3 | 5 | MPLN:FEES:2:TMOMCFASTFLT | MPLN_FEES_2_TMOMCFASTFLT | STATE |
| □ | 3 | 6 | MPLN:FEES:1:TMOOPTFASTFLT | MPLN_FEES_1_TMOOPTFASTFLT | STATE |
| □ | 3 | 7 | MPLN:FEES:1:RIXVACFASTFLT | MPLN_FEES_1_RIXVACFASTFLT | STATE |
| □ | 3 | 8 | MPLN:FEES:1:RIXOPTFASTFLT | MPLN_FEES_1_RIXOPTFASTFLT | STATE |
| □ | 3 | 9 | MPLN:FEES:1:VACFASTFLT | MPLN_FEES_1_VACFASTFLT | STATE |
| □ | 3 | 10 | MPLN:FEES:2:VACFASTFLT | MPLN_FEES_2_VACFASTFLT | STATE |
| □ | 3 | 11 | MPLN:FEES:1:MCFASTFLT | MPLN_FEES_1_MCFASTFLT | STATE |
| □ | 3 | 12 | MPLN:FEES:2:MCFASTFLT | MPLN_FEES_2_MCFASTFLT | STATE |
| □ | 3 | 13 | MPLN:FEES:1:GASATTNFSTFLT | MPLN_FEES_1_GASATTNFSTFLT | STATE |
| □ | 3 | 14 | MPLN:FEES:1:GMDFSTFLT | MPLN_FEES_1_GMDFSTFLT | STATE |
| □ | 3 | 15 | MPLN:FEES:1:XGMDFSTFLT | MPLN_FEES_1_XGMDFSTFLT | STATE |
| □ | 3 | 16 | MPLN:FEES:1:RIXMCFASTFLT | MPLN_FEES_1_RIXMCFASTFLT | STATE |
| □ | 4 | 1 | MPLN:FEES:1:RATEREQ | MPLN_FEES_1_RATEREQ | STATE |
| □ | 4 | 2 | MPLN:FEES:10:RATEREQ | MPLN_FEES_10_RATEREQ | STATE |
| □ | 4 | 3 | MPLN:FEES:120:RATEREQ | MPLN_FEES_120_RATEREQ | STATE |
| □ | 4 | 4 | STPR:FEES:ST1K4:IN | STPR_FEES_ST1K4_IN | STATE |
| □ | 4 | 5 | STPR:FEES:MR1K1:IN | STPR_FEES_MR1K1_IN | STATE |
| □ | 4 | 6 | STPR:FEES:ST1K2:IN | STPR_FEES_ST1K2_IN | STATE |
| □ | 4 | 7 | MPLN:FEES:1:FASTFLT | MPLN_FEES_1_FASTFLT | STATE |
| □ | 4 | 8 | MPLN:FEES:2:FASTFLT | MPLN_FEES_2_FASTFLT | STATE |
| □ | 4 | 9 | MPLN:FEES:1:PWRCLASS | MPLN_FEES_1_PWRCLASS | STATE |
| □ | 4 | 10 | MPLN:FEES:2:PWRCLASS | MPLN_FEES_2_PWRCLASS | STATE |
| □ | 4 | 11 | MPLN:FEES:3:PWRCLASS | MPLN_FEES_3_PWRCLASS | STATE |
| □ | 4 | 12 | MPLN:FEES:4:PWRCLASS | MPLN_FEES_4_PWRCLASS | STATE |
| □ | 4 | 13 | MPLN:FEES:5:PWRCLASS | MPLN_FEES_5_PWRCLASS | STATE |
| □ | 4 | 14 | MPLN:FEES:6:PWRCLASS | MPLN_FEES_6_PWRCLASS | STATE |

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| Verified | Card | Channel | PV Prefix | Device Name | Fault Name |
|---|---|---|---|---|---|
| ☐ | 4 | 15 | MPLN:FEES:7:PWRCLASS | MPLN_FEES_7_PWRCLASS | STATE |
| ☐ | 4 | 16 | MIRR:FEES:MR1K1:OUT | MIRR_FEES_MR1K1_OUT | POSITION |