MPS Link Node Inputs:
2021-05-14-a

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1 Link Node 9

Verified	Card	Channel	PV Prefix	Device Name	Fault Name
□	1	1	KICK:LTUH:320:CURRENT	KICK_LTUH_320_CURRENT	THRESHOLD
□	1	2	KICK:LTUS:350:CURRENT	KICK_LTUS_350_CURRENT	THRESHOLD
□	1	3	BEND:CLTS:180:ON	BEND_CLTS_180_ON	STATE

2 Link Node 10

Verified	Card	Channel	PV Prefix	Device Name	Fault Name
□	1	1	VVS:LI10:1:BREAKER	VVS_LI10_1_BREAKER	STATE
□	1	2	SHUT:LT10:950:OUT	SHUT_LT10_950_OUT	POSITION
□	1	3	SHUT:LT10:950:IN	SHUT_LT10_950_IN	POSITION
□	1	4	SHUT:LT10:950:FAULT	SHUT_LT10_950_FAULT	STATE
□	1	5	KLYS:LI10:21:PEAKPOWER	KLYS_LI10_21_PEAKPOWER	STATE

3 Link Node 11

Verified	Card	Channel	PV Prefix	Device Name	Fault Name
□	1	1	VVPG:IN10:155:POSITION	VVPG_IN10_155_POSITION	STATE
□	1	2	VVPG:IN10:215:POSITION	VVPG_IN10_215_POSITION	STATE
□	1	6	MKSU:LI10:61:WGVACSUM	MKSU_LI10_61_WGVACSUM	STATE
□	1	7	PICM:IN10:1:HV_PWR	PICM_IN10_1_HV_PWR	STATE
□	2	1	YAGS:IN10:241:OUT_LIMSW	YAGS_IN10_241_OUT_LIMSW	POSITION
□	2	2	YAGS:IN10:241:IN_LIMSW	YAGS_IN10_241_IN_LIMSW	POSITION
□	2	3	PROF:IN10:465:OUT_LMTSW	PROF_IN10_465_OUT_LMTSW	STATE
□	2	4	PROF:IN10:465:IN_LMTSW	PROF_IN10_465_IN_LMTSW	STATE
□	2	5	PROF:IN10:471:OUT_LMTSW	PROF_IN10_471_OUT_LMTSW	STATE
□	2	6	PROF:IN10:471:IN_LMTSW	PROF_IN10_471_IN_LMTSW	STATE
□	2	7	PROF:IN10:571:OUT_LMTSW	PROF_IN10_571_OUT_LMTSW	STATE
□	2	8	PROF:IN10:571:IN_LMTSW	PROF_IN10_571_IN_LMTSW	STATE
□	2	9	PROF:IN10:711:OUT_LMTSW	PROF_IN10_711_OUT_LMTSW	STATE
□	2	10	PROF:IN10:711:IN_LMTSW	PROF_IN10_711_IN_LMTSW	STATE
□	2	11	PROF:IN10:921:OUT_LMTSW	PROF_IN10_921_OUT_LMTSW	STATE
□	2	12	PROF:IN10:921:IN_LMTSW	PROF_IN10_921_IN_LMTSW	STATE
□	2	13	PROF:LI11:335:OUT_LMTSW	PROF_LI11_335_OUT_LMTSW	STATE
□	2	14	PROF:LI11:335:IN_LMTSW	PROF_LI11_335_IN_LMTSW	STATE
□	2	15	PROF:LI11:375:OUT_LMTSW	PROF_LI11_375_OUT_LMTSW	STATE
□	2	16	PROF:LI11:375:IN_LMTSW	PROF_LI11_375_IN_LMTSW	STATE
□	3	1	IOC:IN10:MP01:HEARTBEATSUM	IOC_IN10_MP01_HEARTBEATSUM	STATE
□	3	2	PICM:IN10:LN11:HEARTBEATSUM	PICM_IN10_LN11_HEARTBEATSUM	STATE
□	3	3	PLC:IN10:MP01:HEARTBEATSUM	PLC_IN10_MP01_HEARTBEATSUM	STATE
□	3	5	DUMP:LI11:390:OUT_LMTSW	DUMP_LI11_390_OUT_LMTSW	STATE
□	3	6	DUMP:LI11:390:IN_LMTSW	DUMP_LI11_390_IN_LMTSW	STATE
□	7	8	PICM:IN10:300:NO_TRIG	PICM_IN10_300_NO_TRIG	STATE
□	9	1	PICM:IN10:300:LOSSL	PICM_IN10_300_LOSSL	THRESHOLD
□	9	2	PICM:IN10:300:LOSSH	PICM_IN10_300_LOSSH	THRESHOLD
□	10	1	PICM:IN10:301:LOSSL	PICM_IN10_301_LOSSL	THRESHOLD
□	10	2	PICM:IN10:301:LOSSH	PICM_IN10_301_LOSSH	THRESHOLD

4 Link Node 34

Verified	Card	Channel	PV Prefix	Device Name	Fault Name
□	1	1	VVPG:LI20:3208:OPEN	VVPG_LI20_3208_OPEN	STATE
□	1	2	VVPG:LI20:3208:CLOSED	VVPG_LI20_3208_CLOSED	STATE
□	1	3	LI20:MPS:43:STPR_1IN	LI20_MPS_43_STPR_1IN	STATE
□	1	4	LI20:MPS:43:STPR_2IN	LI20_MPS_43_STPR_2IN	STATE
□	1	5	LI20:MPS:6:HLAM_ENB	LI20_MPS_6_HLAM_ENB	STATE
□	1	6	LI20:MPS:7:EKIC_ENB	LI20_MPS_7_EKIC_ENB	STATE
□	1	7	LI20:MPS:13:HV_STATS	LI20_MPS_13_HV_STATS	STATE
□	1	8	PROF:LI20:45:OUT_LMTSW	PROF_LI20_45_OUT_LMTSW	STATE
□	1	9	PROF:LI20:45:IN_LMTSW	PROF_LI20_45_IN_LMTSW	STATE
□	1	10	BLEN:LI20:3014:OUT_LMTSW	BLEN_LI20_3014_OUT_LMTSW	STATE
□	1	11	BLEN:LI20:3014:IN_LMTSW	BLEN_LI20_3014_IN_LMTSW	STATE
□	1	12	VVFS:LI20:1986:POSITION	VVFS_LI20_1986_POSITION	STATE
□	1	13	EXPT:LI20:1:STATUS	EXPT_LI20_1_STATUS	STATE
□	1	14	OTRS:LI20:3158:OUT_LMTSW	OTRS_LI20_3158_OUT_LMTSW	STATE
□	1	15	OTRS:LI20:3158:IN_LMTSW	OTRS_LI20_3158_IN_LMTSW	STATE
□	2	1	COLL:LI20:2058:KLIXON	COLL_LI20_2058_KLIXON	STATE
□	2	2	OVEN:LI20:3185:IN_LMTSW	OVEN_LI20_3185_IN_LMTSW	STATE
□	2	3	OVEN:LI20:3185:OUT_LMTSW	OVEN_LI20_3185_OUT_LMTSW	STATE
□	2	4	LI20:MPS:14:DUMPSHTR_OUT	LI20_MPS_14_DUMPSHTR_OUT	STATE
□	2	5	LI20:MPS:14:DUMPSHTR_IN	LI20_MPS_14_DUMPSHTR_IN	STATE
□	2	6	VVPG:LI20:3076:POSITION	VVPG_LI20_3076_POSITION	STATE
□	2	7	VVPG:LI20:3160:POSITION	VVPG_LI20_3160_POSITION	STATE
□	2	8	VVPG:LI20:3250:POSITION	VVPG_LI20_3250_POSITION	STATE