REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPR.
	A	INITIAL RELEASE	X	

TESTINGBED REV A

INDEX

PG01 INDEX
PG02 BLOCK DIAGRAM
PG03 NOTE
PG04 COME AB CONN[A]
PG05 COME AB CONN[B]
PG06 COME CD CONN[A]
PG07 COME CD CONN[B]
PG08 SERDES CLK BUF1
PG08 SERDES CLK BUF2
PG10 LVDS
PG11 VGA & DVI
PG12 RJ45 X2
PG13 RJ45 & USB
PG14 PCIEX2
PG15 PCIEX4 [A]
PG16 SIDE BAND [B]
PG17 PCIEX4 [B]
PG18 SIDE BAND [C]
PG19 PCIEX4 [C]
PG20 SATA & SDHC
PG21 USB CONN
PG22 USB HDR
PG23 AUDIO
PG24 SPI
PG25 ATX CONN
PG26 RESET
PG27 UART CONN
PG28 UART HDR
PG29 LED
PG30 EEPROM & BAT
PG31 CPLD
PG32 QE_HDR
PG33 SFP
PG34 MISC

EMERSON™
Network Power
EMBEDDED COMPUTING

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THIS DOCUMENT WILL BE GENERAL BUSINESS USE AFTER RELEASE.

BLOCK TITLE: blackadder

ENGINEER: WILSON SUN

LAST MODIFIED BY: BILL WANG

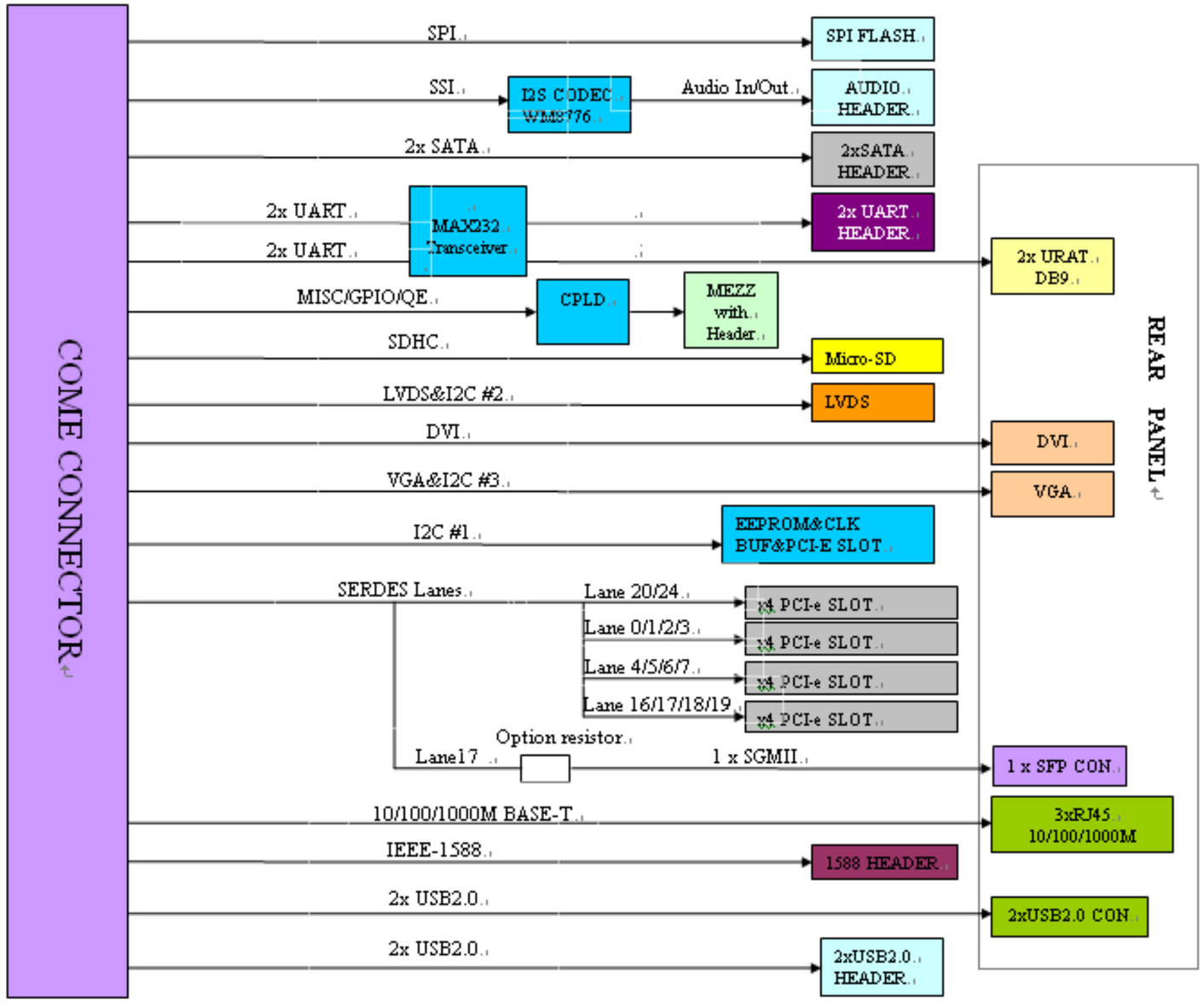
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Tue Sep 21 15:55:58 2010

REV A

PCB: 8406874H01C

BLOCK DIAGRAM



DOCUMENT NUMBER	REV
6306874H0C.cpm	A
	PAGE 2 OF 35

NOTE

SMBUS ADDRESS

I2C

SFP	E2ROM 1010,110
	PHY 1010,000
E2ROM	1010,100
WM8776	0011,010
CLKBUF	1101,110

DDI1	I2C	E2ROM	1010,000
CRT	I2C	E2ROM	1010,101
LVDS	I2C	E2ROM	1010,111

JMP SETTING

P5 LVDS PWR1 SEL

1-2	3.3V(DEFAULT)
2-3	5V

P12 SPI CS SEL

1-2	CS0(DEFAULT)
2-3	CS1

P6 LVDS PWR2 SEL

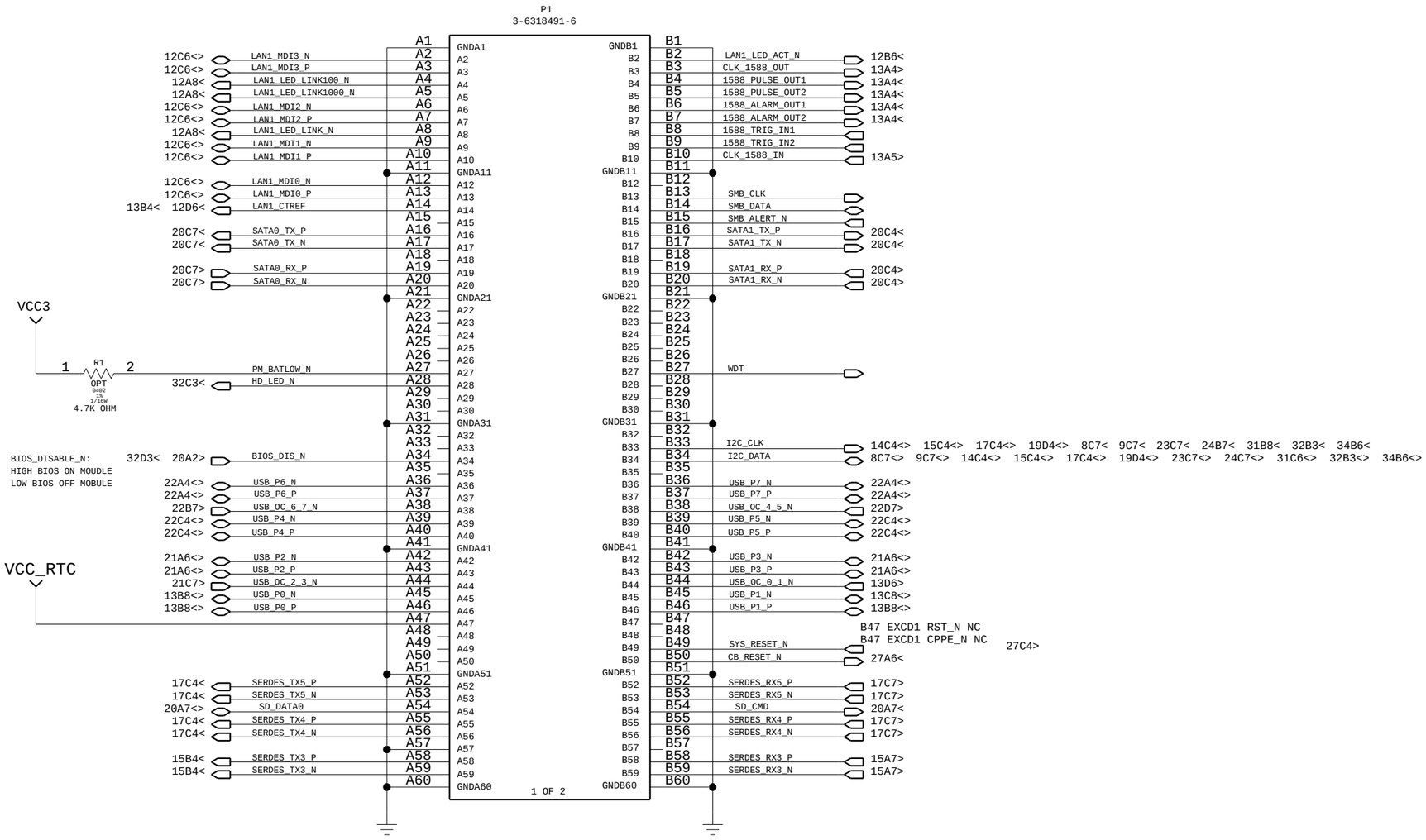
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2-3	NC

P13 BIOS DISABLE

1-2	3.3V(DEFAULT)
2-3	BIOS DIS

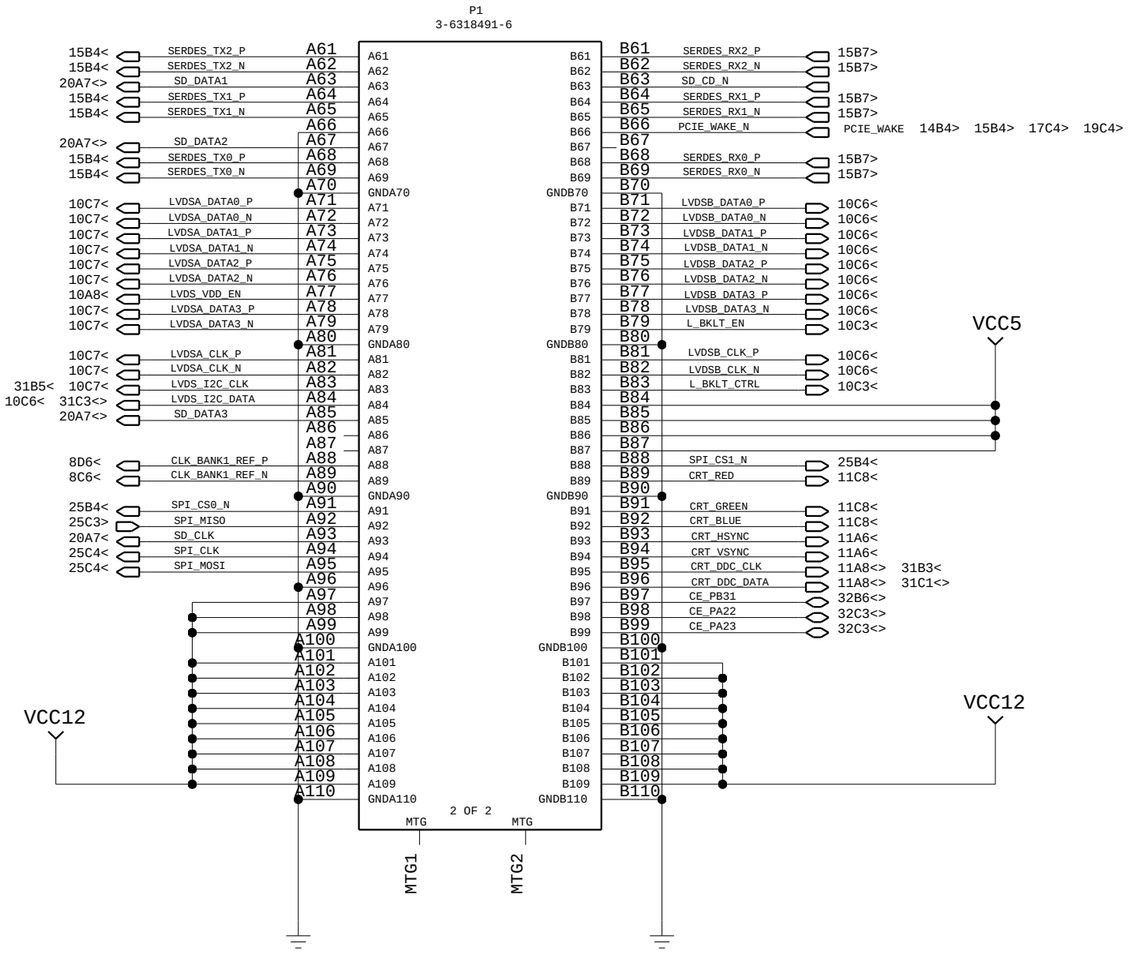
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	PAGE 3 OF 35

AB CONNECTOR



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	PAGE 4 OF 35

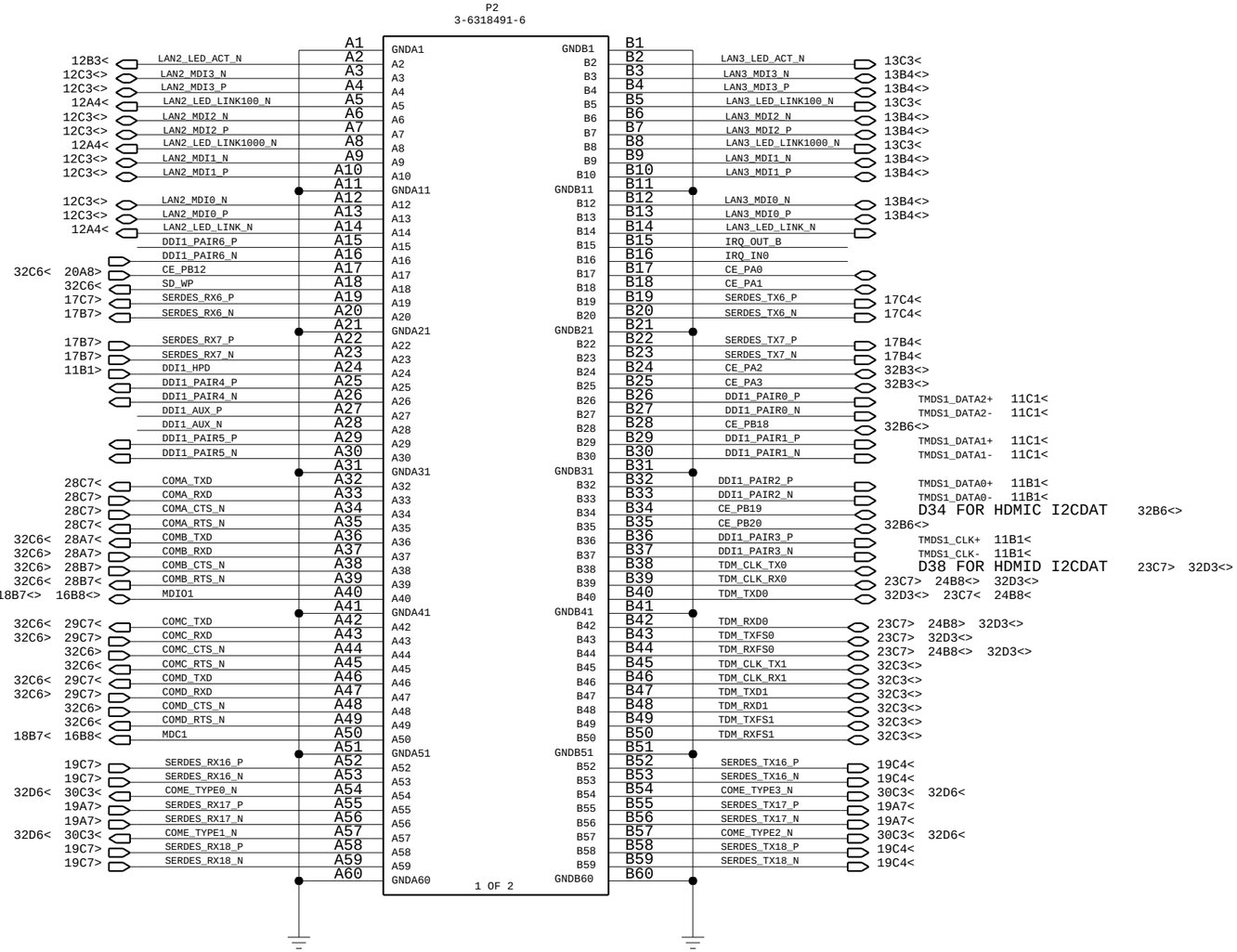
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VCC12 10A TO COME CONN

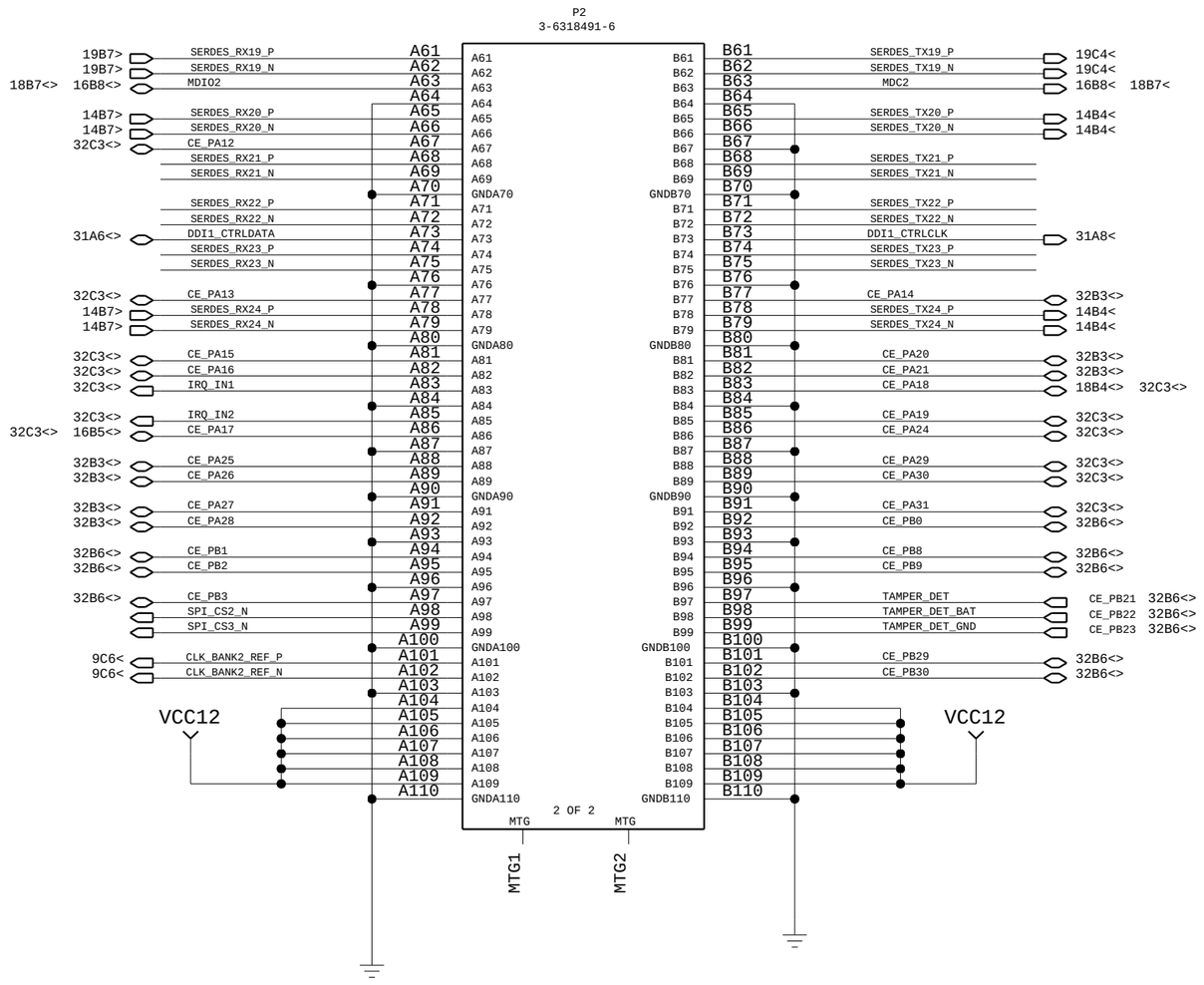
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	PAGE 5 OF 35

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	PAGE 6 OF 35

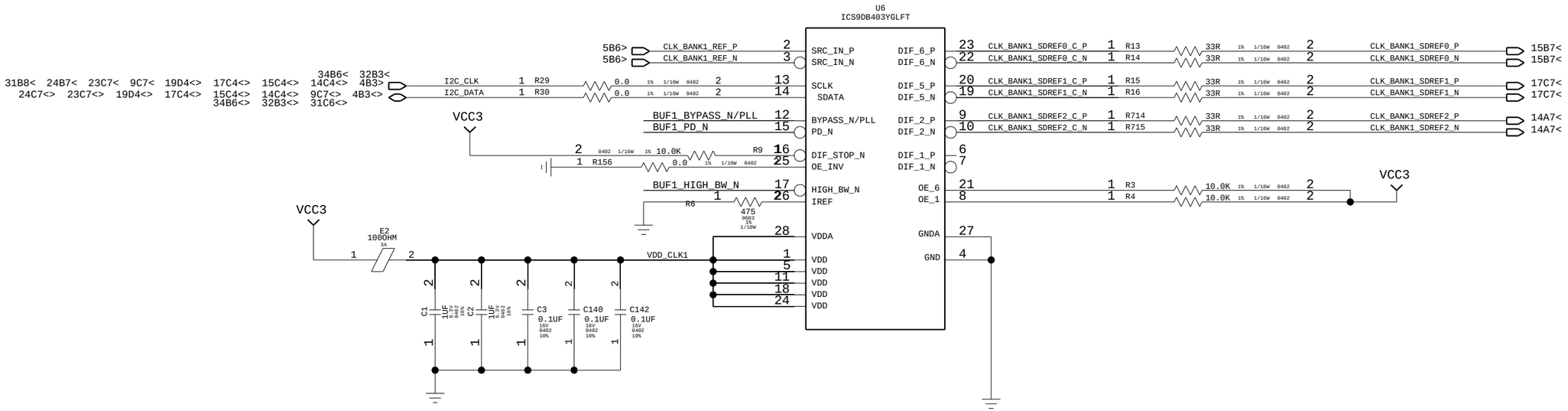
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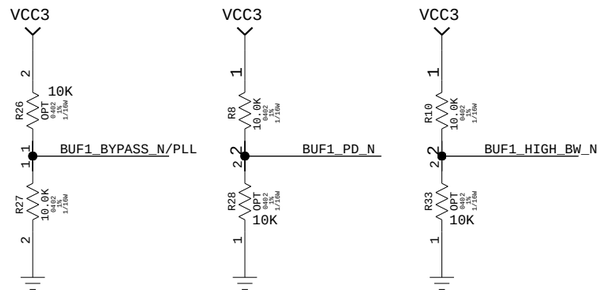


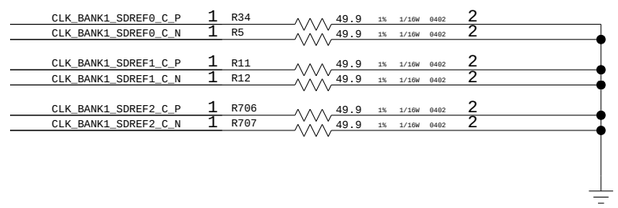
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	PAGE 7 OF 35

CLK BUFFER



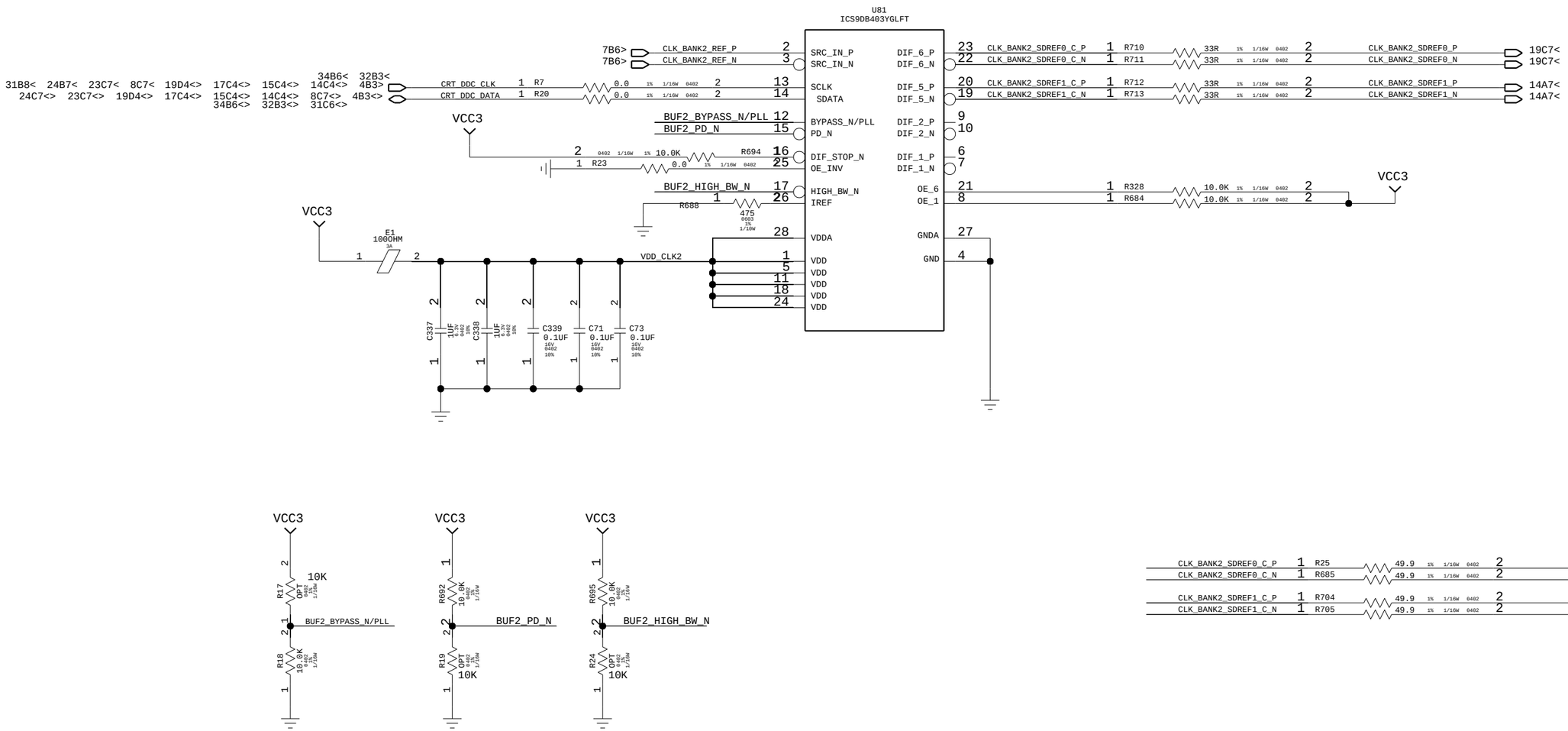




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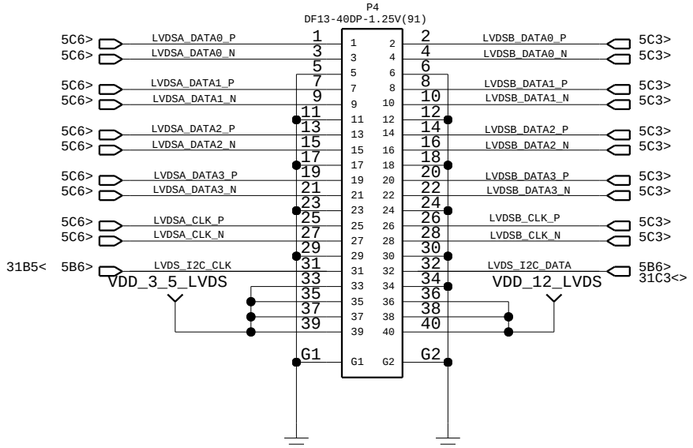
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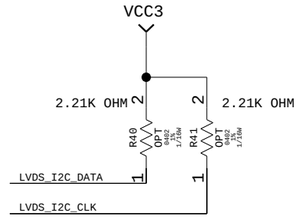
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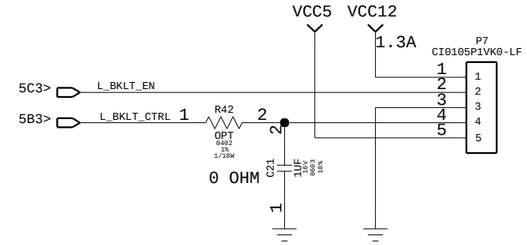


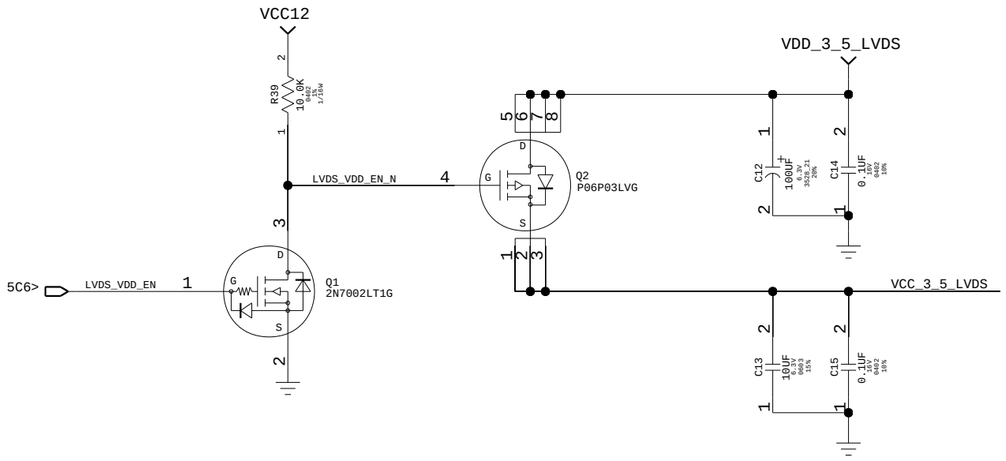
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	PAGE 9 OF 35

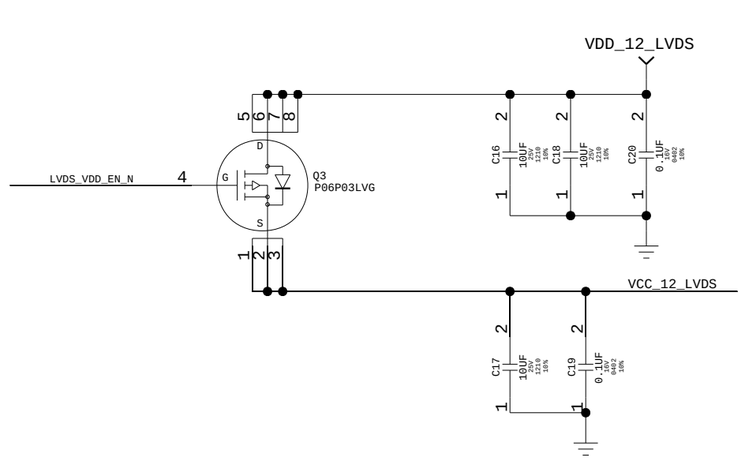
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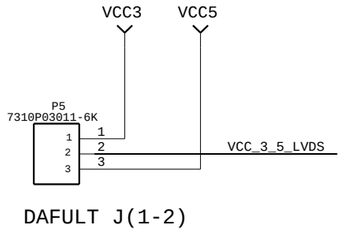


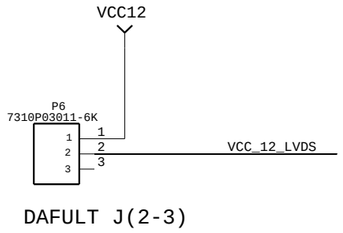






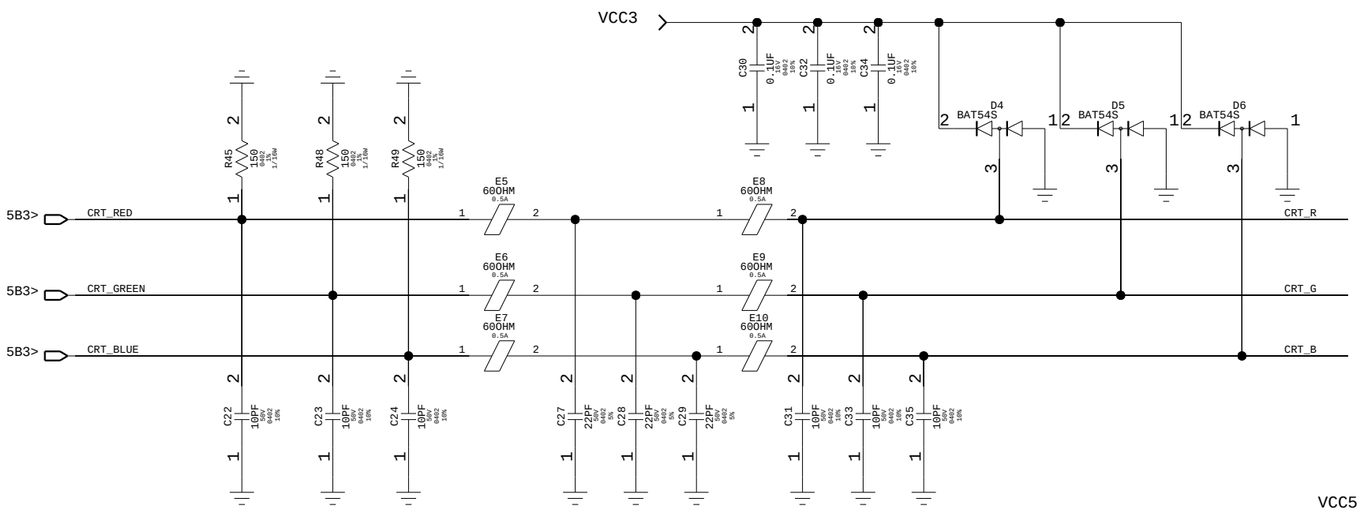


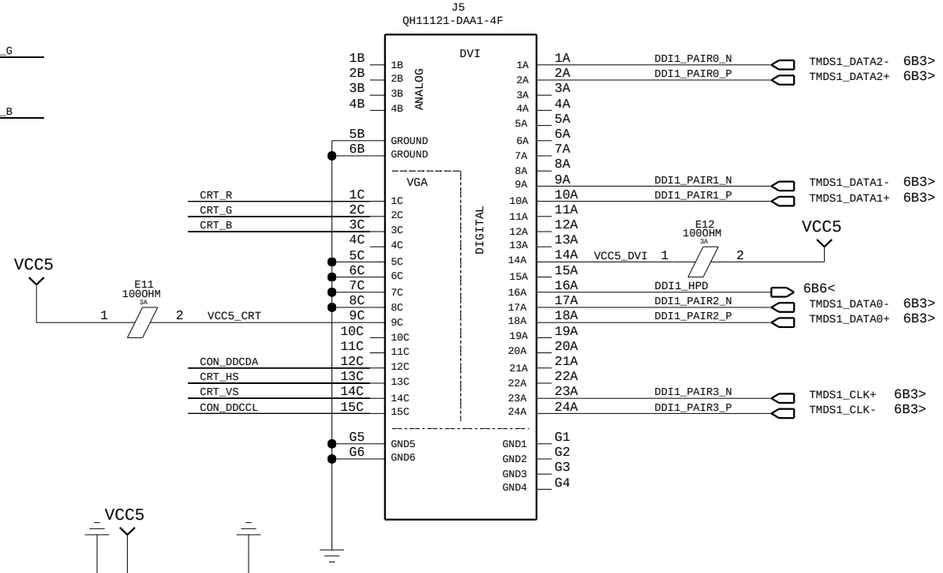


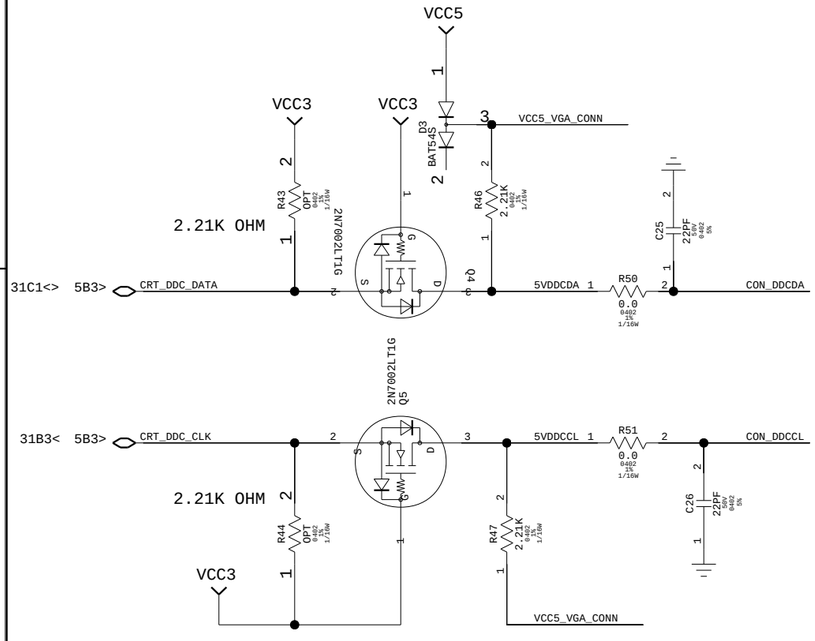


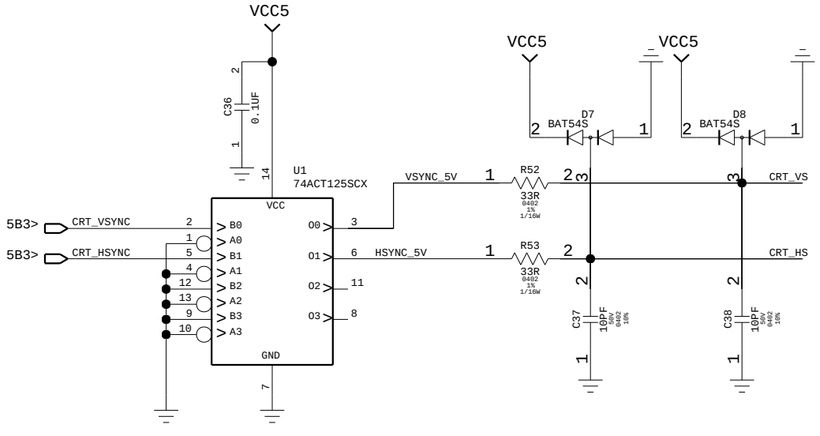
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	PAGE 10 OF 35

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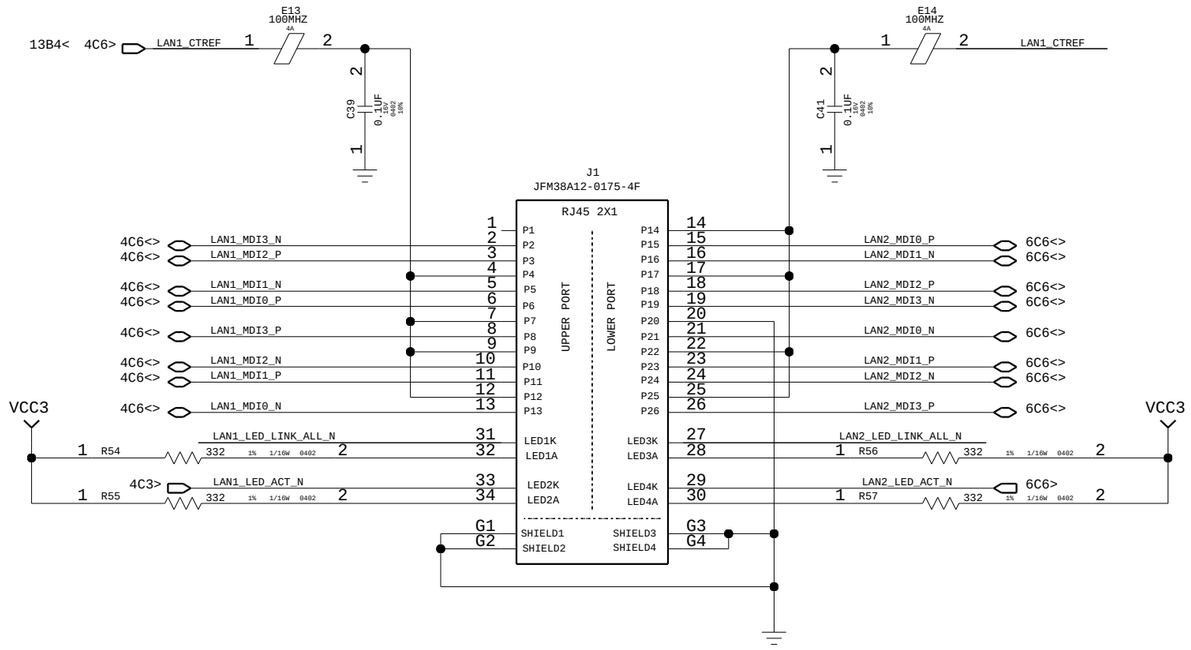






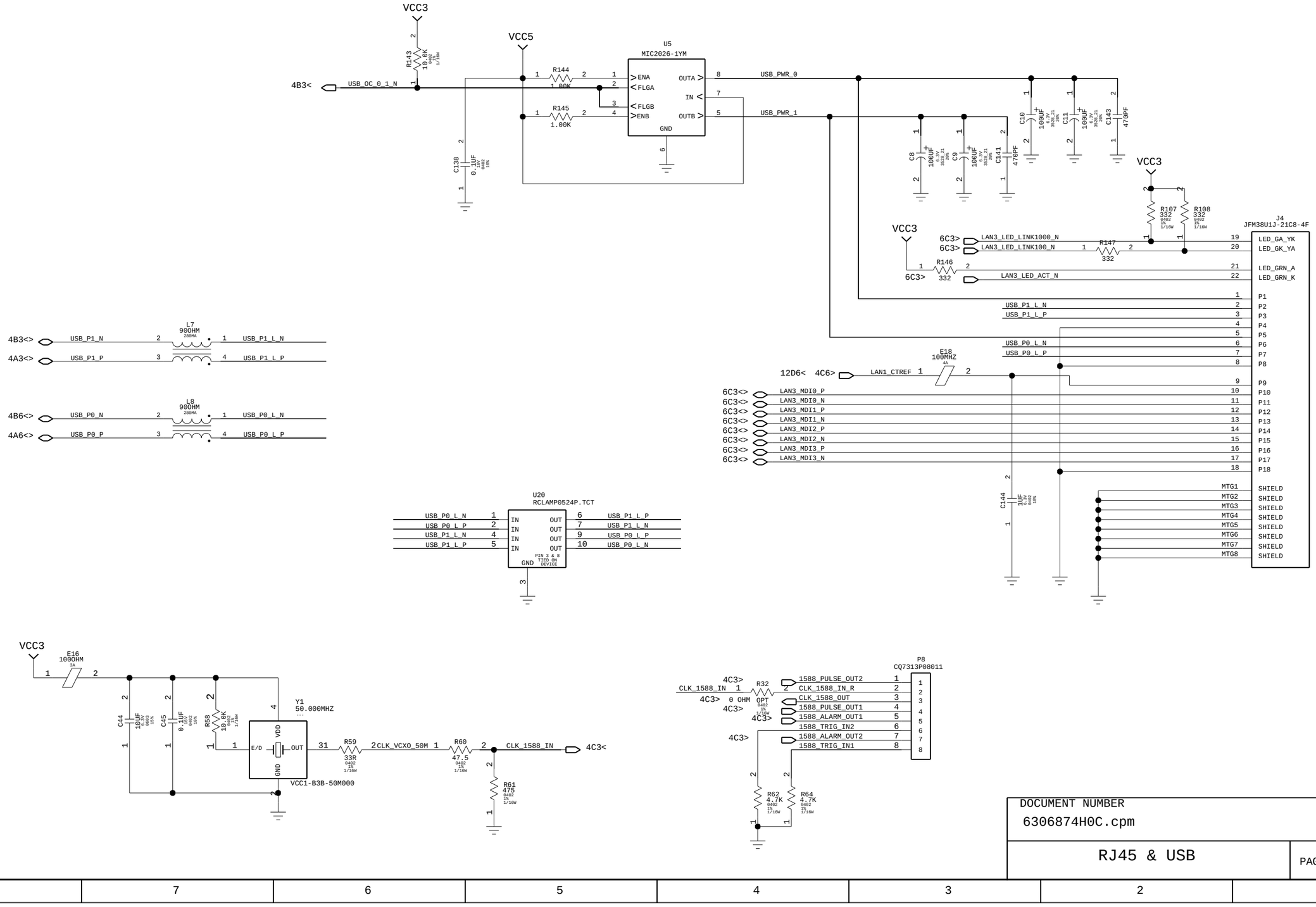
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	PAGE 11 OF 35

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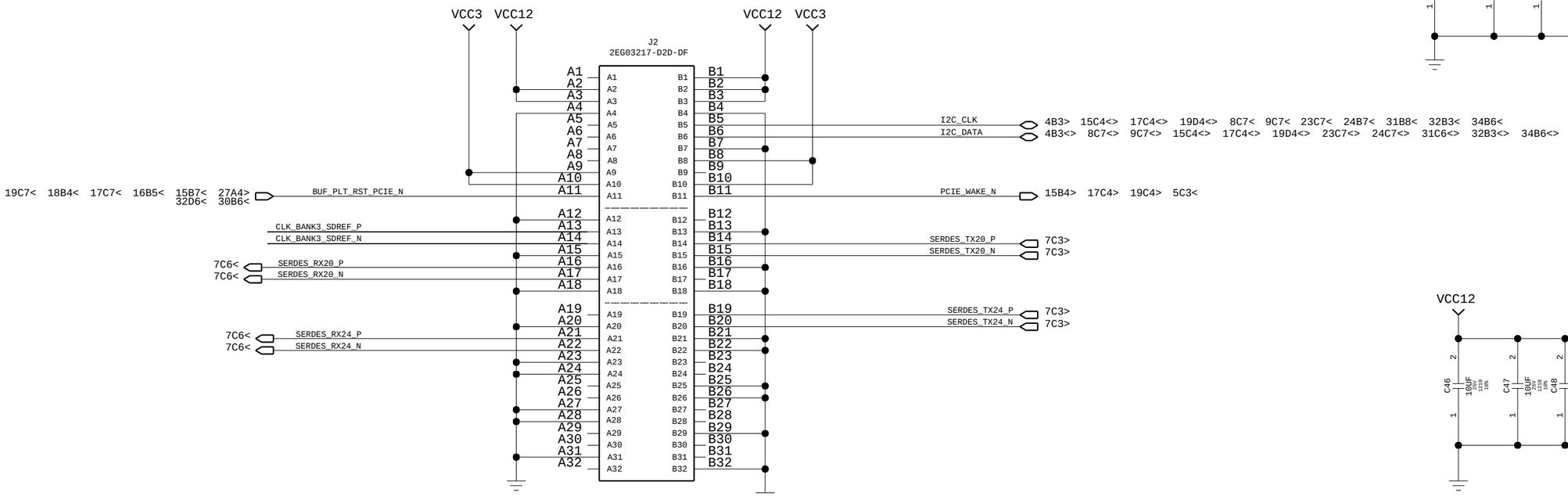
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	PAGE 12 OF 35

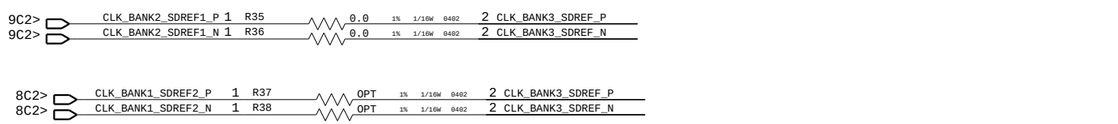
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RJ45 & USB	PAGE 13 OF 35

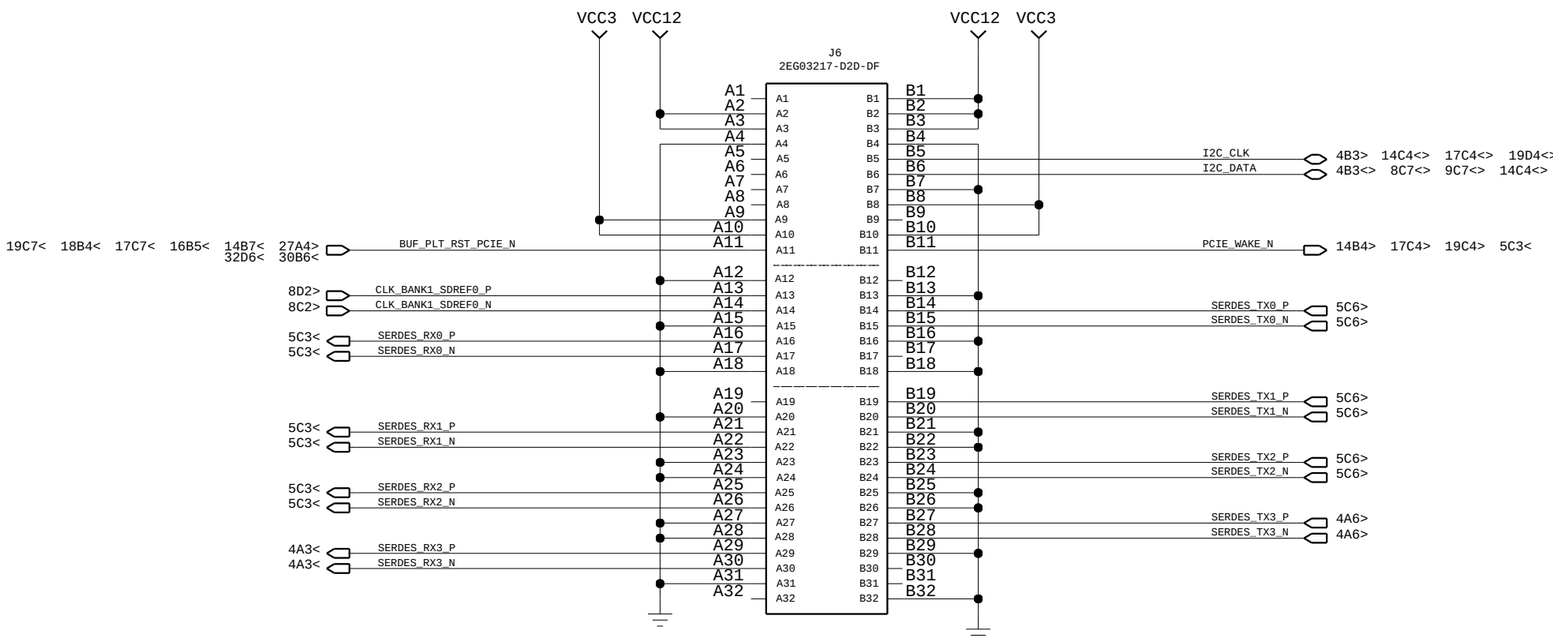
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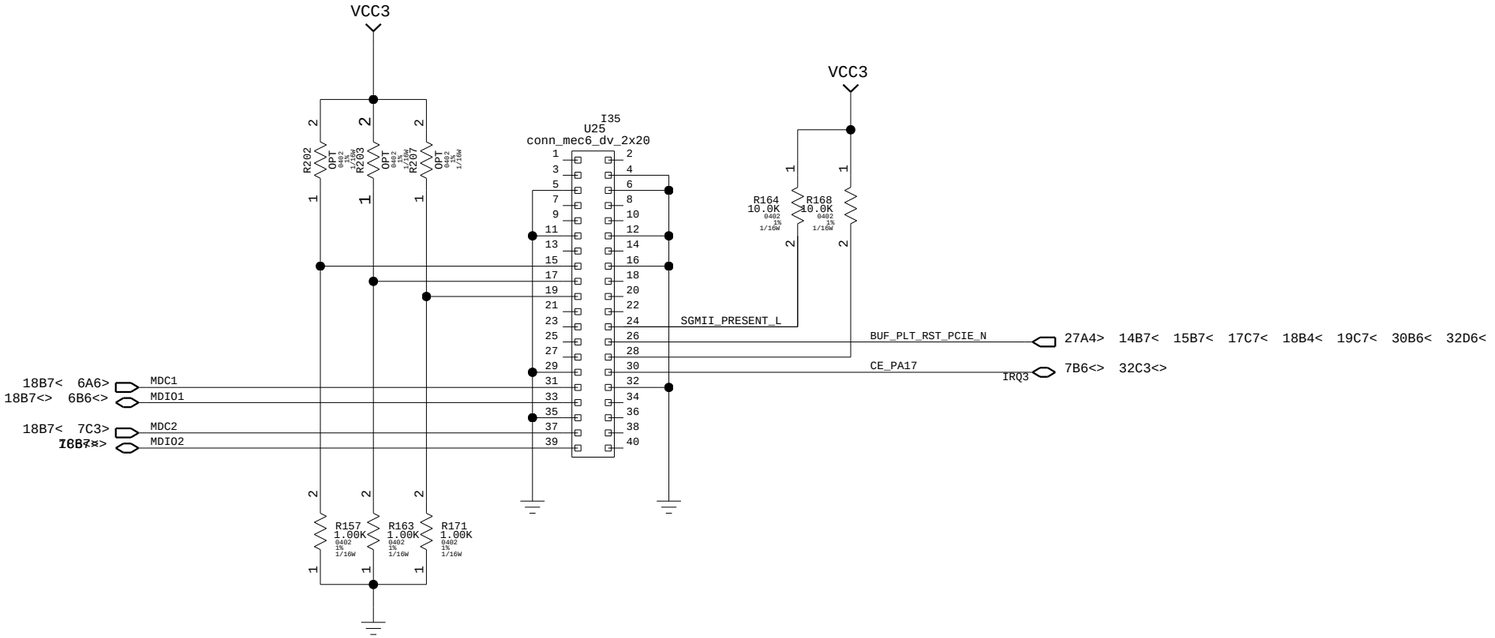


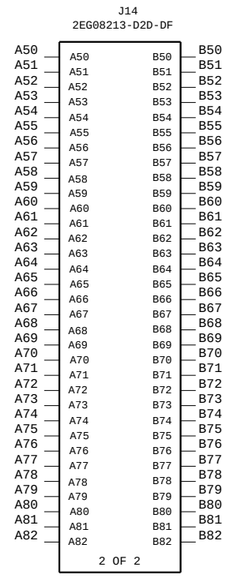
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	PAGE 14 OF 35

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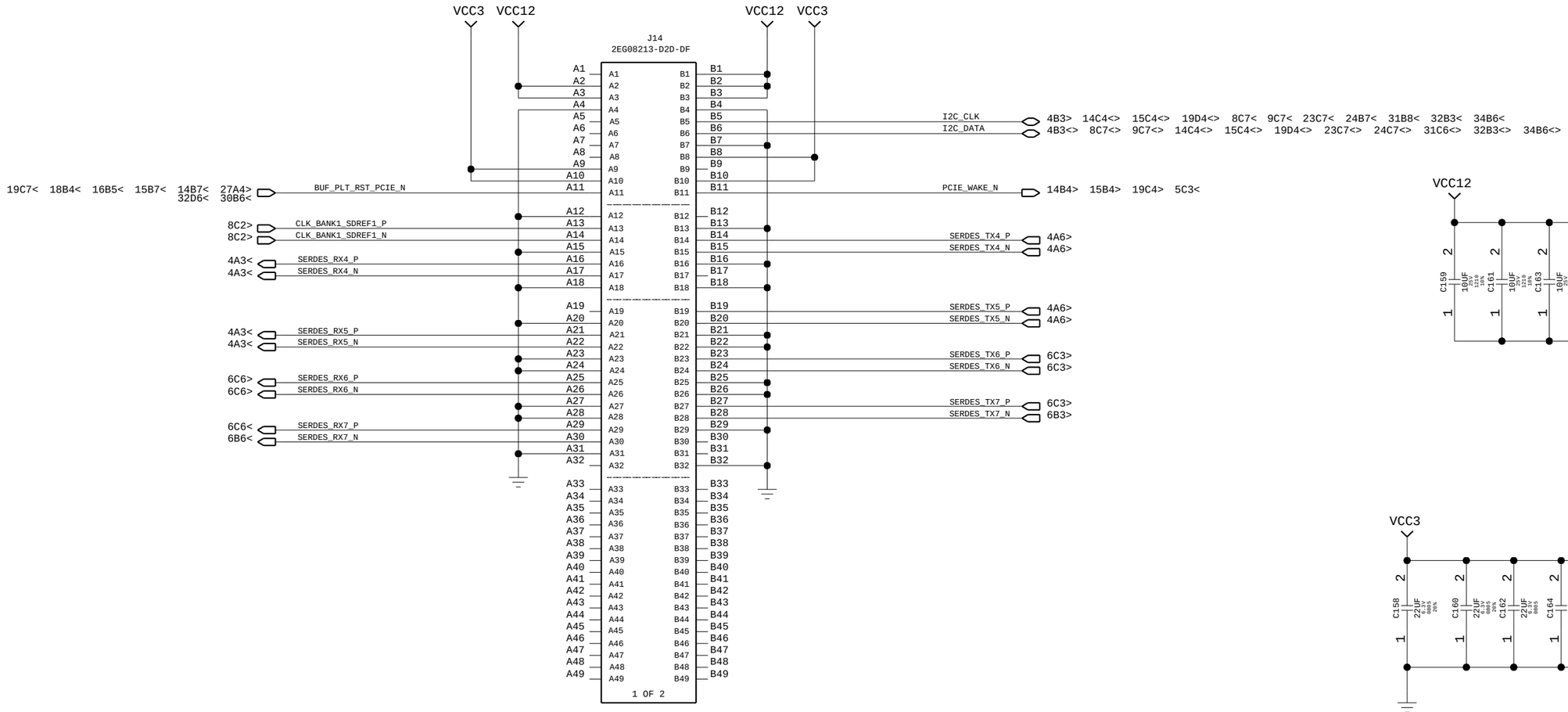
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	PAGE 15 OF 35

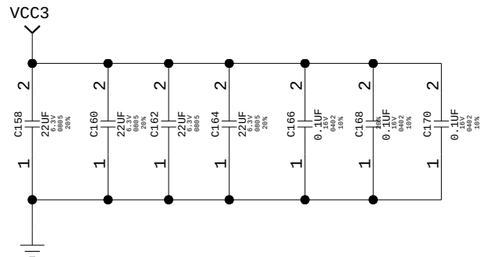




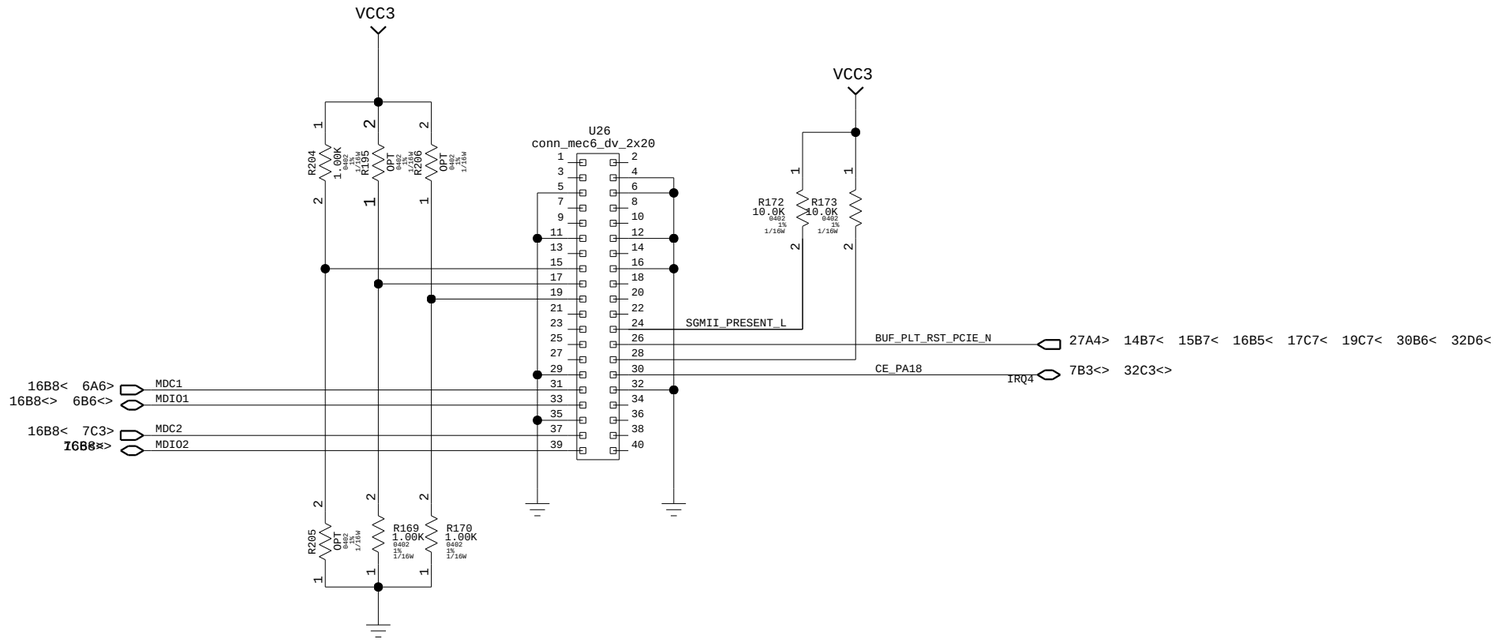
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	PAGE 16 OF 35

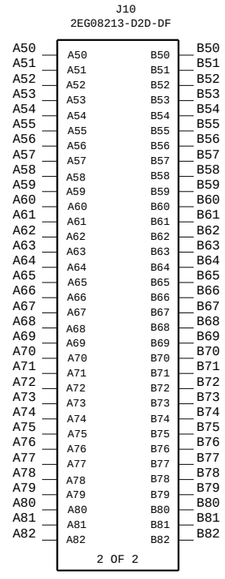
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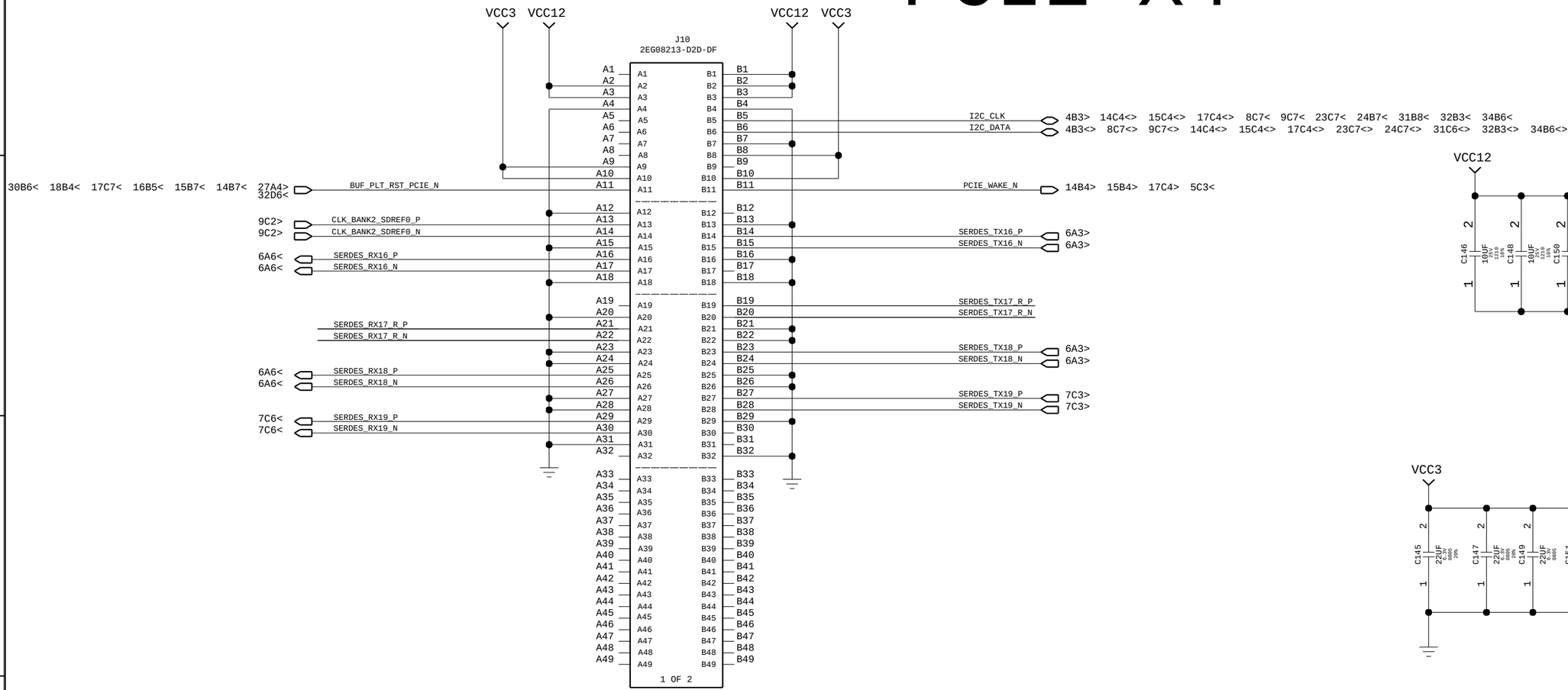
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	PAGE 17 OF 35

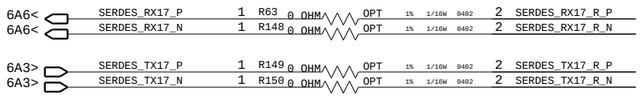




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	PAGE 18 OF 35

PCIE X4

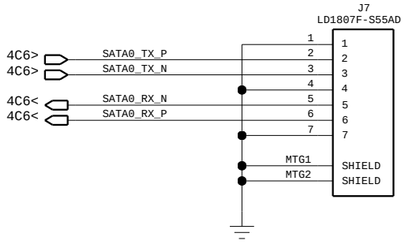


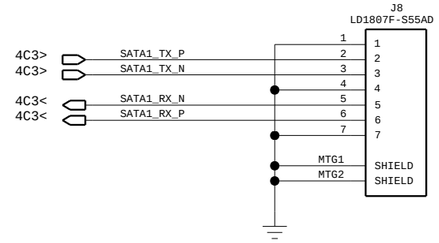


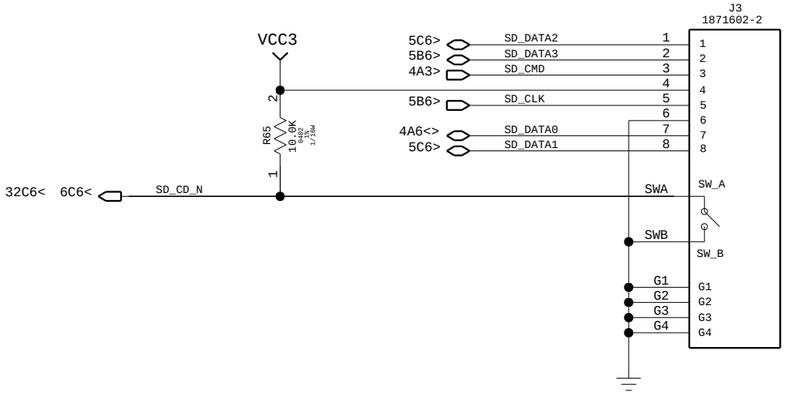
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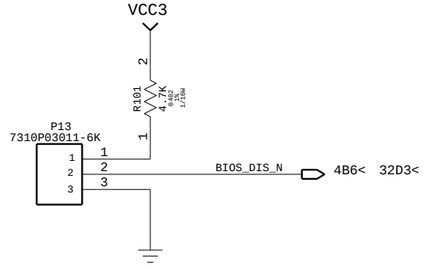
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	PAGE 19 OF 35

SATA & SDCARD





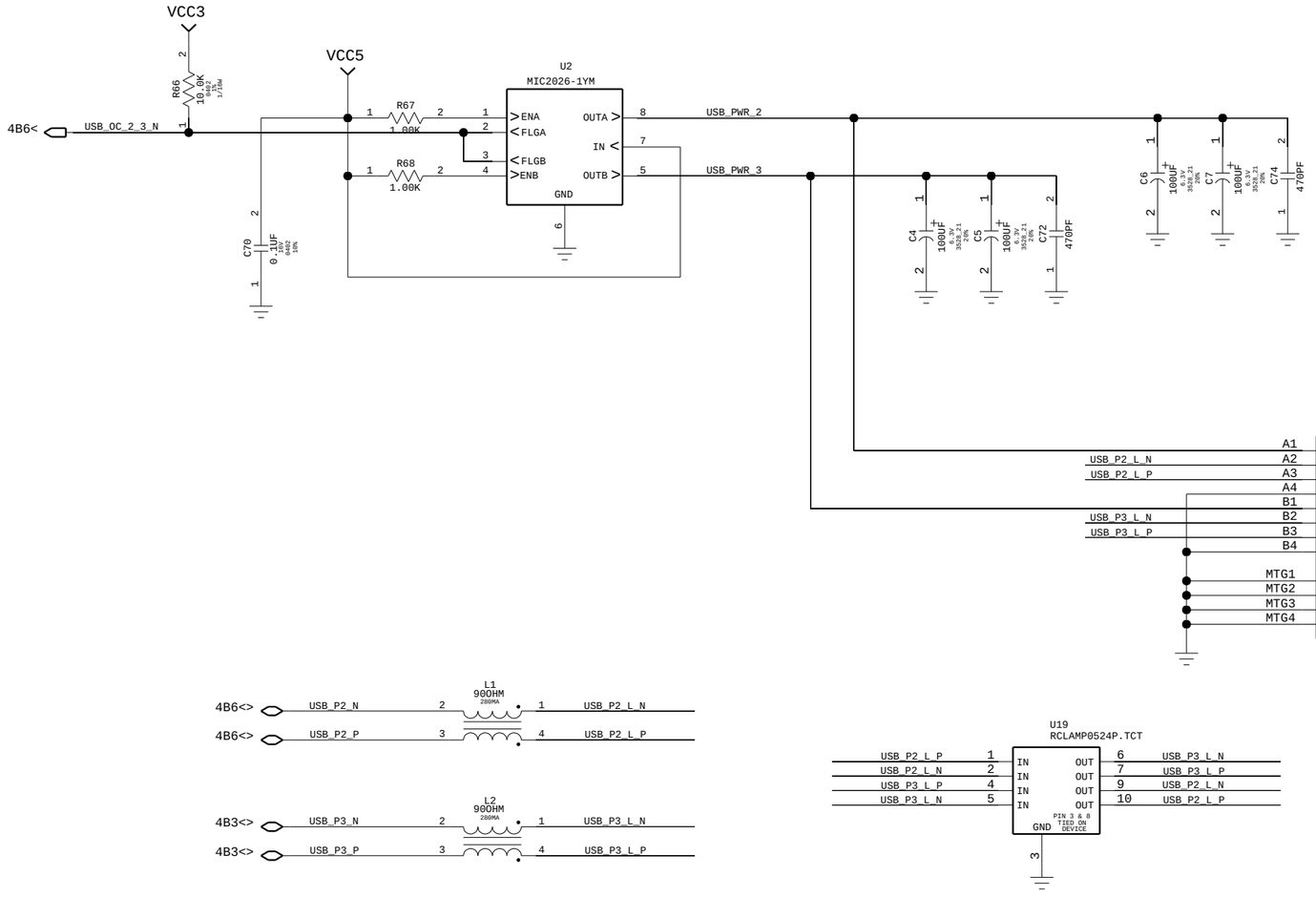




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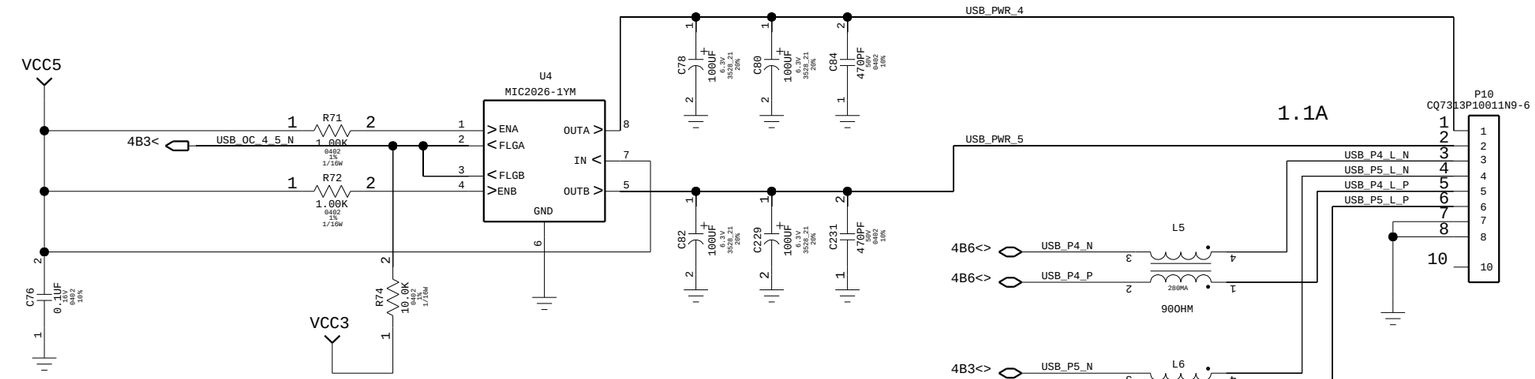
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	PAGE 20 OF 35

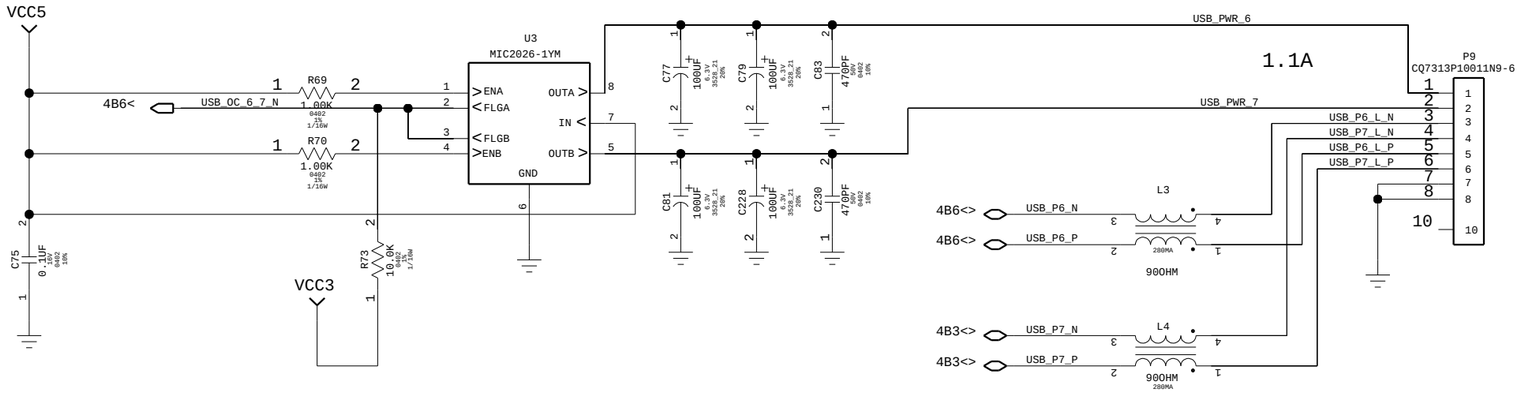
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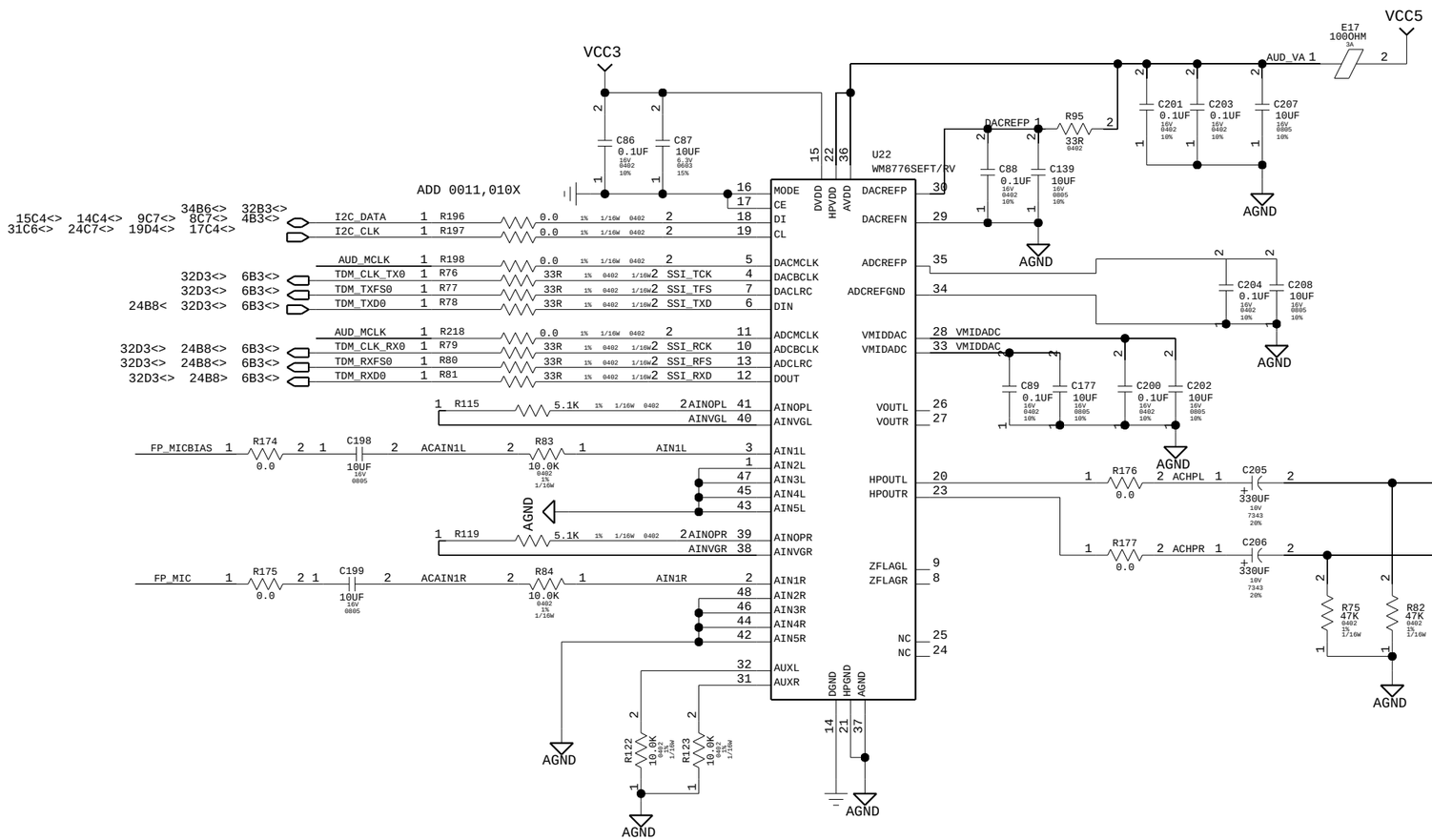
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	PAGE 21 OF 35

USB HEADER

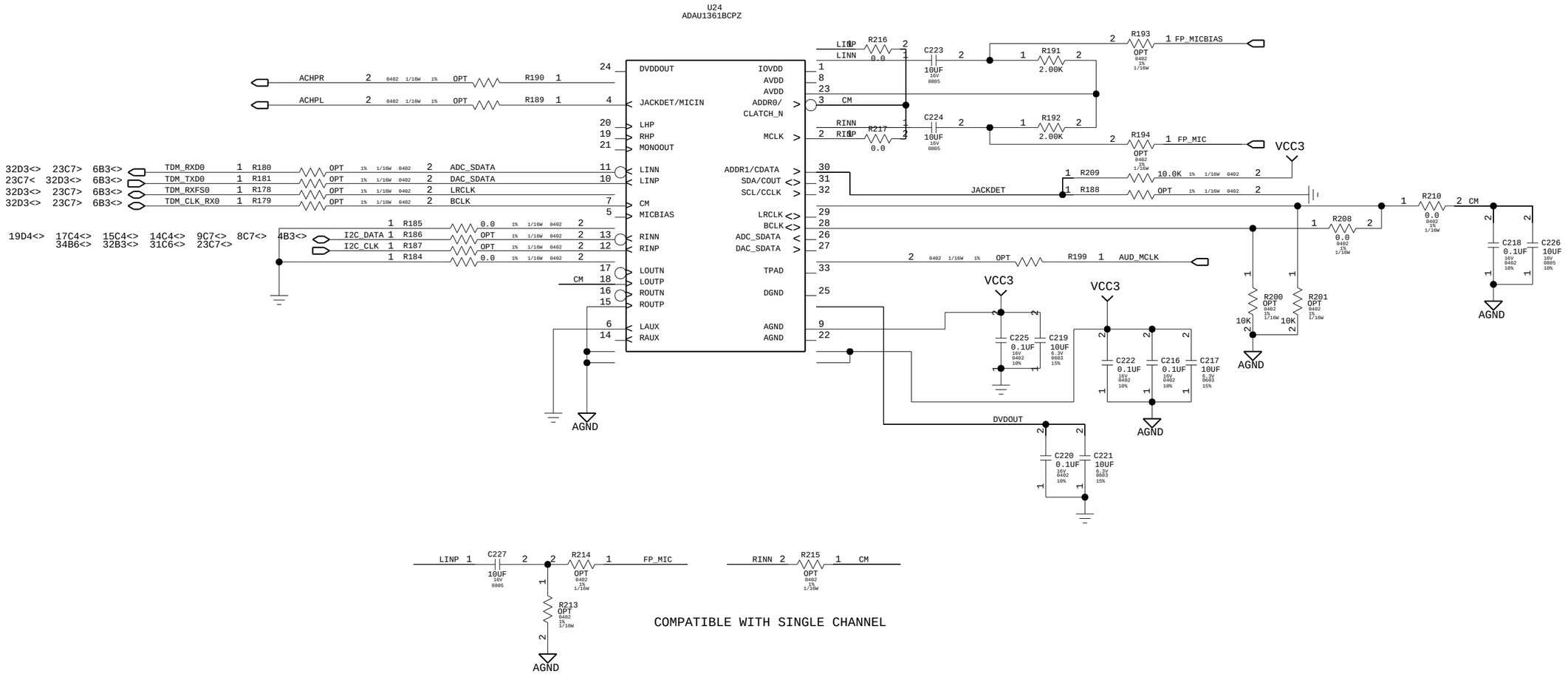




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	PAGE 22 OF 35

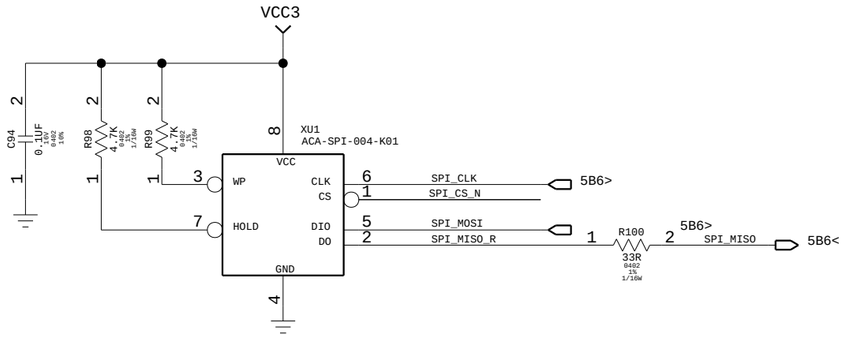


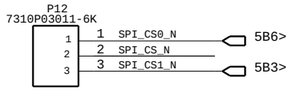
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	PAGE 23 OF 35



DOCUMENT NUMBER	REV
6306874H0C.cpm	A
	PAGE 24 OF 35

SPI FLASH



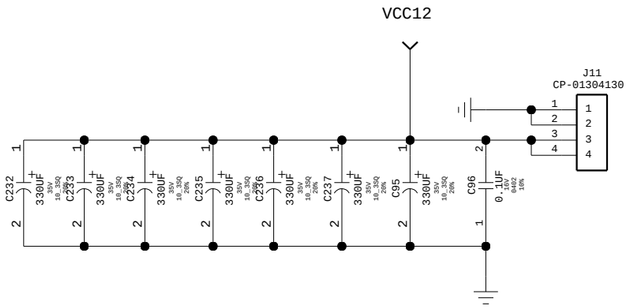


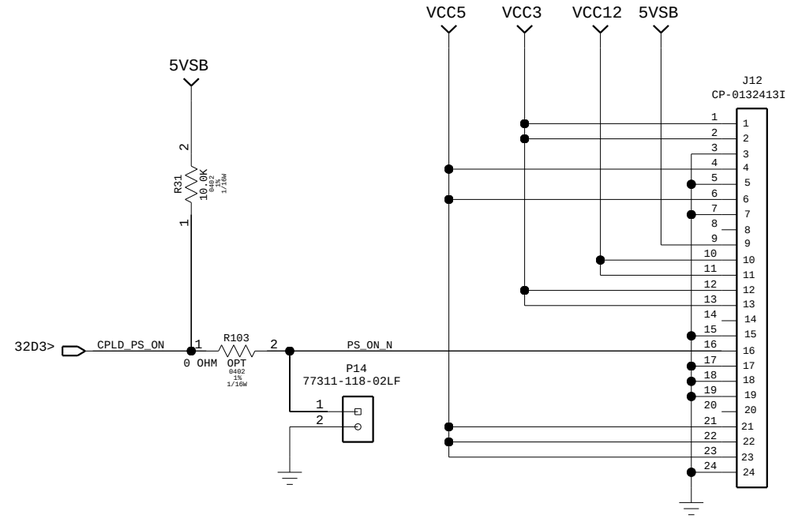
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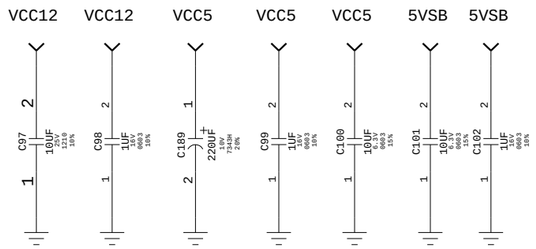
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2-3 SEL SPI 1

DOCUMENT NUMBER	REV
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	PAGE 25 OF 35

ATX POWER



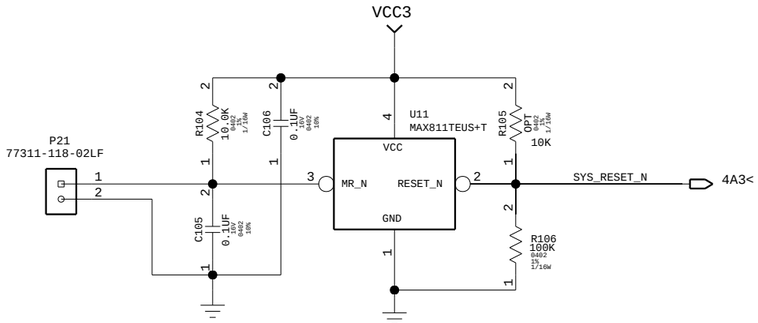


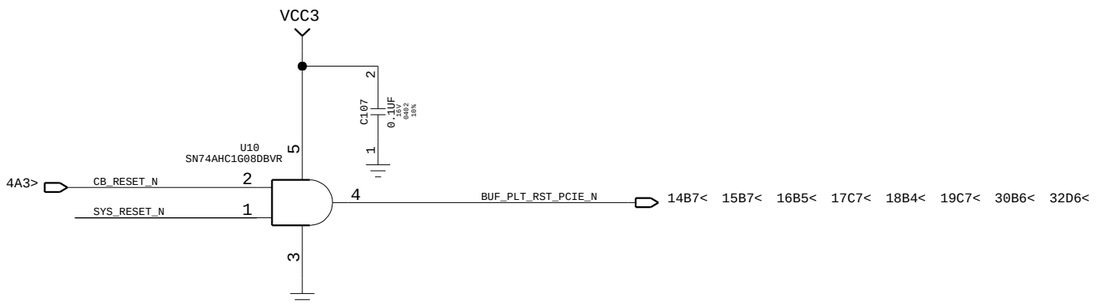


CLOSED TO ATX CONN

DOCUMENT NUMBER	REV
6306874H0C.cpm	A
	PAGE 26 OF 35

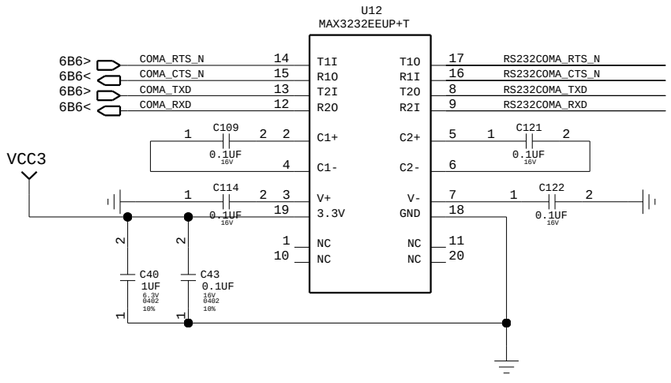
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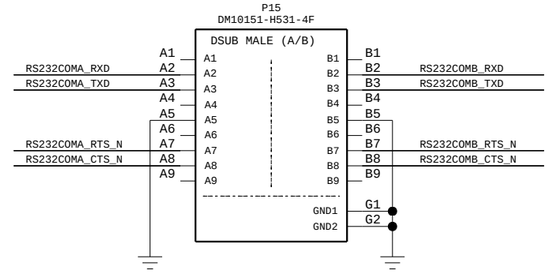


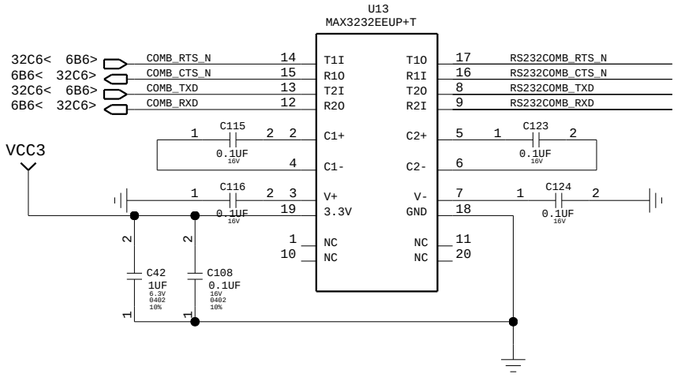


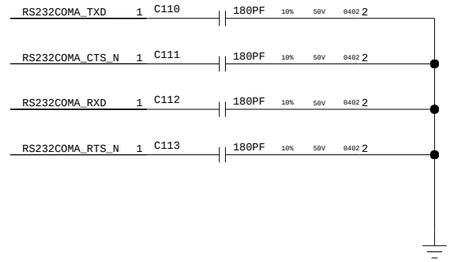
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	PAGE 27 OF 35

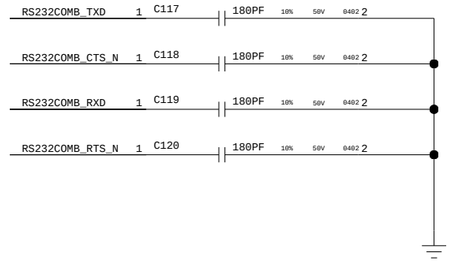
COM1&2 -DB9 CON





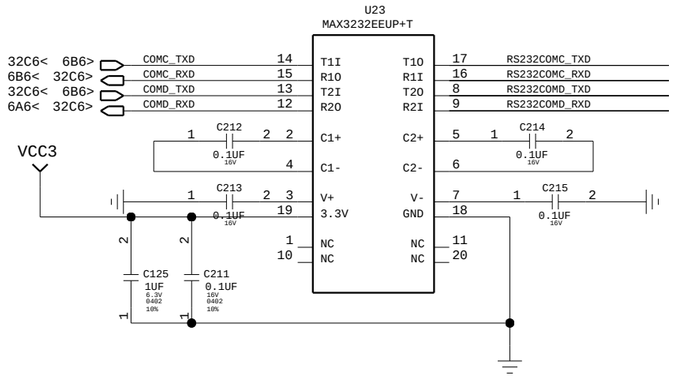


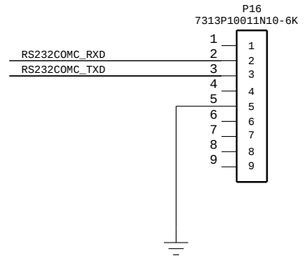


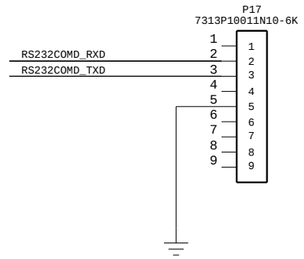


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6306874H0C.cpm	A
	PAGE 28 OF 35

COM3 - RS232 HDR





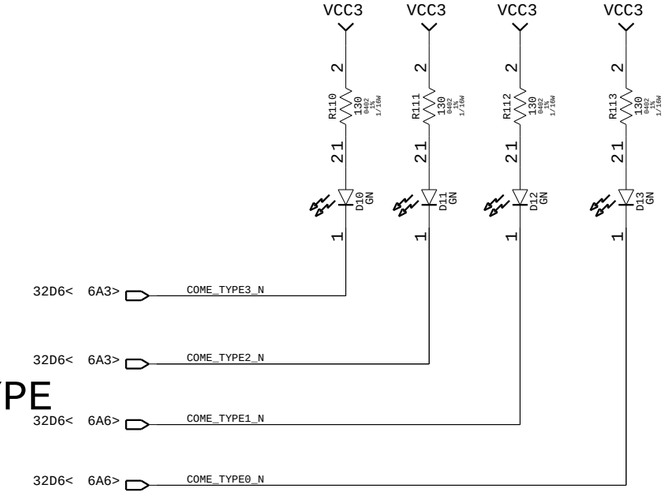


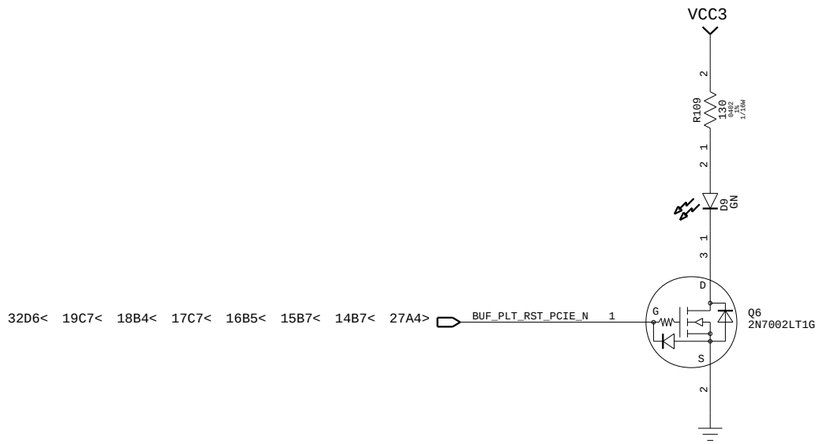


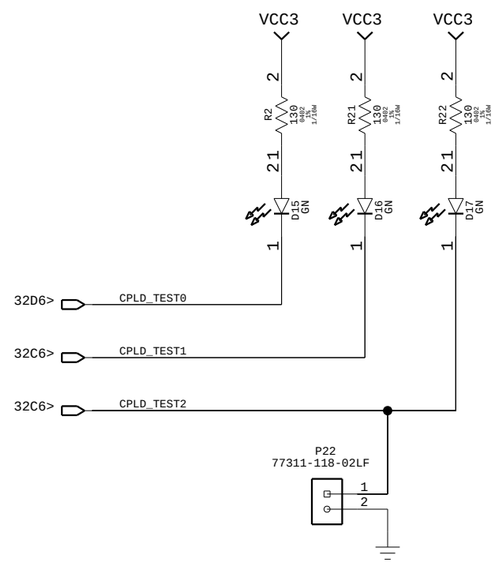
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6306874H0C.cpm	A
	PAGE 29 OF 35

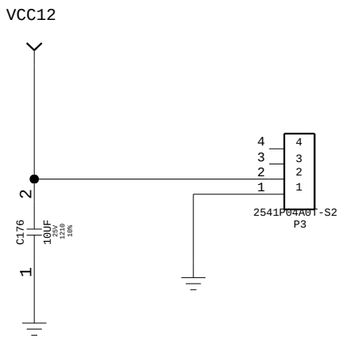
LED

TYPE



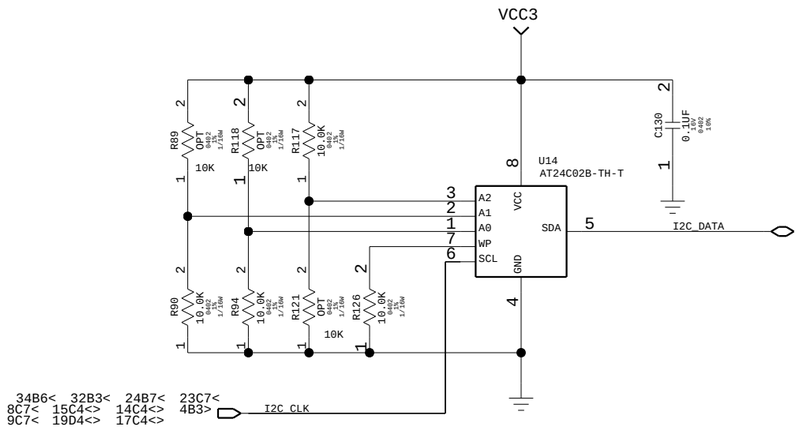




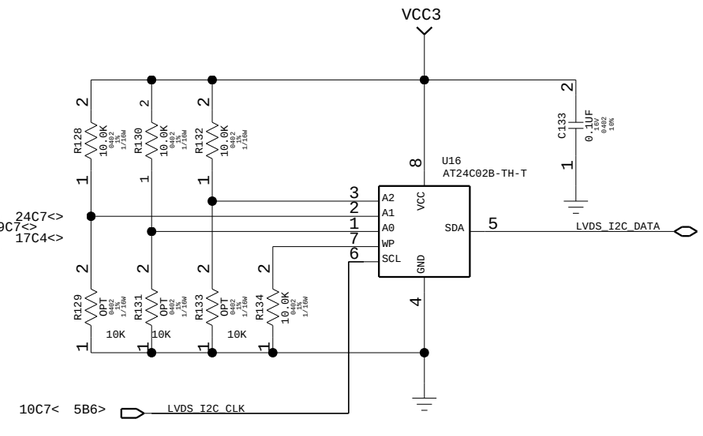


DOCUMENT NUMBER	REV
6306874H0C.cpm	A
	PAGE 30 OF 35

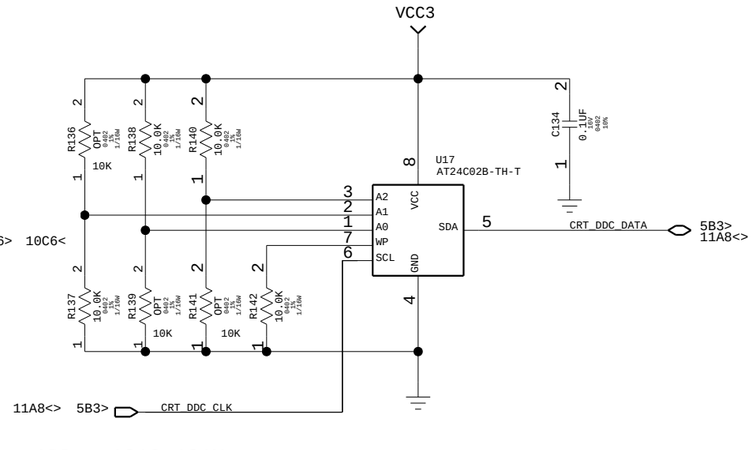
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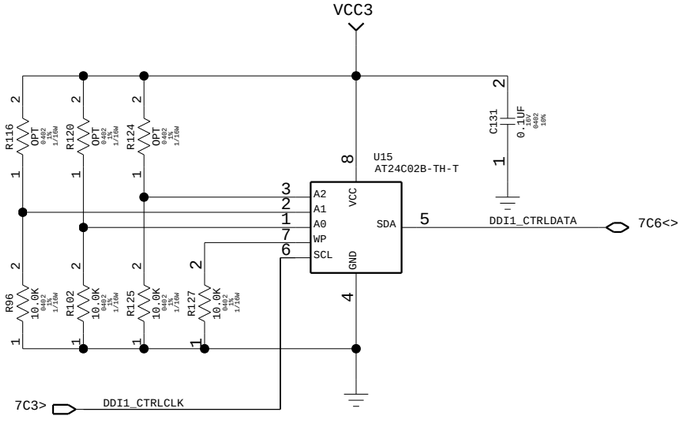
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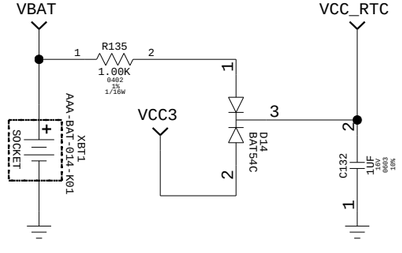


ADD 1010 101X



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BAT SKT



DOCUMENT NUMBER	REV
6306874H0C.cpm	B
	PAGE 31 OF 35

U18
XC95144XL-5TQ144C

30C3< 6A6>	COME_TYPE0_N	140	<> IO15	FB4	IO15 <>	105	BIOS_DIS_N
30C3< 6A6>	COME_TYPE1_N	139	<> IO14		IO14 <>	107	CPLD_PS_ON
30C3< 6A3>	COME_TYPE2_N	138	<> IO13		IO13 <>	104	TDM_CLK_TX0
30C3< 6A3>	COME_TYPE3_N	136	<> IO12		IO12 <>	102	TDM_CLK_RX0
34B6>	SFP_DETECT_N	137	<> IO11		IO11 <>	103	TDM_TXD0
	CLK_VCXO_50M	134	<> IO10		IO10 <>	100	TDM_RXD0
30B6< 19C7< 18B4< 17C7< 16B5< 15B7< 14B7< 27A4>	BUF_PLT_RST_PCIE_N	132	<> IO9		IO9 <>	98	TDM_TXFS0
30B3<	CPLD_TEST0	135	<> IO8		IO8 <>	101	TDM_RXFS0
30B3<	CPLD_TEST1	131	<> IO7		IO7 <>	96	TDM_CLK_TX1
30A3<	CPLD_TEST2	130	<> IO6		IO6 <>	94	TDM_CLK_RX1
33C6<>	CPLD_GPIO_C5	129	<> IO5		IO5 <>	93	TDM_TXD1
33C6<>	CPLD_GPIO_C4	128	<> IO4		IO4 <>	92	TDM_RXD1
33C6<>	CPLD_GPIO_C3	133	<> IO3		IO3 <>	97	TDM_TXFS1
33C6<>	CPLD_GPIO_C2	126	<> IO2		IO2 <>	95	TDM_RXFS1
33C6<>	CPLD_GPIO_C1	118	<> IO1		IO1 <>	91	HD_LED_N
29C7< 6B6>	COMC_TXD	51	<> IO15	FB3	IO15 <>	88	CE_PA18
6B6< 29C7>	COMC_RXD	50	<> IO14		IO14 <>	83	CE_PA22
6B6<	COMC_CTS_N	49	<> IO13		IO13 <>	87	CE_PA23
6B6>	COMC_RTS_N	45	<> IO12		IO12 <>	86	CE_PA19
29C7< 6B6>	COMD_TXD	43	<> IO11		IO11 <>	81	CE_PA24
6A6< 29C7> UART USED AS QE ON P1021	COMD_RXD	48	<> IO10		IO10 <>	85	CE_PA29
6A6<	COMD_CTS_N	40	<> IO9		IO9 <>	82	CE_PA30
6A6>	COMD_RTS_N	38	<> GCK3/IO8		IO8 <>	79	CE_PA31
28A7< 6B6>	COMB_TXD	46	<> IO7		IO7 <>	80	CE_PA12
6B6< 28A7>	COMB_RXD	34	<> IO6		IO6 <>	78	CE_PA13
6B6< 28B7>	COMB_CTS_N	33	<> IO5		IO5 <>	77	CE_PA15
28B7< 6B6>	COMB_RTS_N	44	<> IO4		IO4 <>	76	CE_PA16
6C6< 20A8> CE_PB12	CE_PB12	41	<> IO3		IO3 <>	74	IRQ_IN1
6C6> CE_PB13	SD_WP	32	<> GCK2/IO2		IO2 <>	75	IRQ_IN2
33A6<>	CPLD_GPIO_C6	39	<> IO1		IO1 <>	71	CE_PA17
6B3<>	CE_PB18	15	<> IO15	FB2	IO13 <>	125	CE_PA25
6B3<>	CE_PB19	14	<> IO14		IO12 <>	117	CE_PA26
6B3<>	CE_PB20	13	<> IO13		IO11 <>	124	CE_PA27
7B3<>	CE_PB0	11	<> IO12		IO10 <>	121	CE_PA28
7B3<>	CE_PB8	12	<> IO11		IO9 <>	120	CE_PA14
7B3<>	CE_PB9	10	<> IO10		IO8 <>	119	CE_PA20
7B3<>	CE_PB29	9	<> IO9		IO7 <>	115	CE_PA21
7B3<>	CE_PB30	7	<> IO8		IO6 <>	116	CE_PA2
7B6<>	CE_PB1	6	<> GTS2/IO7		IO5 <>	113	CE_PA3
7B6<>	CE_PB2	5	<> GTS1/IO6		IO4 <>	112	I2C_DATA
7B6<>	CE_PB3	3	<> GTS4/IO5		IO3 <>	110	I2C_CLK
5B3<>	CE_PB31	2	<> GTS3/IO4		IO2 <>	111	CPLD_GPIO_C8
7B3< CE_PB21	TAMPER_DET	4	<> IO3		IO1 <>	106	CPLD_GPIO_C7
7B3< CE_PB22	TAMPER_DET_BAT	143	<> GSR/IO2				
7B3< CE_PB23	TAMPER_DET_GND	142	<> IO1				
33A6<>	CPLD_GPIO_B15	30	<> GCK1/IO15	FB1	IO14 <>	69	CPLD_GPIO_A14
33A6<>	CPLD_GPIO_B14	35	<> IO14		IO13 <>	64	CPLD_GPIO_A13
33A6<>	CPLD_GPIO_B13	28	<> IO13		IO12 <>	61	CPLD_GPIO_A12
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33B6<>	CPLD_GPIO_B5	19	<> IO5		IO4 <>	54	CPLD_GPIO_A4
33B6<>	CPLD_GPIO_B4	25	<> IO4		IO3 <>	53	CPLD_GPIO_A3
33B6<>	CPLD_GPIO_B3	17	<> IO3		IO2 <>	59	CPLD_GPIO_A2
33B6<>	CPLD_GPIO_B2	16	<> IO2		IO1 <>	52	CPLD_GPIO_A1
33B6<>	CPLD_GPIO_B1	23	<> IO1				

JTAG_CPLD_TCK	67	> TCK	
JTAG_CPLD_TMS	65	> TMS	
JTAG_CPLD_TDI	63	> TDI	
JTAG_CPLD_TDO	122	< TDO	

VCCIO	127		
VCCIO	109		
VCCIO	73		
VCCIO	55		
VCCIO	37		
VCCIO	1		
VCCINT	141		
VCCINT	84		
VCCINT	42		
VCCINT	8		
GND[1:13]			

VCC3

6B6>	105	BIOS_DIS_N	20A2> 4B6<				
	107	CPLD_PS_ON	26C5<				
	104	TDM_CLK_TX0	6B3<> 23C7>				
	102	TDM_CLK_RX0	6B3<> 23C7> 24B8<>				
	103	TDM_TXD0	6B3<> 23C7< 24B8<				
	100	TDM_RXD0	6B3<> 23C7> 24B8>				
	98	TDM_TXFS0	6B3<> 23C7>				
	101	TDM_RXFS0	6B3<> 23C7> 24B8<>				
	96	TDM_CLK_TX1	6B3<>				
	94	TDM_CLK_RX1	6B3<>				
	93	TDM_TXD1	6A3<>				
	92	TDM_RXD1	6A3<>				
	97	TDM_TXFS1	6A3<>				
	95	TDM_RXFS1	6A3<>				
	91	HD_LED_N	4B6>				
FB8	88	CE_PA18	7B3<> 18B4<>				
	83	CE_PA22	5B3<>				
	87	CE_PA23	5B3<>				
	86	CE_PA19	7B3<>				
	81	CE_PA24	7B3<>				
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	79	CE_PA31	7B3<>				
	80	CE_PA12	7C6<>				
	78	CE_PA13	7C6<>				
	77	CE_PA15	7C6<>				
	76	CE_PA16	7C6<>				
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FB7	125	CE_PA25	7B6<>				
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	121	CE_PA28	7B6<>				
	120	CE_PA14	7C3<>				
	119	CE_PA20	7C3<>				
	115	CE_PA21	7C3<>				
	116	CE_PA2	6B3<>				
	113	CE_PA3	6B3<>				
	112	I2C_DATA	4B3<> 8C7<> 9C7<> 14C4<> 15C4<> 17C4<> 19D4<> 23C7<> 24C7<> 31C6<> 34B6<>				
	110	I2C_CLK	4B3> 14C4<> 15C4<> 17C4<> 19D4<> 8C7< 9C7< 23C7< 24B7< 31B8< 34B6<				
FB6	111	CPLD_GPIO_C8	33C6<>				
	106	CPLD_GPIO_C7	33C6<>				
	69	CPLD_GPIO_A14	33B6<>				
	64	CPLD_GPIO_A13	33B6<>				
	61	CPLD_GPIO_A12	33B6<>				
	70	CPLD_GPIO_A11	33B6<>				
	60	CPLD_GPIO_A10	33B6<>				
	58	CPLD_GPIO_A9	33B6<>				
	68	CPLD_GPIO_A8	33B6<>				
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	66	CPLD_GPIO_A5	33C6<>				
	54	CPLD_GPIO_A4	33B6<>				
	53	CPLD_GPIO_A3	33C6<>				
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FB5	52	CPLD_GPIO_A1	33C6<>				

VCC3

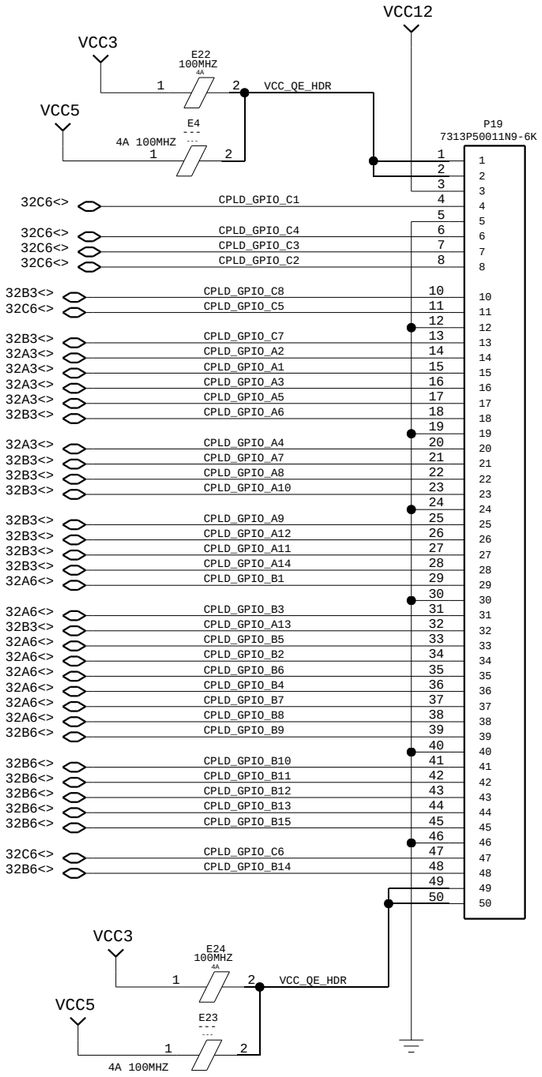
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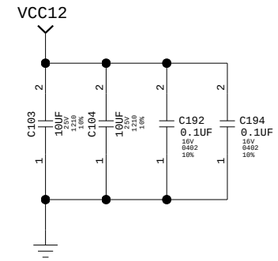
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R165 10 10K	2	JTAG_CPLD_TDO	3
R166 10 10K	2	JTAG_CPLD_TMS	5
R167 10 10K	2	JTAG_CPLD_TDI	9

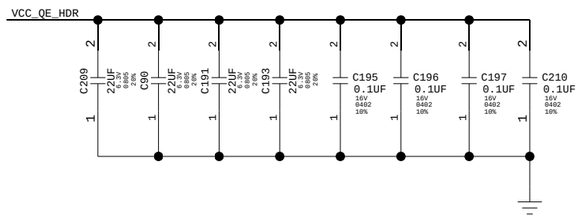
VCC3

C190 100UF 6.3V 3528-21 20%
C178 10UF 6.3V 0603 10%
C179 0.1UF 16V 0402 10%
C180 0.1UF 16V 0402 10%
C181 0.1UF 16V 0402 10%
C182 0.1UF 16V 0402 10%
C183 0.1UF 16V 0402 10%
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C186 0.1UF 16V 0402 10%
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	PAGE 32 OF 35



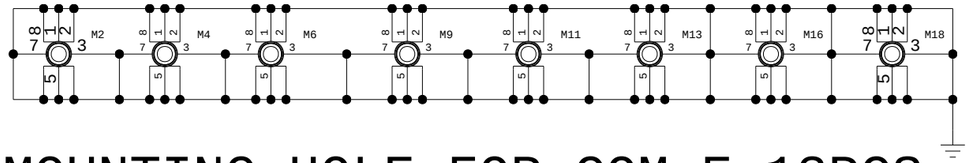




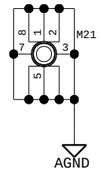
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	PAGE 33 OF 35

DOCUMENT NUMBER	REV
6306874H0C.cpm	A
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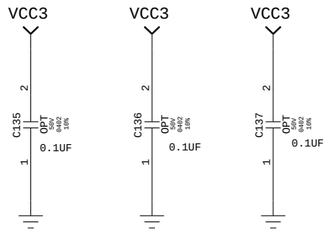
MOUNTING HOLE 9PCS



MOUNTING HOLE FOR COM-E 13PCS



FOR EMC



PLS NEAR CLK BUF

ADDITION MATERAL

1. PCB QTY 1
2. COIN BATTERY QTY 1
3. JMP QTY
4. SPI FLASH QTY 1
5. SFP MODULE

DOCUMENT NUMBER	REV
6306874H0C.cpm	A
	PAGE 35 OF 35