
Big–Endian Layout
Byte–Address:
0x00
Bit 11
Bit 10
Bit 9
Bit 8
Bit 7
Bit 6
Bit 5
Bit 4
0x01
Bit 3
Bit 2
Bit 1
Bit 0
lsBit: 4
Little–Endian Layout
Byte–Address:
0x00
Bit 3
Bit 2
Bit 1
Bit 0
lsBit: 4
0x01
Bit 11
Bit 10
Bit 9
Bit 8
Bit 7
Bit 6
Bit 5
Bit 4