

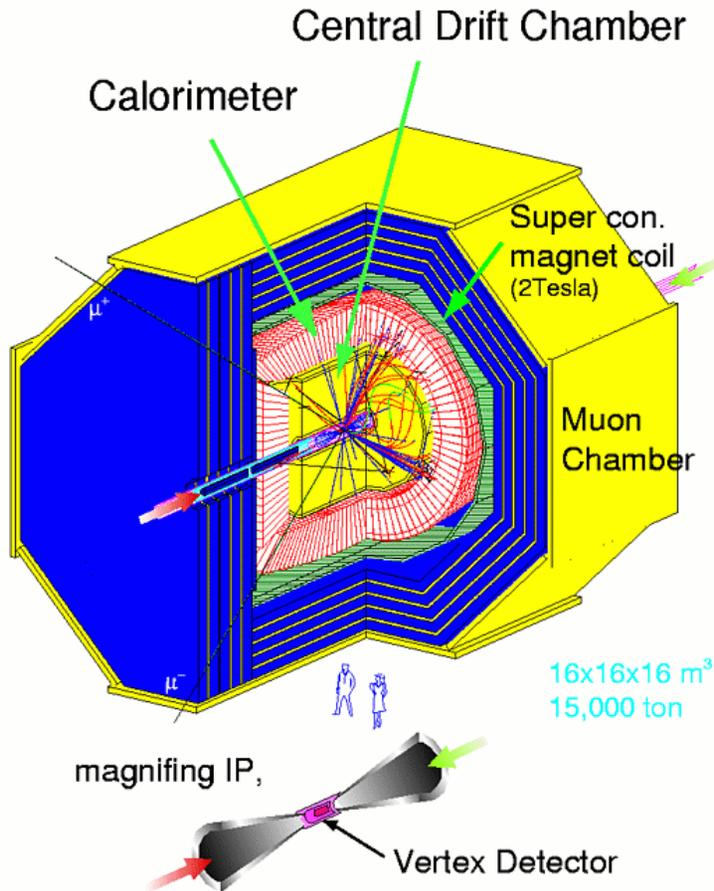
SiD Detector Concept R&D Perspective

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Detector Concepts and Challenges



- Three concepts under study
- Typically requires factors of two or so improvements in granularity, resolution, etc. from present generation detectors
- Focused R&D program required to develop the detectors -- end of 2005

The GDE Plan and Schedule

2005

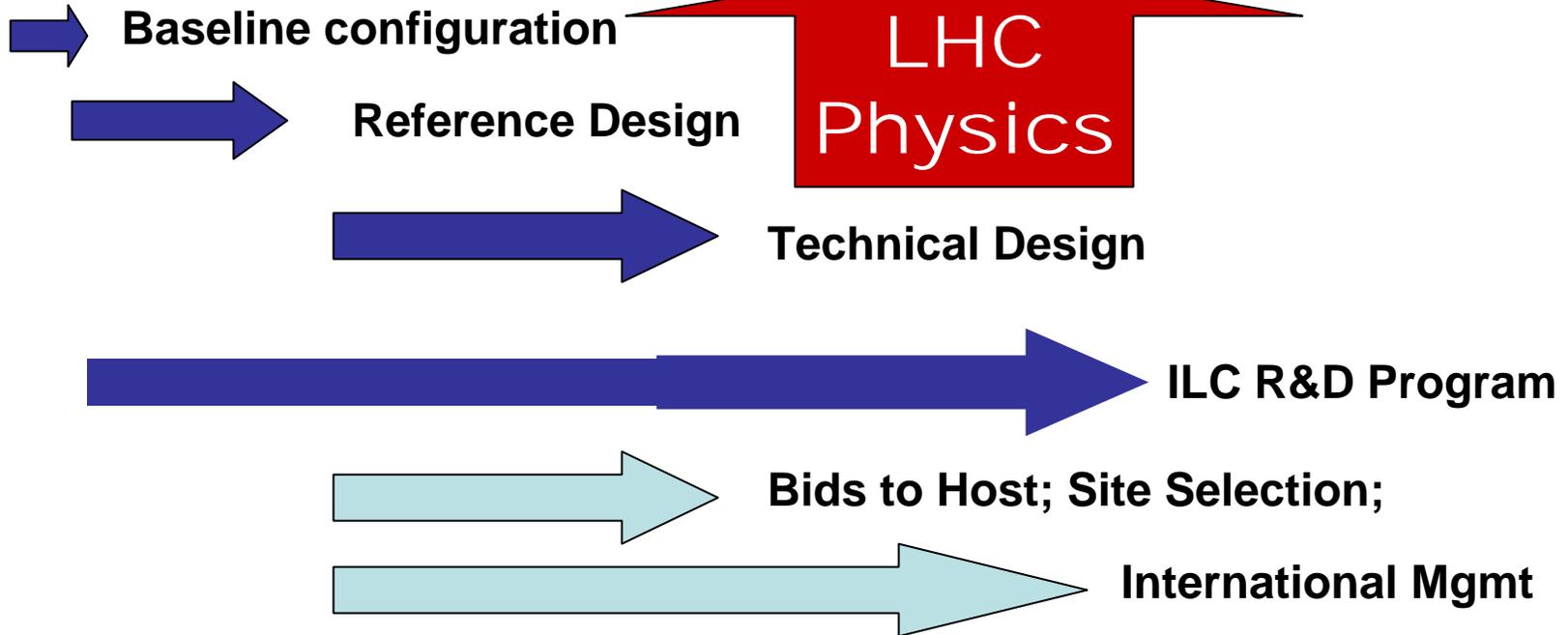
2006

2007

2008

2009

20010



Detector R&D Phases

- Present phase (-> FY05) - exploratory R&D, several/many approaches to each subsystem.
- Next phase (FY06-FY08?) - medium scale tests of selected technologies, data analysis, technology(s) comparison, selection
- Design and Prototype phase (FY08-FY09?) - design using selected technology, sector module prototype, testing, analysis, and iteration towards production design.

We need to understand the details of these phases for *each* of the SiD subsystems and the interactions between subsystems.

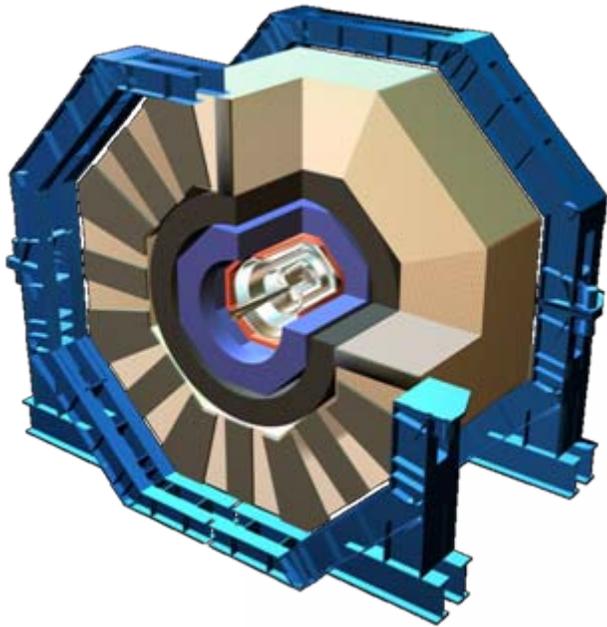
Note: this 5-year timescale has been discussed/agreed with the WWS R&D Chair.

Detector R&D Phases

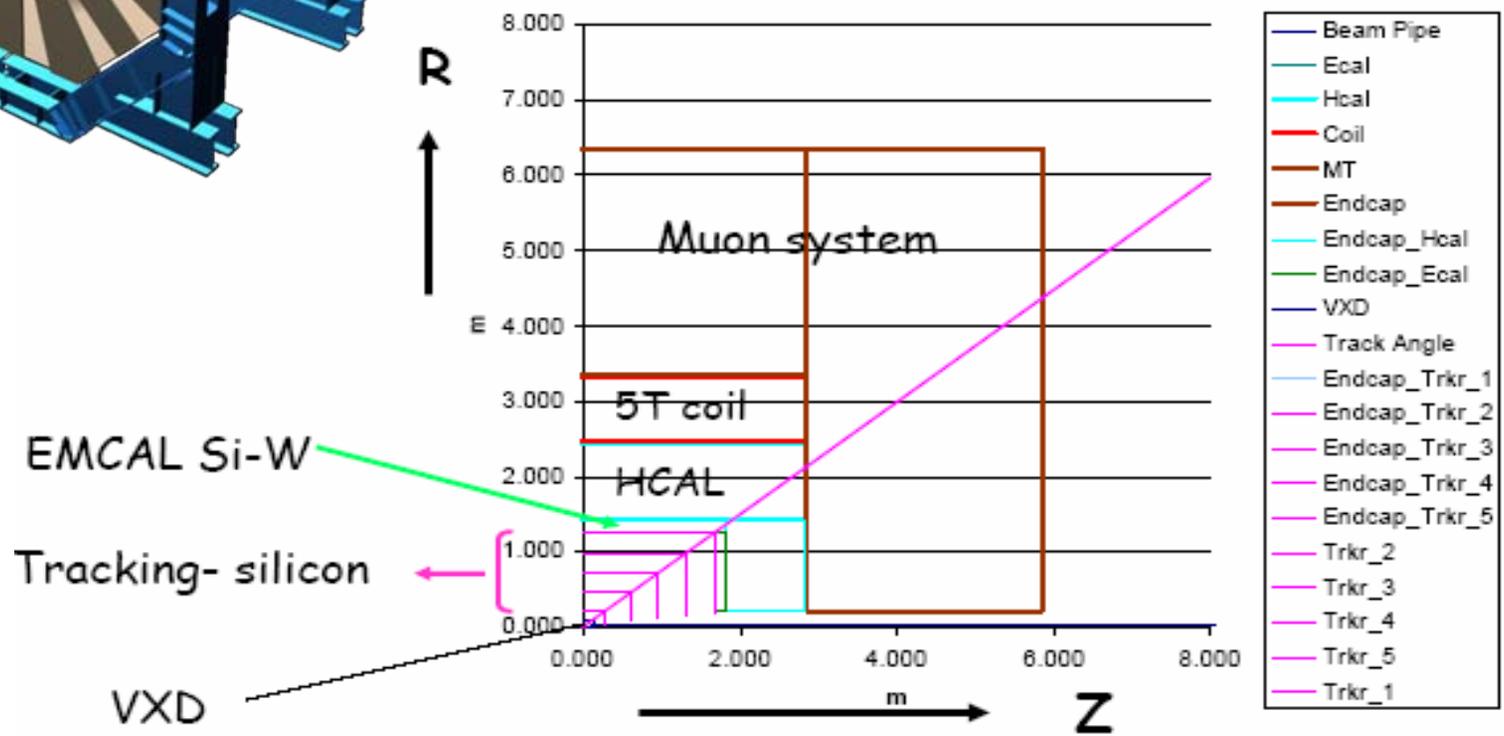
- Each of these R&D phases has associated costs.
- We need to understand the funding profiles for each SiD subsystem – must be as realistic as possible!
- We need to *prioritize* the many SiD R&D tasks for each phase.

Compact detector

SiD



Quadrant View



NOT A SMALL DETECTOR

SiD R&D Areas

Vertex Detector (Su Dong)

Main vertex detector technology: Monolithic CMOS, small pixels ($5\mu\text{m} \times 5\mu\text{m}$) – with larger pixel readout zones. Aiming for thin devices.

Tracking system (M. DeMarteau, R. Partridge)

Main technology(s): Monolithic pixels; Long and short ladder Si-strips; long shaping time/thin design.

Electromagnetic Calorimetry (R. Frey) Main technology: Silicon/Tungsten.

Hadronic Calorimetry (J. Repond)

Main technology(s): Digital GEM- and RPC-based with steel or tungsten.

Muon system and tail catcher (TBA) Main technology: RPC planes or Scintillator planes.

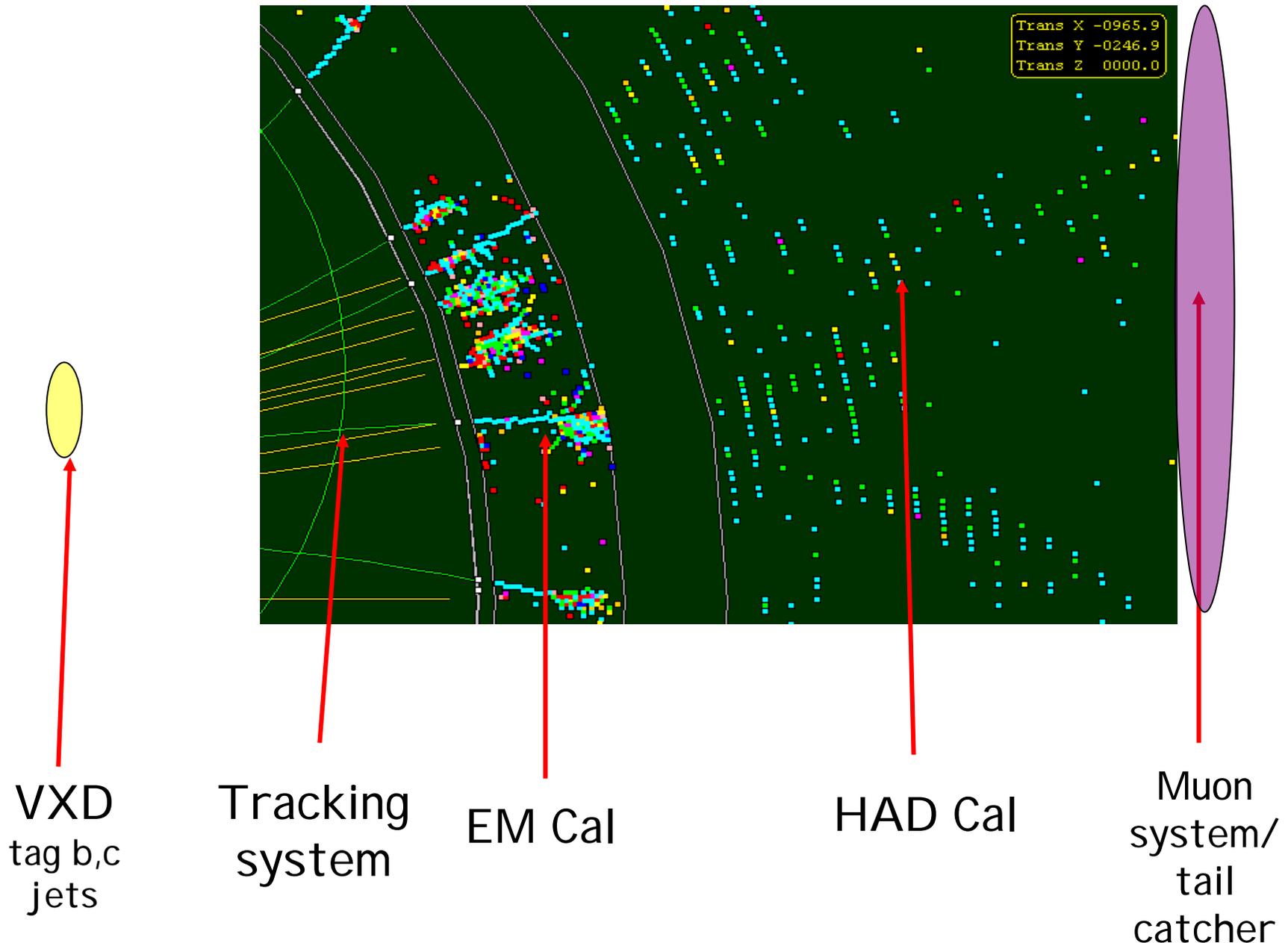
Electronics (M. Breidenbach)

Magnet (R. Smith)

Main technology: CMS-style superconductor

Machine Detector Interface (P. Burrows) Phil Burrows will identify those MDI items requiring R&D support.

Integrated Detector Design



Integrated Detector Design

We must consider the detector as a *whole* (even though we often meet as separate “subgroups”).

The tracker not only provides excellent momentum resolution (certainly good enough for replacing cluster energies in the calorimeter with track momenta), but *also* must:

- efficiently find all the charged tracks:

Any missed charged tracks will result in the corresponding energy clusters in the calorimeter being measured with lower energy resolution *and* a potentially larger confusion term.

Integrated Detector Design

- provide **excellent two track resolution** for correct track/energy cluster association
- > tracker outer radius/magnetic field size – implications for **e.m. shower separation/Moliere radius** in ECal.
- Different technologies for the ECal and HCal ??
 - do we lose by not having the same technology?
 - compensation – is the need for this completely overcome by using the energy flow approach?

Integrated Detector Design

- Services for Vertex Detector and Tracker should not cause **large penetrations, spaces, or dead material** within the calorimeter system – implications for inner systems design.
- Calorimeters should provide **excellent MIP identification for muon tracking** between the tracker and the muon system itself. High granularity digital calorimeters should naturally provide this – but what *is* the granularity requirement?
- We must be able to **find/track low energy (< 3.5 GeV) muons** completely contained inside the calorimeter.

Current SiD R&D Projects

Vertex Detector

U. Oregon and Yale, with collaboration/coordination with: University: U. Oklahoma, Labs: SLAC, KEK, FNAL, RAL

Tracking system

- a) Monolithic pixels: Fermilab
- b) Long shaping time thin Si-strips: UC Santa Cruz with collaborators from Fermilab, LPNHE Paris.
- c) Detector mounting frame/materials; the detector technology; detector ROC and cabling: SLAC, Fermilab
- d) Thin Si sensors: Purdue University.
- e) Simulation and alignment: University of Michigan.
- f) Reconstruction Studies: University of Colorado.
- g) Simulation Studies for Si tracker: Brown University.
- h) Calorimeter-based tracking for Particle Flow and Reconstruction of Long-Lived Particles: Kansas State University, collaborating with Fermilab, SLAC, University of Iowa, Northern Illinois University.

Current SiD R&D Projects

Forward tracking

Main technology: (Simulation study only): University of Oklahoma

Electromagnetic Calorimetry

Silicon/Tungsten. University of Oregon, SLAC, BNL, University of California at Davis.

Hadronic Calorimetry

a) GEM-based: University of Texas at Arlington, University of Washington, Changwon National University, Tsinghua University.

b) RPC-based: Argonne National Laboratory, Boston University, University of Chicago, Fermilab, University of Iowa.

c) Particle Flow Studies (simulation only): University of Iowa.

Muon system and tail catcher

RPC planes or Scintillator planes.

Electronics

SLAC

Current SiD R&D Projects

Magnet

CMS-style superconductor: Fermilab, (France – F. Richard advising).

Machine Detector Interface

Need to identify those MDI items requiring R&D support.

-> Need to review all these projects – understand role(s) in SiD.

-> What is missing?

-> What is redundant?

Estimating SiD R&D Funding needs

Experience tells us that the total R&D costs for major detectors (SLD, D0,....) are ~15-20% of the total detector cost.

We can take **two approaches** to R&D cost estimation:

- (1) Top-down – using an estimated percentage of final detector cost for each subsystem.
- (2) Bottoms-up – what we have so far, expect in short term, and can extrapolate.

Clearly we have to work towards convergence of these two approaches, in parallel with appropriate scheduling and prioritization of tasks.

“Top-down SiD Cost Estimate

SiD Subsystem	Cost	Estimated R&D %	R&D Cost	R&D	R&D Cost
				Contingency %	With Contingency
VXD	6.0	50	3.0	20	3.6
Tracker	19.9	15	3.0	20	3.6
EMCal	74.7	20	14.9	20	17.9
HCal	74.2	15	11.1	20	13.4
Muon system	52.1	10	5.2	20	6.3
Electronics	37.5	50	18.8	20	22.5
Magnet	167.1	10	16.7	20	20.1
MDI	20.0	10	2.0	20	2.4
TOTALS/AVG	451.5	22.5	74.7	20.0	89.7

cf. Total SiD cost ~\$641M

SiD R&D Report

SiD Detector Concept for the International Linear Collider Research and Development Report for WWS R&D Panel Introduction

The SiD design concept is continuously evolving. R&D on most detector subsystems has started, but generally with rather limited funding. For the purposes of providing information on the status and needs of R&D, this report therefore takes two approaches: (1) We give a "top-down" estimate (separate file) based on the overall best estimate of the final SiD detector cost, by subsystem, and experienced-based estimates of the R&D as a percentage of final cost. This also involves risk assessment for each subsystem in terms of a contingency for the R&D.

(2)(below) We give a detailed list of the present SiD R&D projects as far as is known and their present/anticipated funding. Clearly there is a large gap in the figures for each subsystem deriving from the two approaches. However, we take this information to be input to the WWS R&D Panel and future deliberations, starting at Snowmass 2005.

Vertex Detector (Su Dong)

Main vertex detector technology: Monolithic CMOS, small pixels ($5\mu\text{m} \times 5\mu\text{m}$) – with larger pixel readout zones. Aiming for thin devices.

Groups involved: Universities: U. Oregon and Yale, with collaboration/coordination with:

University: U. Oklahoma, Labs: SLAC, KEK, FNAL, RAL

R&D equipment/manpower: DoE/LCRD \$72K FY04

DoE/LCRD (awarded for FY05, requested for FY06, FY07):

FY05 \$ 64.5K

FY06 \$150Ketc.

SiD R&D Report

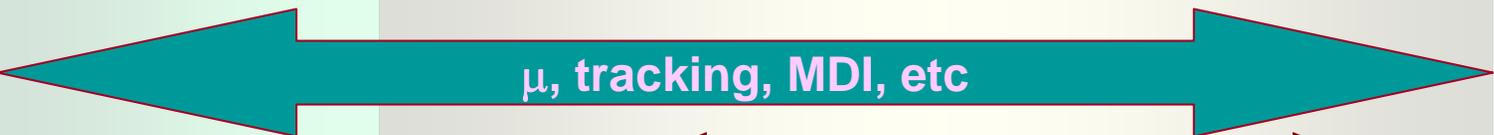
Goals for this meeting:

- Update/correct project descriptions, people, funding for specific current R&D efforts.
- Establish timeline, major steps, funding profiles with subsystem leaders – **need to schedule meetings with each subsystem leader(s)**
- Start process of convergence between top-down and bottoms-up estimates.

SiD R&D Snowmass

- Tuesday 8/16 Pre-meeting (closed) WWS panel + Detector Concept R&D Coordinators.
- Saturday 8/20 Detector Concepts meet WWS R&D Panel.
- Ongoing: discussions with individual SiD subgroups.
- Wednesday 8/24 SiD Technology Choices and R&D Plans.

Timeline of Beam Tests



Phase 0:
Prep.

Phase I: Detector R&D, PFA
development, Tech. Choice

Phase II
From Jae Yu

Timescales for LC Calorimeter and Muon development

We have maybe 3-5 years to build, test*, and understand, calorimeter and muon technologies for the Linear Collider.

By “understand” I mean that the cycle of testing, data analysis, re-testing etc. should have converged to the point at which we can reliably design calorimeter and muon systems from a secure knowledge base.

For the calorimeter, this means having trusted Monte Carlo simulations of technologies at unprecedented small distance scales (~1cm), well-understood energy cut-offs, and demonstrated, efficient, complete energy flow algorithms.

Since the first modules are only now being built, 3-5 years is not an over-estimate to accomplish these tasks!

* See talk by Jae Yu for Test Beam details