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[Select language] ▶GO

**Complete evaluation in Motorola format with word alignment**

Conditions	Address		Input data		Output data	
	Word offset		High byte	Low byte	High byte	Low byte
Complete evaluation: yes	0		reserved	SB1	reserved	CB0
Motorola format: yes	1		Ch1 D0	Ch1 D1	Ch1 D0	Ch1 D1
Word alignment: yes	2		reserved	SB2	reserved	CB1
	3		Ch2 D0	Ch2 D1	Ch2 D0	Ch2 D1

**Key**

Complete evaluation: In addition to the process data, the control and status bytes are also mapped in the address space.

Motorola format: Motorola or Intel format can be set.

Word alignment: In order for the channel address range to commence at a word boundary, empty bytes are inserted into the process image as appropriate.

SB n: status byte for channel n (appears in the input process image).

CB n: control byte for channel n (appears in the output process image).

Ch n D0: channel n, lower-value data byte

Ch n D1: channel n, higher-value data byte

reserved: This byte occupies process data memory, although it has no function.

"-": This byte is not assigned or used by the terminal/module.