

Agenda (Meeting 9/9/04)

J.W.

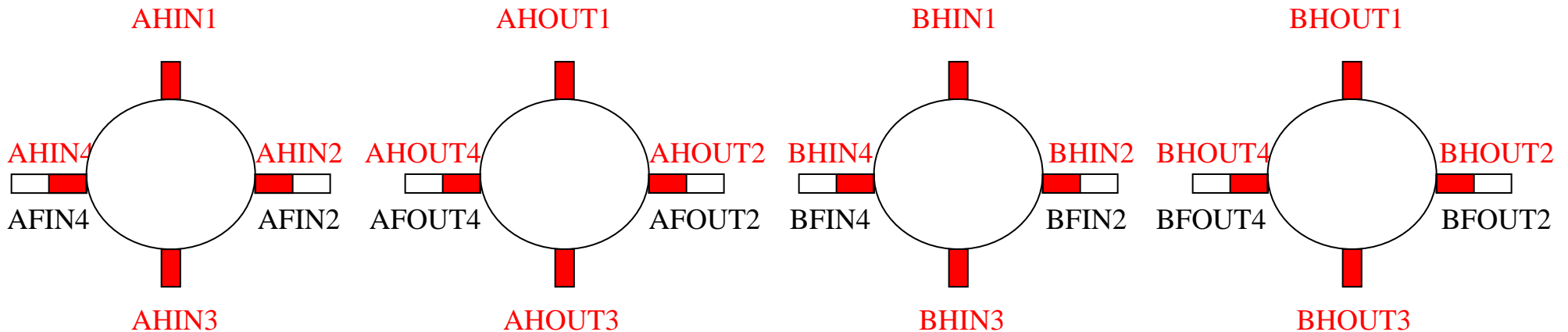
1. NLCTA (Chris or Steffen)
2. Fermi Lab (John)
3. Preparation for ASSET test of H60VG4SL17-A,B
 - Structures QC done.
 - H60VG4SL17-B is under baking and -A will follow.
 - All HOM loads in hand and will be checked.
 - Strongback in under fabrication.
 - Wakefield calculation is ongoing.
4. Design and Study Issues:
 - H60VG4SL17A/B QC and analysis (Jim and Juwen).

Instruction to Attachments for ASSET Test

H60VG4SL17-A

H60VG4SL17-B

Upstream



A -- Structure A
B -- Structure B

H – HOM port
F – Fundamental Mode Port

IN – Input End
OUT – Output End

View from Upstream End

Assignment for HOM Loads

AHIN1	AHIN2	AHIN3	AHIN4	AHOUT1	AHOUT2	AHOUT3	AHOUT4	BHIN1	BHIN2	BHIN3	BHIN4	BHOUT1	BHOUT2	BHOUT3	BHOUT4
040805	0004	040802	040803	0002	0001	0005	040804	101	0003	102	0006	103	040801	100	040806

Setting for Fundamental Ports

Each fundamental port is attached with a WR90 Male-Male adapter and a window assembly to have WR90 instrumentation flange for connection of instrumentation load. There is no special assignment for the component series number.