Put a 100 nF bypass capacitor between power pins of all ICs.

LEDs have a 10 mm diffused lens. The 121 ohm resistors limit LED current to about 15 mA.

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**Diagram Details:**

- **74LS27** is a triple 3-input NAND gate. 5 chips are required.
- **74LS30** is a single 4-input NAND gate. 1 chip is required.
- **74LS14** is an open collector 74LS14 driving

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**Example Text:**

- **U1A:** HIGH is normal, LOW is broken cable
- **U1B:** LOW is normal, HIGH is broken cable
- **U1C:** HIGH is normal, LOW is broken cable
- **U1D:** LOW is normal, HIGH is broken cable
- **U1E:** LOW is normal, HIGH is broken cable

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**Notes:**

- Put a wire jumper across "A" on the board if a flasher is used. It has a built-in current-limiting resistor.
- If replaced by a regular 10 mm LED, use a 121 ohm resistor.

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**References:**

- [Schematic diagram for DRC water leak logic card (Card #2)]
- [Checked by Svetlosp Blikov (svetlov@blikov) Date: Thursday, September 16, 2008]