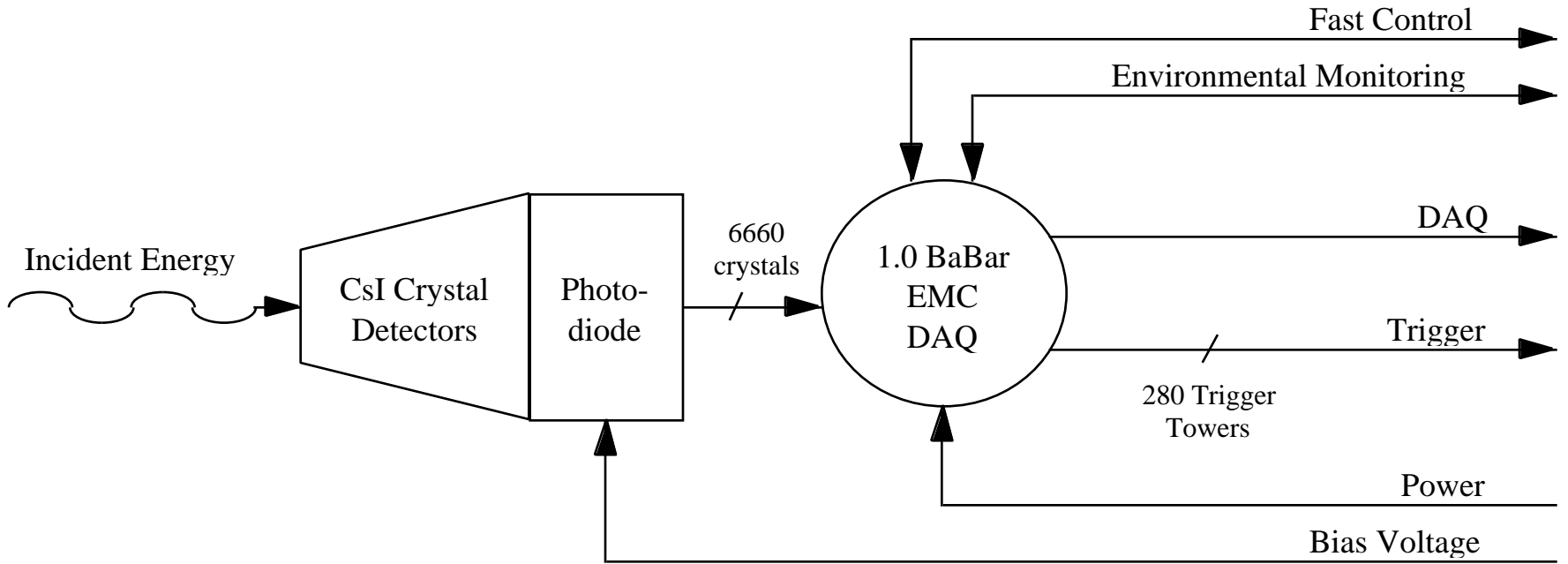
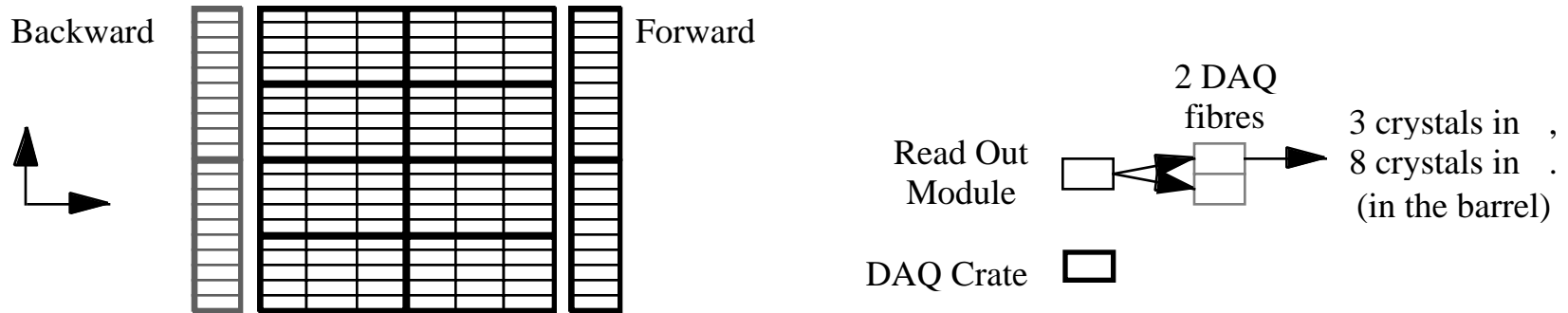


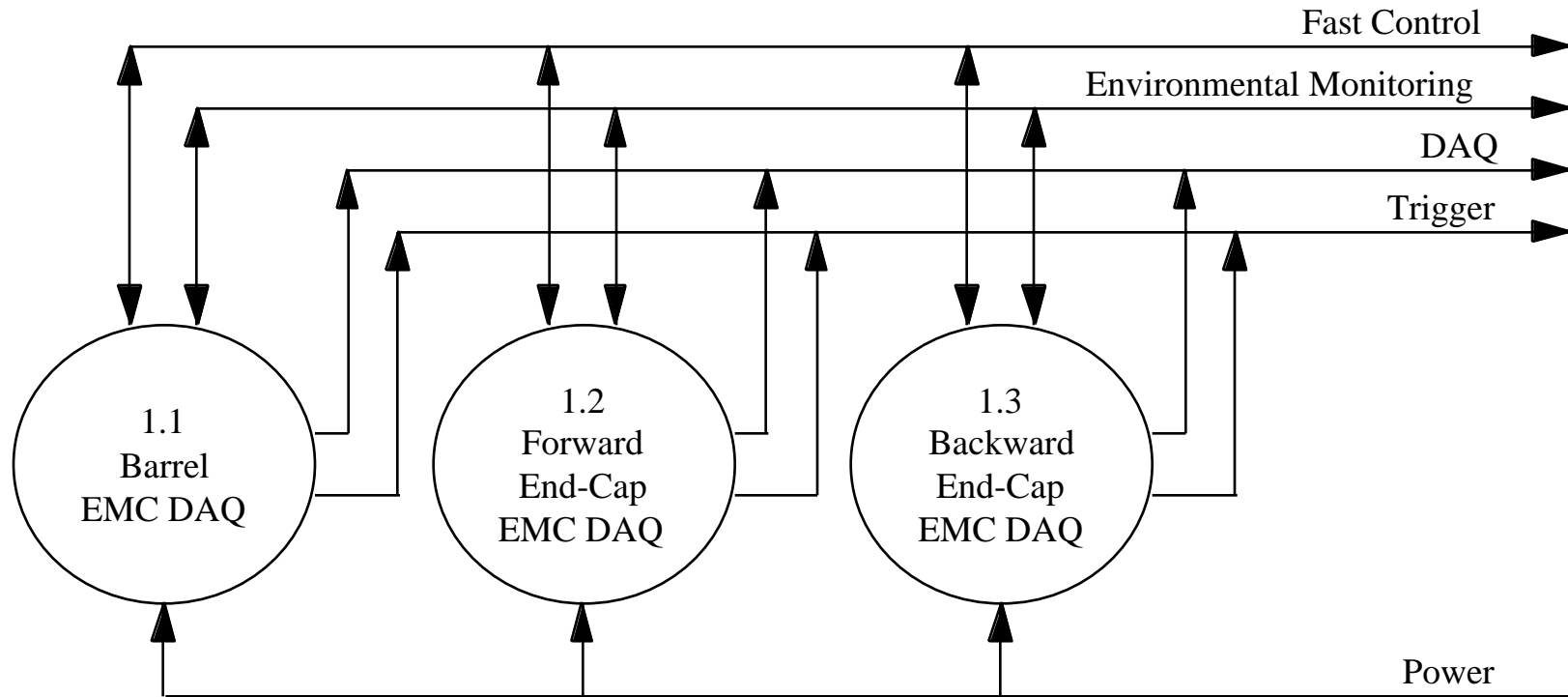
0.0 BaBar EMC DAQ Context



Flattened View of the EMC:



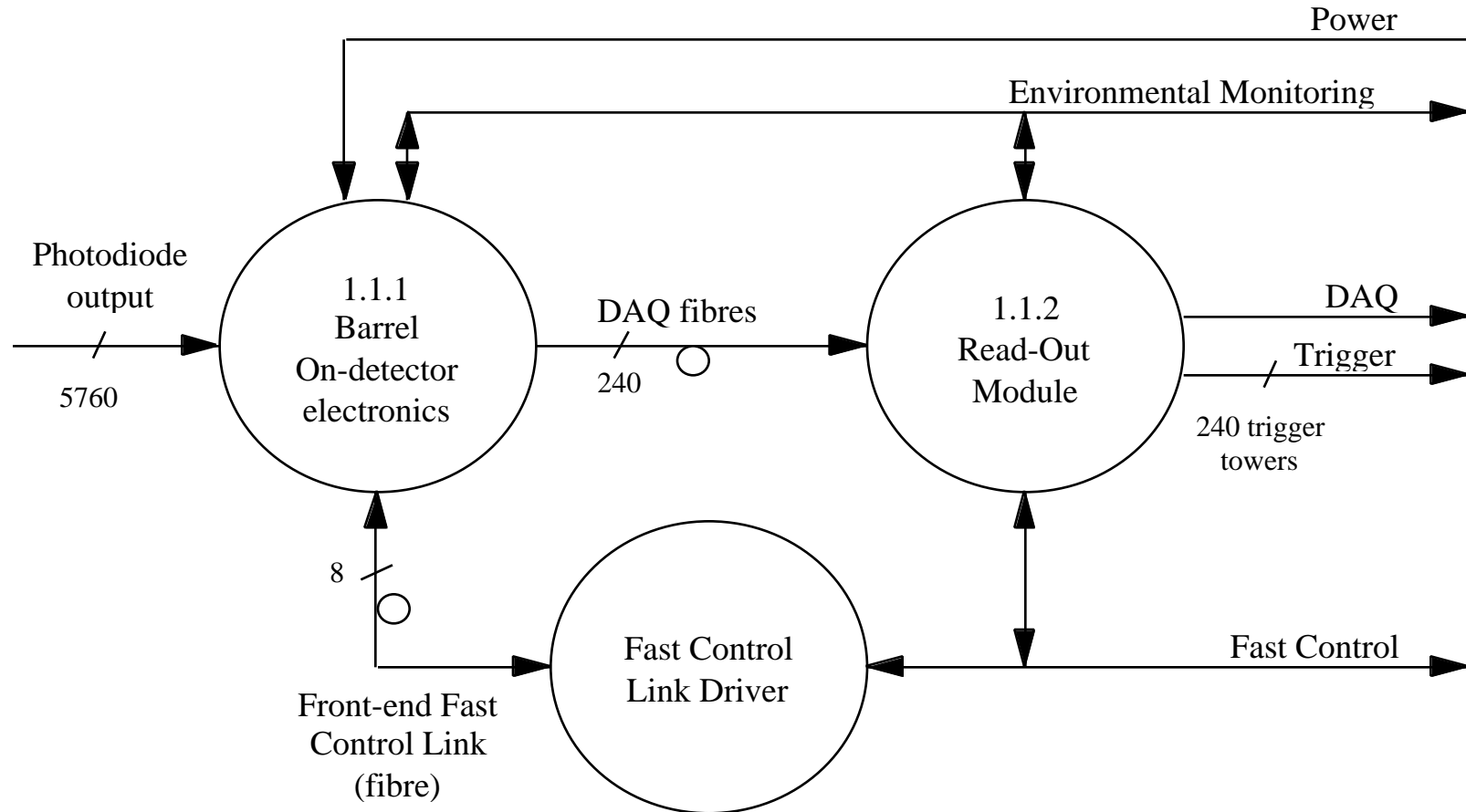
1.0 BaBar EMC DAQ



The backward end-cap has been staged. The electronics must, however, be designed to accommodate it at a later date.

1.3 will not be expanded further, it is believed to be identical to 1.2.

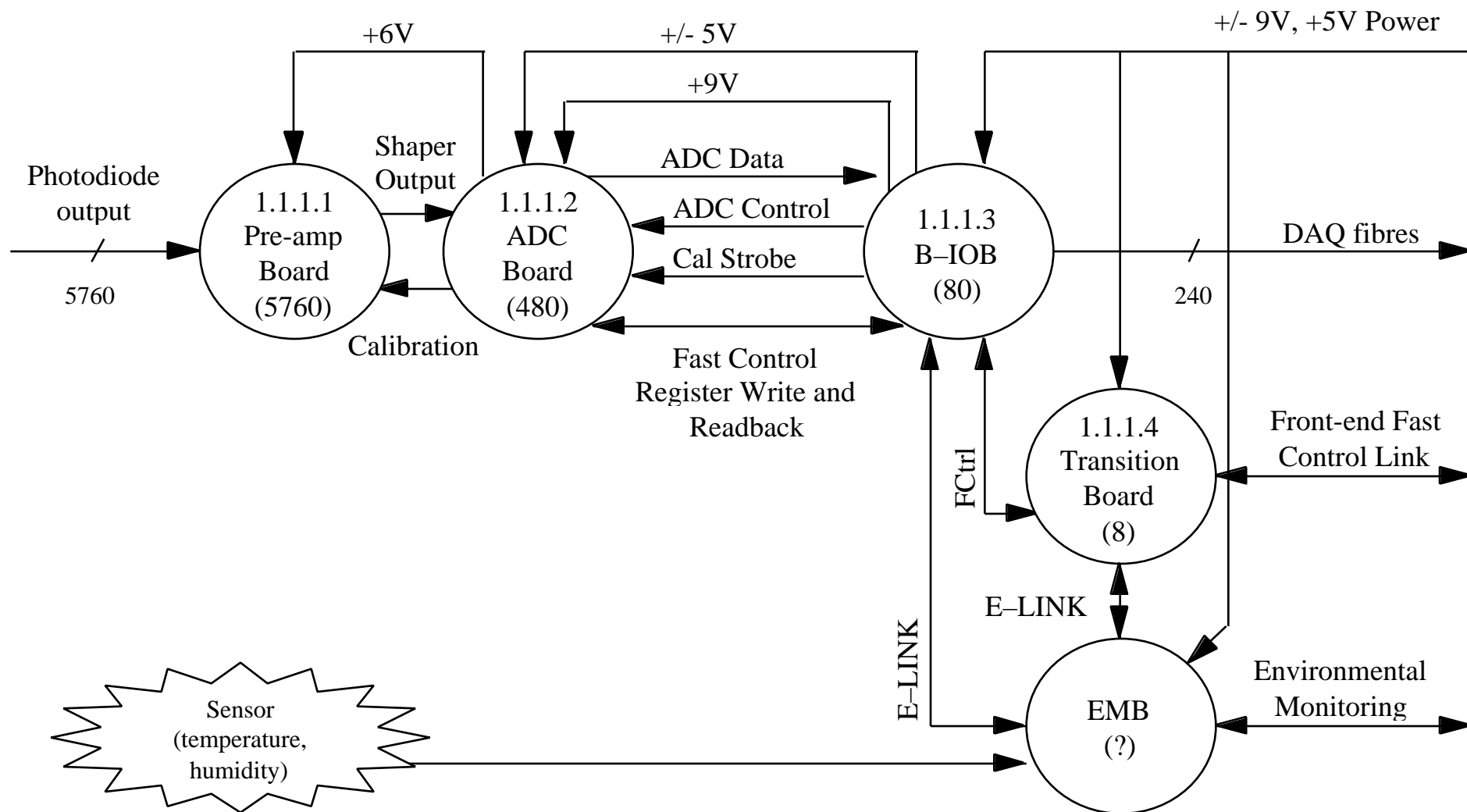
1.1 Barrel EMC DAQ



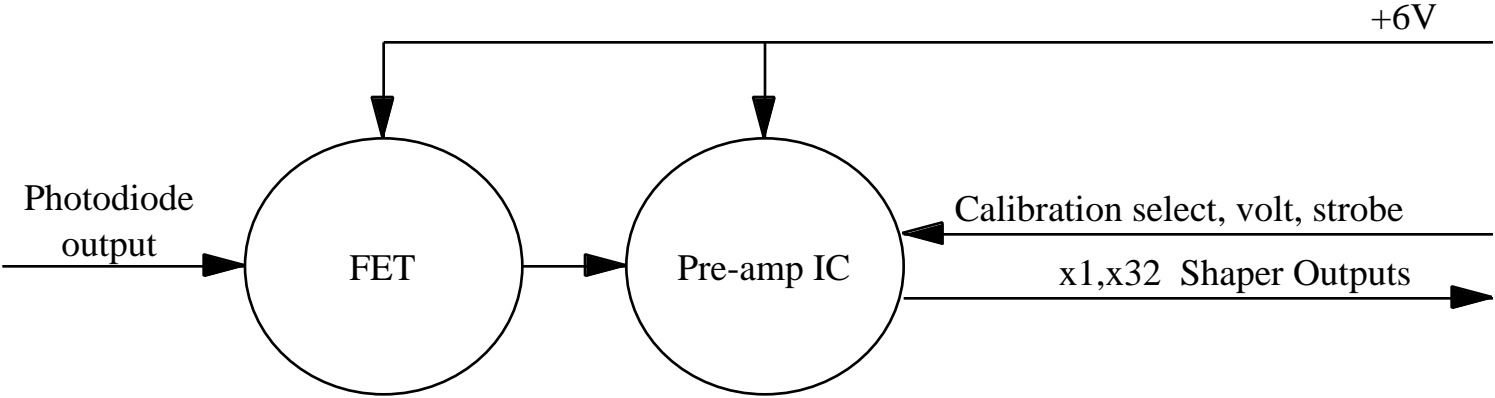
The Fast Control Link Driver is a DAQ board with the SVT/DIRC/IFR/ATC DAQ personality board.

Because the off-detector electronics is housed in standard VME crates the power connections are not shown explicitly.

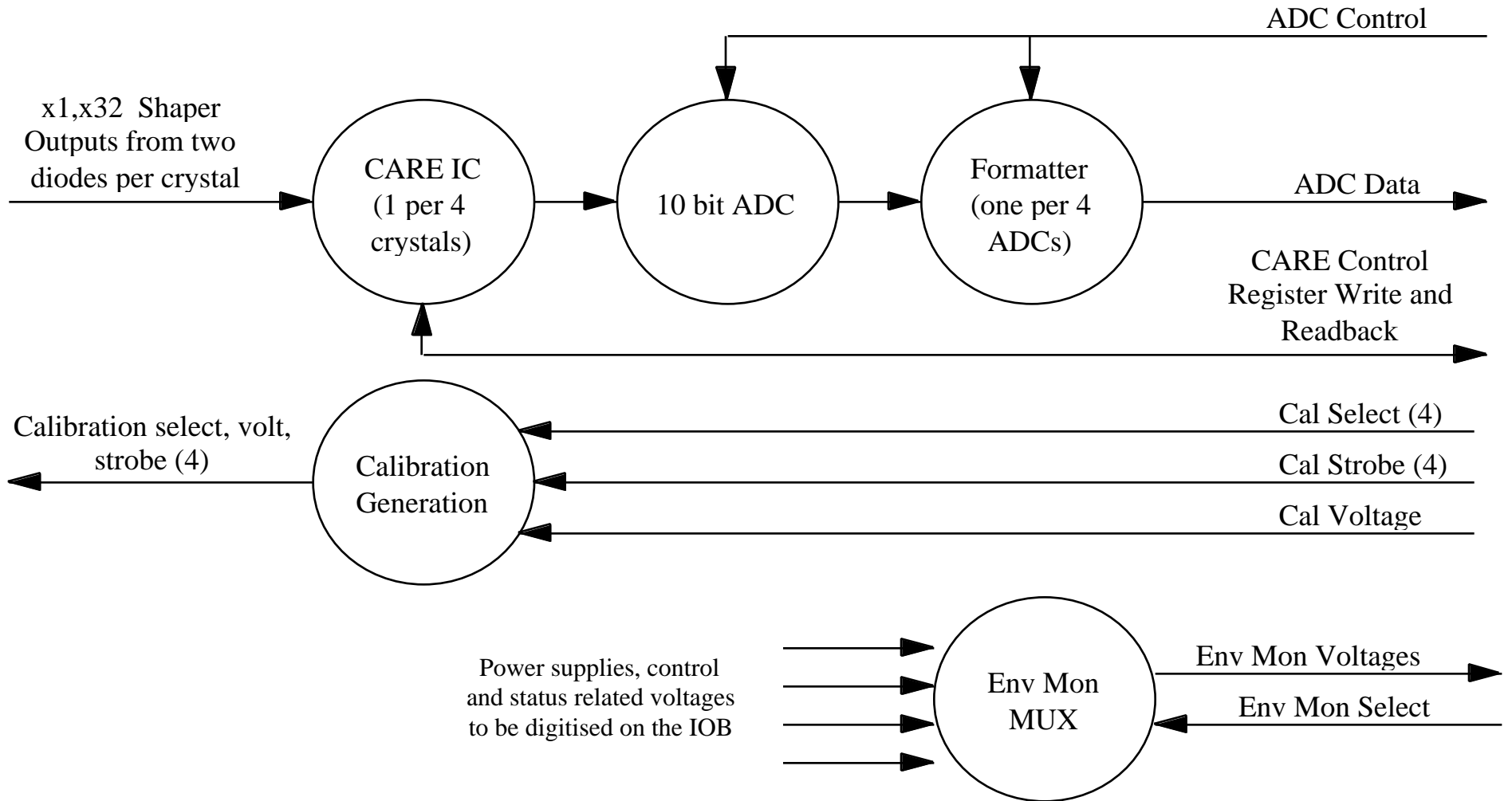
1.1.1 Barrel On-Detector Electronics



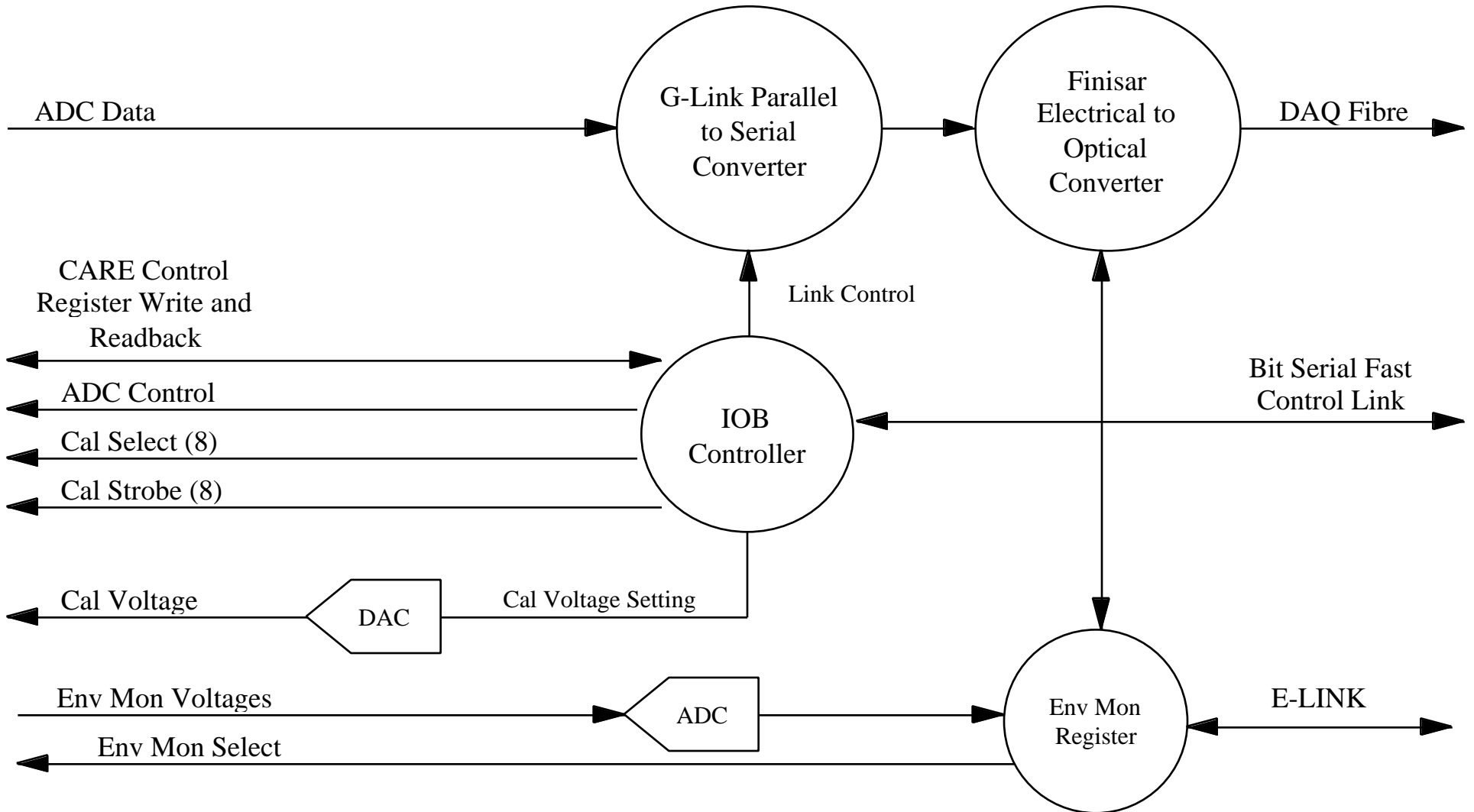
1.1.1.1 Pre-amplifier Board



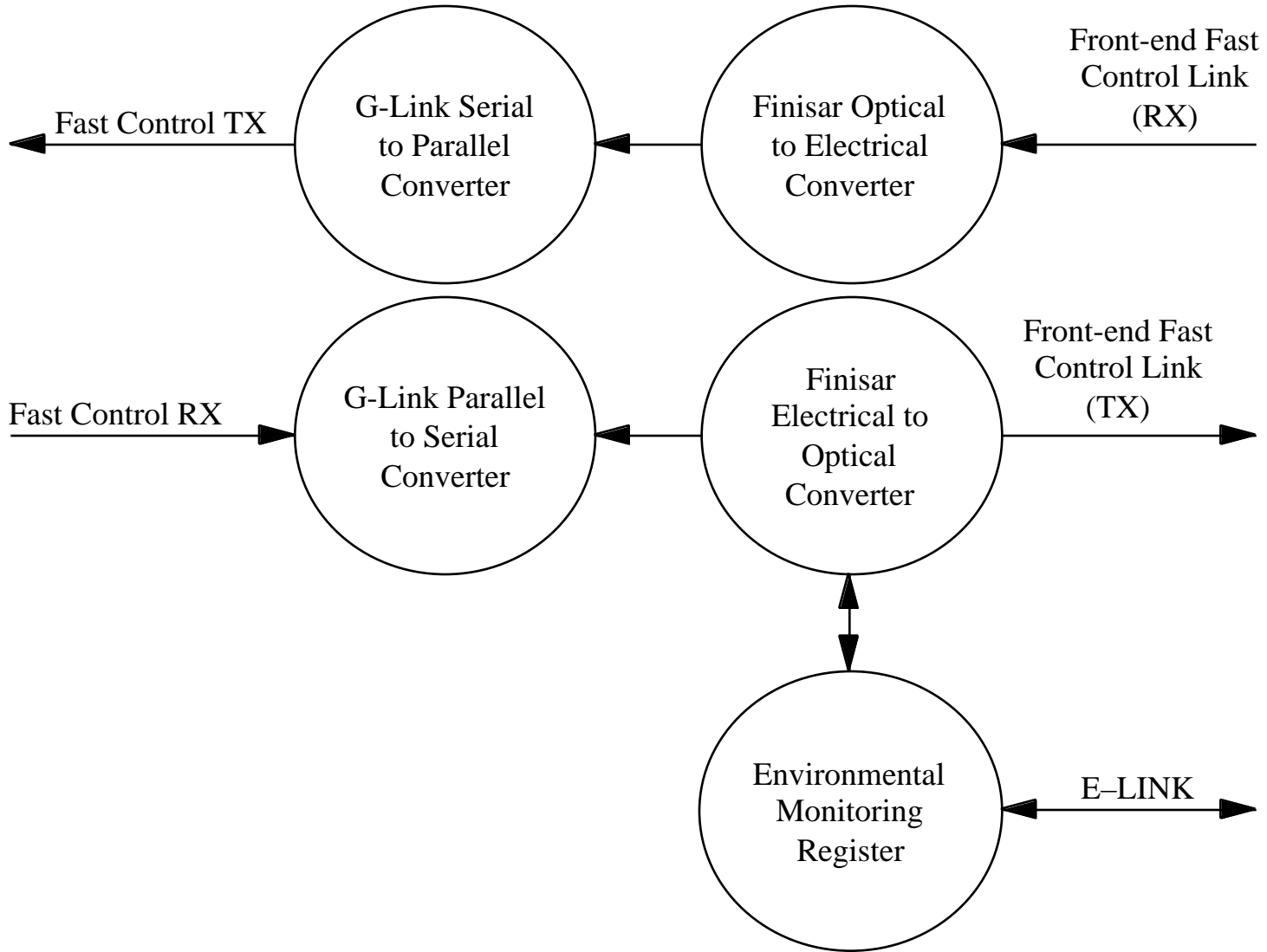
1.1.1.2 ADC Board



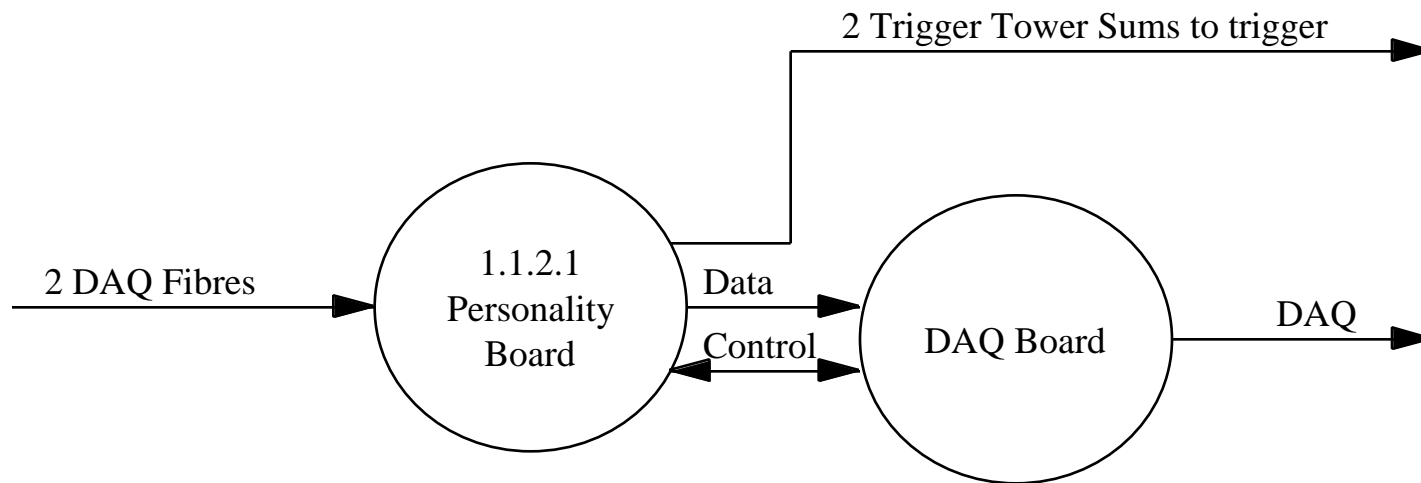
1.1.1.3 I/O Board



1.1.1.4 Transition Board

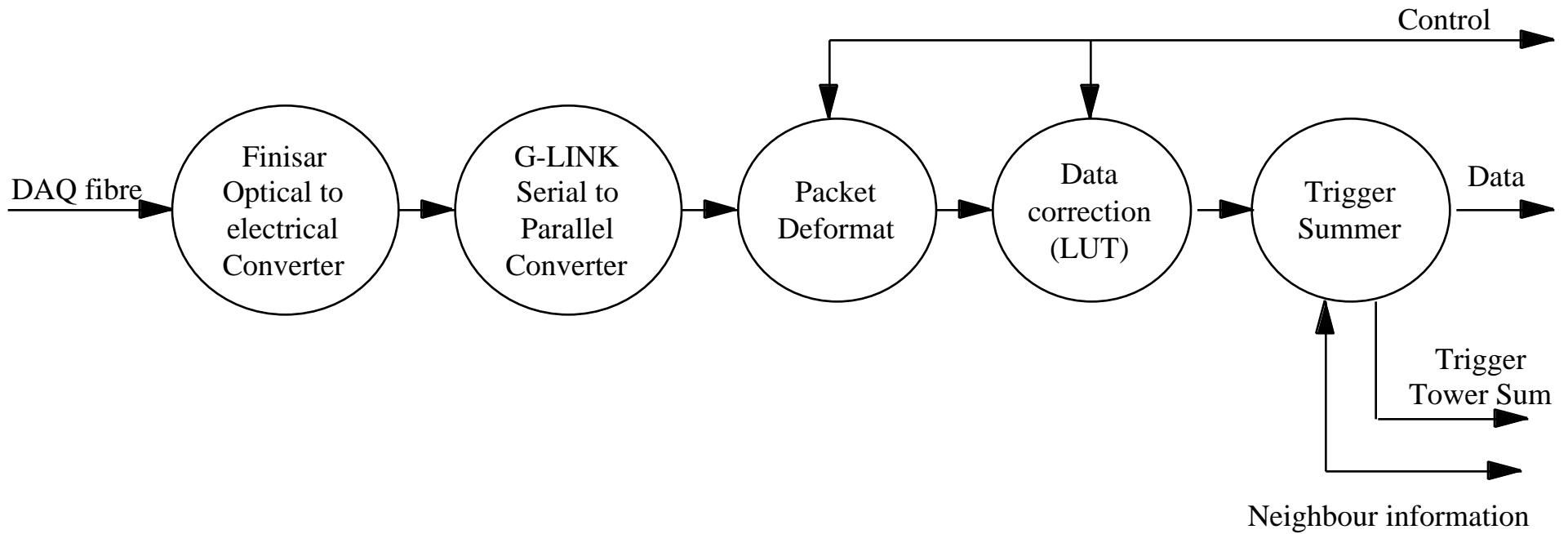


1.1.2 Read-Out Modules

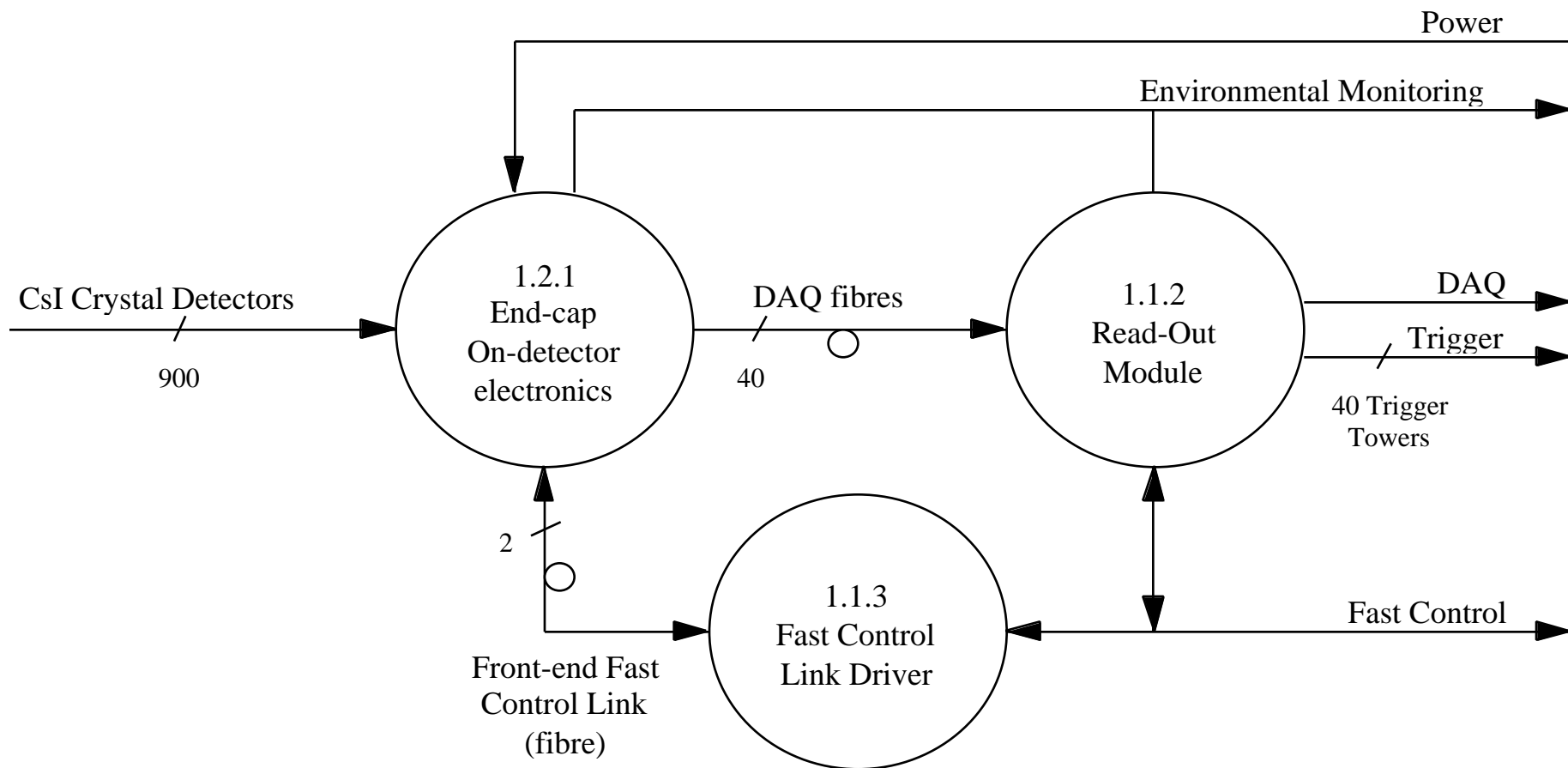


Sharing of neighbour information between DAQ boards is not shown.

1.1.2.1 EMC Personality Board



1.2 End-cap EMC DAQ



The Fast Control Link Driver is a DAQ board with the SVT/DIRC/IFR/ATC DAQ personality board.

Because the off-detector electronics is housed in standard VME crates the power connections are not shown explicitly.

1.2.1 End-cap On-Detector Electronics

