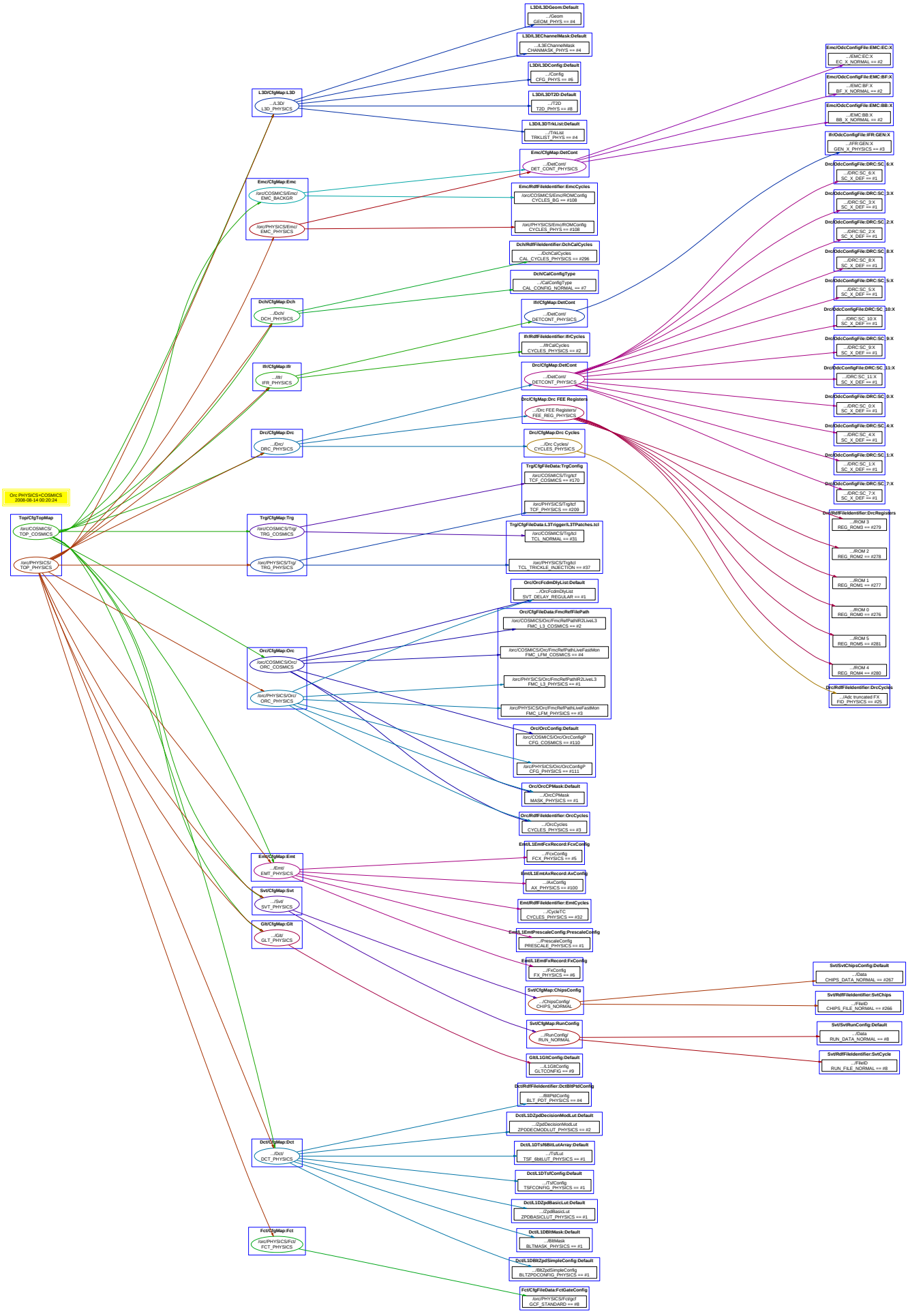
Orc PHYSICS+COSMICS
2008-08-14 00:20:24
Top/CfgTopMap
/orc/COSMICS/
TOP_COSMICS
/orc/PHYSICS/
TOP_PHYSICS
L3D/CfgMap:L3D
../L3D/
L3D_PHYSICS
Emc/CfgMap:Emc
/orc/COSMICS/Emc/
EMC_BACKGR
/orc/PHYSICS/Emc/
EMC_PHYSICS
Dch/CfgMap:Dch
../Dch/
DCH_PHYSICS
Ifr/CfgMap:Ifr
../Ifr/
IFR_PHYSICS
Drc/CfgMap:Drc
../Drc/
DRC_PHYSICS
Trg/CfgMap:Trg
/orc/COSMICS/Trg/
TRG_COSMICS
/orc/PHYSICS/Trg/
TRG_PHYSICS
Orc/CfgMap:Orc
/orc/COSMICS/Orc/
ORC_COSMICS
/orc/PHYSICS/Orc/
ORC_PHYSICS
Emt/CfgMap:Emt
../Emt/
EMT_PHYSICS
Svt/CfgMap:Svt
../Svt/
SVT_PHYSICS
Glt/CfgMap:Glt
../Glt/
GLT_PHYSICS
Dct/CfgMap:Dct
../Dct/
DCT_PHYSICS
Fct/CfgMap:Fct
/orc/PHYSICS/Fct/
FCT_PHYSICS
L3D/L3DGeom:Default
../Geom
GEOM_PHYS == #4
L3D/L3EChannelMask:Default
../L3EChannelMask
CHANMASK_PHYS == #4
L3D/L3DConfig:Default
../Config
CFG_PHYS == #6
L3D/L3D72D:Default
.../T2D
T2D_PHYS == #8
L3D/L3DTrkList:Default
../TrkList
TRKLIST_PHYS == #4
Emc/CfgMap:DetCont
../DetCont/
DET_CONT_PHYSICS
Emc/RdfFileIdentifier:EmcCycles
/orc/COSMICS/Emc/ROMConfig
CYCLES_BG == #108
/orc/PHYSICS/Emc/ROMConfig
CYCLES_PHYS == #108
Dch/RdfFileIdentifier:DchCalCycles
../DchCalCycles
CAL_CYCLES_PHYSICS == #296
Dch/CalConfigType
../CalConfigType
CAL_CONFIG_NORMAL == #7
Ifr/CfgMap:DetCont
../DetCont/
DETCONT_PHYSICS
Ifr/RdfFileIdentifier:IfrCycles
.../IfrCalCycles
CYCLES_PHYSICS == #2
Drc/CfgMap:DetCont
../DetCont/
DETCONT_PHYSICS
Drc/CfgMap:Drc FEE Registers
.../Drc FEE Registers/
FEE_REG_PHYSICS
Drc/CfgMap:Drc Cycles
../Drc Cycles/
CYCLES_PHYSICS
Trg/CfgFileData:TrgConfig
/orc/COSMICS/Trg/tcf
TCF_COSMICS == #170
/orc/PHYSICS/Trg/tcf
TCF_PHYSICS == #209
Trg/CfgFileData:L3Trigger/L3TPatches.tcl
/orc/COSMICS/Trg/tcl
TCL_NORMAL == #31
/orc/PHYSICS/Trg/tcl
TCL_TRICKLE_INJECTION == #37
Orc/OrcFcdmDlyList:Default
../OrcFcdmDlyList
SVT_DELAY_REGULAR == #1
Orc/CfgFileData:FmcRefFilePath
/orc/COSMICS/Orc/FmcRefPathIR2LiveL3
FMC_L3_COSMICS == #2
/orc/COSMICS/Orc/FmcRefPathLiveFastMon
FMC_LFM_COSMICS == #4
/orc/PHYSICS/Orc/FmcRefPathIR2LiveL3
FMC_L3_PHYSICS == #1
/orc/PHYSICS/Orc/FmcRefPathLiveFastMon
FMC_LFM_PHYSICS == #3
Orc/OrcConfig:Default
/orc/COSMICS/Orc/OrcConfigP
CFG_COSMICS == #110
/orc/PHYSICS/Orc/OrcConfigP
CFG_PHYSICS == #111
Orc/OrcCPMask:Default
.../OrcCPMask
MASK_PHYSICS == #1
Orc/RdfFileIdentifier:OrcCycles
.../OrcCycles
CYCLES_PHYSICS == #3
Emt/L1EmtFcxRecord:FcxConfig
../FcxConfig
FCX_PHYSICS == #5
Emt/L1EmtAxRecord:AxConfig
../AxConfig
AX_PHYSICS == #100
Emt/RdfFileIdentifier:EmtCycles
../CycleTC
CYCLES_PHYSICS == #32
Emt/L1EmtPrescaleConfig:PrescaleConfig
.../PrescaleConfig
PRESCALE_PHYSICS == #1
Emt/L1EmtFxRecord:FxConfig
../FxConfig
FX_PHYSICS == #6
Svt/CfgMap:ChipsConfig
../ChipsConfig/
CHIPS_NORMAL
Svt/CfgMap:RunConfig
../RunConfig/
RUN_NORMAL
Glt/L1GltConfig:Default
.../L1GltConfig
GLTCONFIG == #9
Dct/RdfFileIdentifier:DctBltPtdConfig
../BltPtdConfig
BLT_PDT_PHYSICS == #4
Dct/L1DZpdDecisionModLut:Default
.../ZpdDecisionModLut
ZPDDECMODLUT_PHYSICS == #2
Dct/L1DTsf6BitLutArray:Default
../TsfLut
TSF_6bitLUT_PHYSICS == #1
Dct/L1DTsfConfig:Default
../TsfConfig
TSFCONFIG_PHYSICS == #1
Dct/L1D2zpdBasicLut:Default
.../ZpdBasicLut
ZPDBASICLUT_PHYSICS == #1
Dct/L1DBltMask:Default
.../BltMask
BLTMASK_PHYSICS == #1
Dct/L1DBlt2pdSimpleConfig:Default
../Blt2pdSimpleConfig
BLT2PDCONFIG_PHYSICS == #1
Fct/CfgFileData:FctGateConfig
/orc/PHYSICS/Fct/gcf
GCF_STANDARD == #8
Emc/OdcConfigFile:EMC:EC:X
.../EMC:EC:X
EC_X_NORMAL == #2
Emc/OdcConfigFile:EMC:BF:X
.../EMC:BF:X
BF_X_NORMAL == #2
Emc/OdcConfigFile:EMC:BB:X
.../EMC:BB:X
BB_X_NORMAL == #2
Ifr/OdcConfigFile:IFR:GEN:X
.../IFR:GEN:X
GEN_X_PHYSICS == #3
Drc/OdcConfigFile:DRC:SC_6:X
.../DRC:SC_6:X
SC_X_DEF == #1
Drc/OdcConfigFile:DRC:SC_3:X
.../DRC:SC_3:X
SC_X_DEF == #1
Drc/OdcConfigFile:DRC:SC_2:X
.../DRC:SC_2:X
SC_X_DEF == #1
Drc/OdcConfigFile:DRC:SC_8:X
.../DRC:SC_8:X
SC_X_DEF == #1
Drc/OdcConfigFile:DRC:SC_5:X
.../DRC:SC_5:X
SC_X_DEF == #1
Drc/OdcConfigFile:DRC:SC_10:X
.../DRC:SC_10:X
SC_X_DEF == #1
Drc/OdcConfigFile:DRC:SC_9:X
.../DRC:SC_9:X
SC_X_DEF == #1
Drc/OdcConfigFile:DRC:SC_11:X
.../DRC:SC_11:X
SC_X_DEF == #1
Drc/OdcConfigFile:DRC:SC_0:X
.../DRC:SC_0:X
SC_X_DEF == #1
Drc/OdcConfigFile:DRC:SC_4:X
.../DRC:SC_4:X
SC_X_DEF == #1
Drc/OdcConfigFile:DRC:SC_1:X
.../DRC:SC_1:X
SC_X_DEF == #1
Drc/OdcConfigFile:DRC:SC_7:X
.../DRC:SC_7:X
SC_X_DEF == #1
Drc/RdfFileIdentifier:DrcRegisters
.../ROM 3
REG_ROM3 == #279
.../ROM 2
REG_ROM2 == #278
.../ROM 1
REG_ROM1 == #277
.../ROM 0
REG_ROM0 == #276
.../ROM 5
REG_ROM5 == #281
.../ROM 4
REG_ROM4 == #280
Drc/RdfFileIdentifier:DrcCycles
../Adc truncated FX
FID_PHYSICS == #25
Svt/SvtChipsConfig:Default
.../Data
CHIPS_DATA_NORMAL == #267
Svt/RdfFileIdentifier:SvtChips
.../FileID
CHIPS_FILE_NORMAL == #266
Svt/SvtRunConfig:Default
.../Data
RUN_DATA_NORMAL == #8
Svt/RdfFileIdentifier:SvtCycle
.../FileID
RUN_FILE_NORMAL == #8